SPEC® CINT2006 Result

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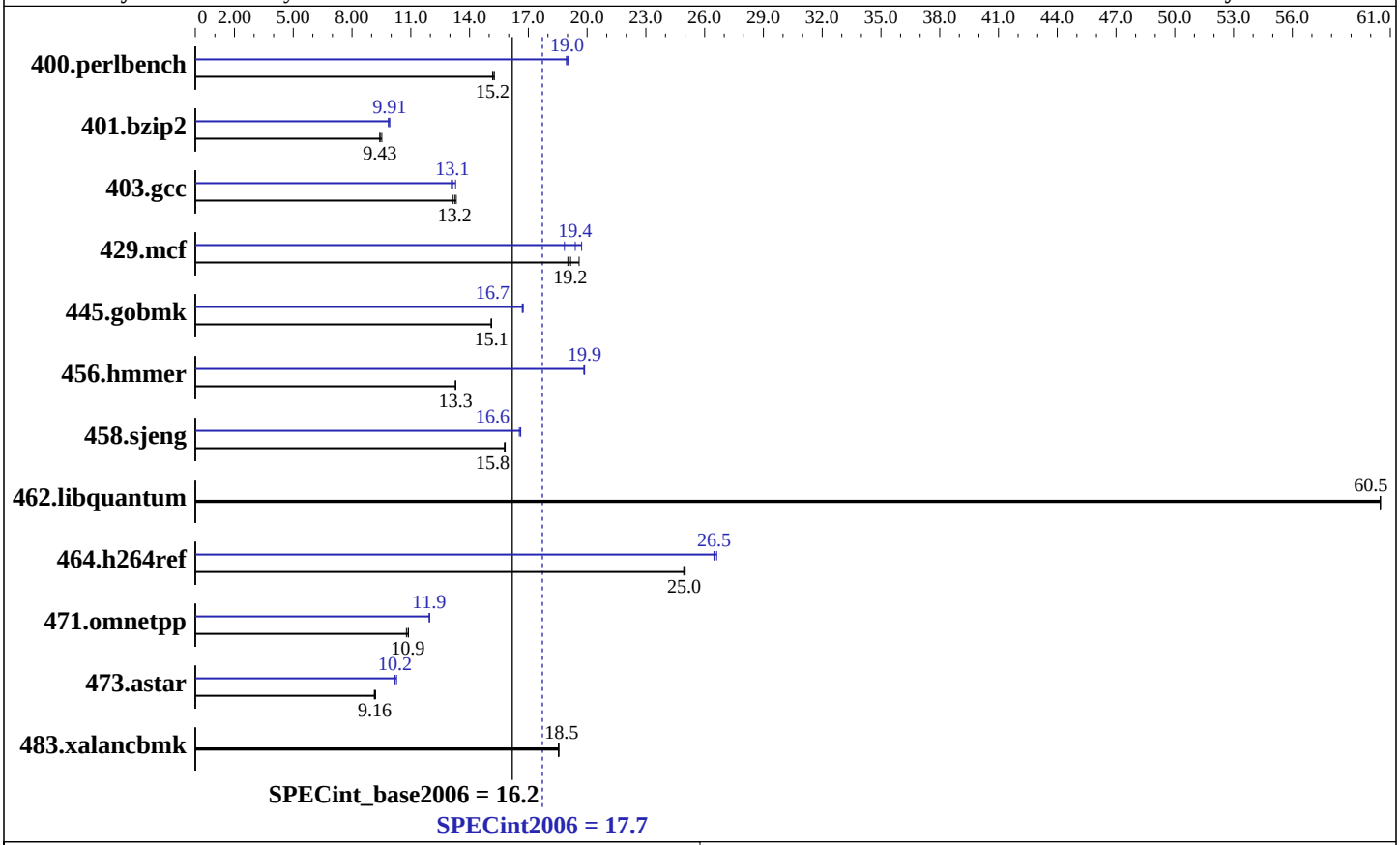
NTT System S. A.
NTT Business W 907G

SPECint®2006 = 17.7

SPECint_base2006 = 16.2

CPU2006 license: 9013
Test sponsor: NTT System S. A.
Tested by: NTT System S. A.

Test date: Jul-2009
Hardware Availability: Jul-2009
Software Availability: Nov-2009



Hardware

CPU Name:	Intel Celeron E3200
CPU Characteristics:	
CPU MHz:	2400
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	1 MB I+D on chip per chip
L3 Cache:	None
Other Cache:	None
Memory:	4 GB (2x2GB) DDR2 800Mhz
Disk Subsystem:	250 GB SATA, 7200RPM
Other Hardware:	None

Software

Operating System:	SuSe Linux SLES10 SP2, Kernel 2.6.16.60-0.21-smp
Compiler:	Intel C++ Compiler 11.0 for Linux Build 20080930 Package ID: l_cproc_p_11.0.066
Auto Parallel:	Yes
File System:	ReiserFS
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502

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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	**643**	**15.2**	640	15.3	644	15.2	516	18.9	513	19.0	**515**	**19.0**
401.bzip2	**1023**	**9.43**	1025	9.42	1014	9.52	979	9.85	**974**	**9.91**	973	9.92
403.gcc	**608**	**13.2**	604	13.3	613	13.1	616	13.1	**613**	**13.1**	606	13.3
429.mcf	480	19.0	**476**	**19.2**	465	19.6	**470**	**19.4**	462	19.7	484	18.8
445.gobmk	**694**	**15.1**	694	15.1	695	15.1	**628**	**16.7**	628	16.7	627	16.7
456.hmmer	703	13.3	**702**	**13.3**	702	13.3	470	19.9	**470**	**19.9**	470	19.8
458.sjeng	765	15.8	767	15.8	**765**	**15.8**	728	16.6	732	16.5	**729**	**16.6**
462.libquantum	343	60.5	343	60.5	**343**	**60.5**	343	60.5	343	60.5	**343**	**60.5**
464.h264ref	888	24.9	885	25.0	**887**	**25.0**	836	26.5	832	26.6	**835**	**26.5**
471.omnetpp	574	10.9	**575**	**10.9**	580	10.8	**523**	**11.9**	523	11.9	523	12.0
473.astar	768	9.14	763	9.20	**767**	**9.16**	689	10.2	683	10.3	**688**	**10.2**
483.xalancbmk	**372**	**18.5**	372	18.5	372	18.6	**372**	**18.5**	372	18.5	372	18.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
OMP_NUM_THREADS set to number of processors
KMP_AFFINITY set to "physical,0"
```

Base Compiler Invocation

C benchmarks:
`icc`

C++ benchmarks:
`icpc`

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:
`-xSSSE3 -ipo -O3 -no-prec-div -static -parallel -par-runtime-control -opt-prefetch`

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Base Optimization Flags (Continued)

C++ benchmarks:
`-xSSSE3 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap`

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
`icc`

401.bzip2: `/opt/intel/Compiler/11.0/066/bin/intel64/icc`

456.hmmer: `/opt/intel/Compiler/11.0/066/bin/intel64/icc`

C++ benchmarks:
`icpc`

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
401.bzip2: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xSSSE3 -ipo -O3 -no-prec-div -static -ansi-alias -opt-prefetch`

401.bzip2: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xSSSE3 -ipo -O3 -no-prec-div -static -auto-ilp32 -opt-prefetch -ansi-alias`

Continued on next page

Peak Optimization Flags (Continued)

403.gcc: `-xSSSE3 -ipo -O3 -no-prec-div -static -inline-calloc -opt-malloc-options=3`

429.mcf: `-xSSSE3 -ipo -O3 -no-prec-div -static -opt-prefetch`

445.gobmk: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xSSSE3 -O2 -ipo -no-prec-div -ansi-alias`

456.hmmer: `-xSSSE3 -ipo -O3 -no-prec-div -static -unroll2 -ansi-alias -auto-ilp32`

458.sjeng: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xSSSE3 -ipo -O3 -no-prec-div -static -unroll4`

462.libquantum: `basepeak = yes`

464.h264ref: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xSSSE3 -ipo -O3 -no-prec-div -static -unroll2 -ansi-alias`

C++ benchmarks:

471.omnetpp: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xSSSE3 -ipo -O3 -no-prec-div -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap`

473.astar: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xSSSE3 -ipo -O3 -no-prec-div -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap`

483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revF.html`
`http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090710.00.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revF.xml`
`http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090710.00.xml`

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