



SPEC® CFP2006 Result

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NTT System S. A.
NTT Business W 907G

SPECfp®_rate2006 = 24.0
SPECfp_rate_base2006 = 23.3

CPU2006 license: 9013

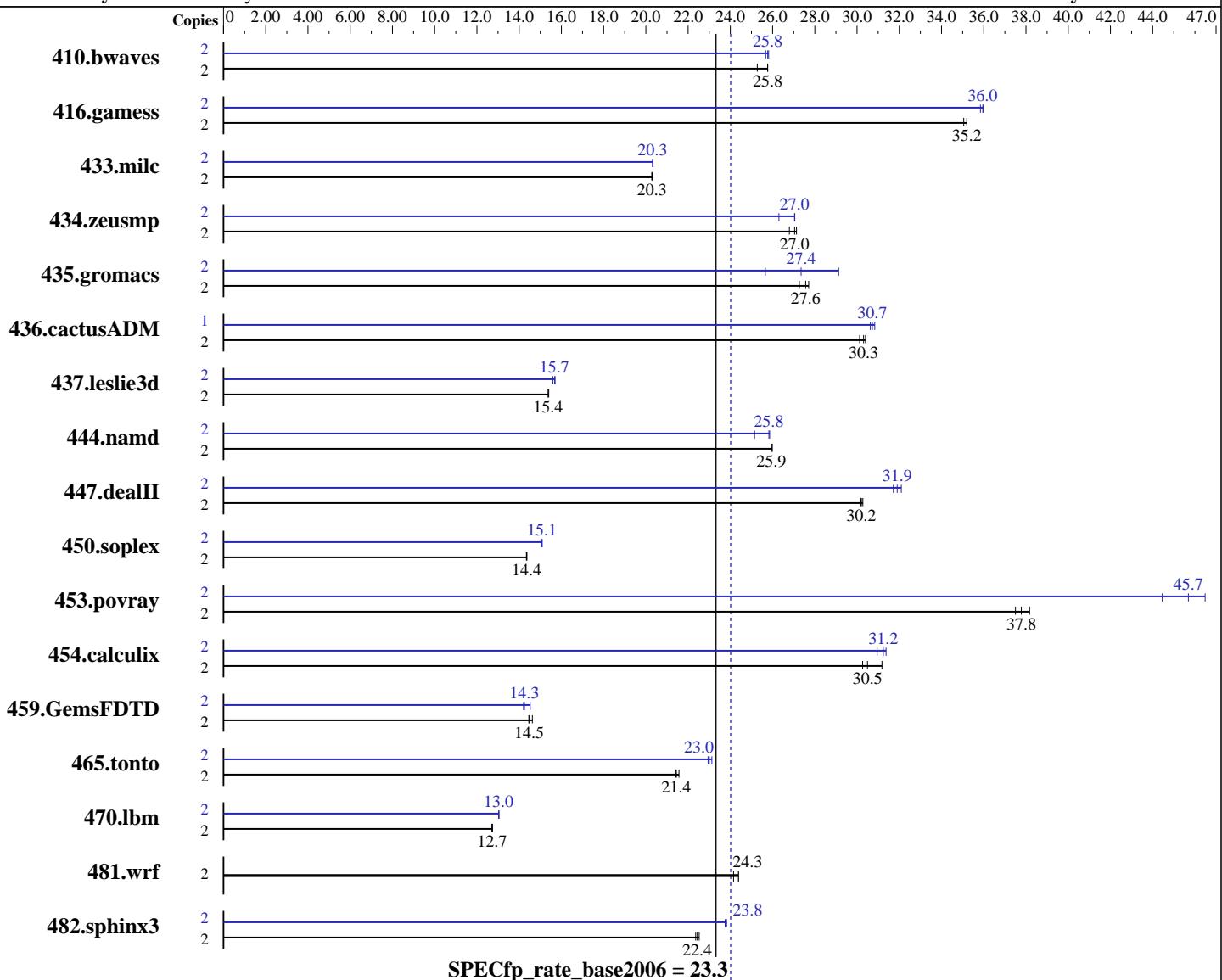
Test sponsor: NTT System S. A.

Tested by: NTT System S. A.

Test date: Jul-2009

Hardware Availability: Jul-2009

Software Availability: Nov-2008



Hardware

CPU Name: Intel Celeron E3200
CPU Characteristics:
CPU MHz: 2400
FPU: Integrated
CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
CPU(s) orderable: 1 chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 1 MB I+D on chip per chip

Software

Operating System: SuSe Linux SLES10 SP2, Kernel 2.6.16.60-0.21-smp
Compiler: Intel C++ and Fortran Compiler 11.0 for Linux Build 20080930 Package ID: l_cproc_p_11.0.066, l_cprof_p_11.0.066
Auto Parallel: Yes
File System: ReiserFS
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 32/64-bit

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L3 Cache: None
 Other Cache: None
 Memory: 2 GB (2x1GB) DDR2 800Mhz
 Disk Subsystem: 250 GB SATA, 7200RPM
 Other Hardware: None

Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	2	1075	25.3	1055	25.8	1055	25.8	2	1059	25.7	1053	25.8	1055	25.8
416.gamess	2	1117	35.1	1113	35.2	1112	35.2	2	1089	36.0	1093	35.8	1089	36.0
433.milc	2	905	20.3	905	20.3	904	20.3	2	903	20.3	904	20.3	903	20.3
434.zeusmp	2	673	27.0	679	26.8	671	27.1	2	692	26.3	673	27.0	673	27.1
435.gromacs	2	518	27.6	515	27.7	524	27.3	2	522	27.4	490	29.1	557	25.7
436.cactusADM	2	786	30.4	793	30.1	788	30.3	1	390	30.6	389	30.7	387	30.8
437.leslie3d	2	1223	15.4	1227	15.3	1221	15.4	2	1199	15.7	1197	15.7	1206	15.6
444.namd	2	617	26.0	618	25.9	618	25.9	2	621	25.8	638	25.1	620	25.9
447.dealII	2	757	30.2	756	30.3	758	30.2	2	713	32.1	717	31.9	721	31.7
450.soplex	2	1162	14.4	1161	14.4	1163	14.3	2	1109	15.0	1106	15.1	1106	15.1
453.povray	2	279	38.2	282	37.8	284	37.5	2	239	44.5	229	46.5	233	45.7
454.calculix	2	541	30.5	545	30.3	529	31.2	2	533	31.0	528	31.2	526	31.4
459.GemsFDTD	2	1451	14.6	1465	14.5	1468	14.5	2	1494	14.2	1461	14.5	1489	14.3
465.tonto	2	919	21.4	918	21.4	912	21.6	2	858	22.9	856	23.0	851	23.1
470.lbm	2	2158	12.7	2159	12.7	2162	12.7	2	2109	13.0	2108	13.0	2107	13.0
481.wrf	2	916	24.4	918	24.3	925	24.2	2	916	24.4	918	24.3	925	24.2
482.sphinx3	2	1731	22.5	1737	22.4	1743	22.4	2	1636	23.8	1640	23.8	1641	23.8

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

General Notes

numactl was used to bind processes to cores
 OMP_NUM_THREADS set to number of processors
 KMP_AFFINITY set to "physical,0"
 KMP_STACKSIZE set to 64M



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Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64
```

Base Optimization Flags

C benchmarks:
-xSSSE3 -ipo -O3 -no-prec-div -static -opt-prefetch

C++ benchmarks:
-xSSSE3 -ipo -O3 -no-prec-div -static -opt-prefetch

Fortran benchmarks:
-xSSSE3 -ipo -O3 -no-prec-div -static -opt-prefetch

Benchmarks using both Fortran and C:
-xSSSE3 -ipo -O3 -no-prec-div -static -opt-prefetch



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Peak Compiler Invocation

C benchmarks (except as noted below):

icc

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):

icpc

450.soplex: icpc -m32

Fortran benchmarks (except as noted below):

ifort

437.leslie3d: ifort -m32

Benchmarks using both Fortran and C:

icc ifort

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

433.milc: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
-no-prec-div -static -fno-alias

470.lbm: -xSSSE3 -ipo -O3 -no-prec-div -static -opt-prefetch
-auto-ilp32

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Peak Optimization Flags (Continued)

482.sphinx3: -xSSSE3 -ipo -O3 -no-prec-div -static -unroll12

C++ benchmarks:

444.namd: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
-no-prec-div -static -fno-alias -auto-ilp32

447.dealII: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
-no-prec-div -static -unroll12 -ansi-alias -scalar-rep-

450.soplex: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
-no-prec-div -static -opt-malloc-options=3

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
-no-prec-div -static -unroll14 -ansi-alias

Fortran benchmarks:

410.bwaves: -xSSSE3 -ipo -O3 -no-prec-div -static -opt-prefetch

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
-no-prec-div -static -unroll12 -Obo -ansi-alias
-scalar-rep-

434.zeusmp: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
-no-prec-div -static

437.leslie3d: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
-no-prec-div -static -opt-malloc-options=3 -opt-prefetch

459.GemsFDTD: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
-no-prec-div -static -unroll12 -Obo -opt-prefetch

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
-no-prec-div -static -unroll14 -auto

Benchmarks using both Fortran and C:

435.gromacs: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
-no-prec-div -static -opt-prefetch -auto-ilp32

436.cactusADM: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
-no-prec-div -static -unroll12 -opt-prefetch -parallel
-auto-ilp32

454.calculix: -xSSSE3 -ipo -O3 -no-prec-div -static -auto-ilp32

481.wrf: basepeak = yes



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The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20090827.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20090827.xml>

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For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.

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