



SPEC® CINT2006 Result

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IBM Corporation

SPECint®_rate2006 = 129

IBM System x iDataPlex dx340 (Intel Xeon E5430)

SPECint_rate_base2006 = 119

CPU2006 license: 11

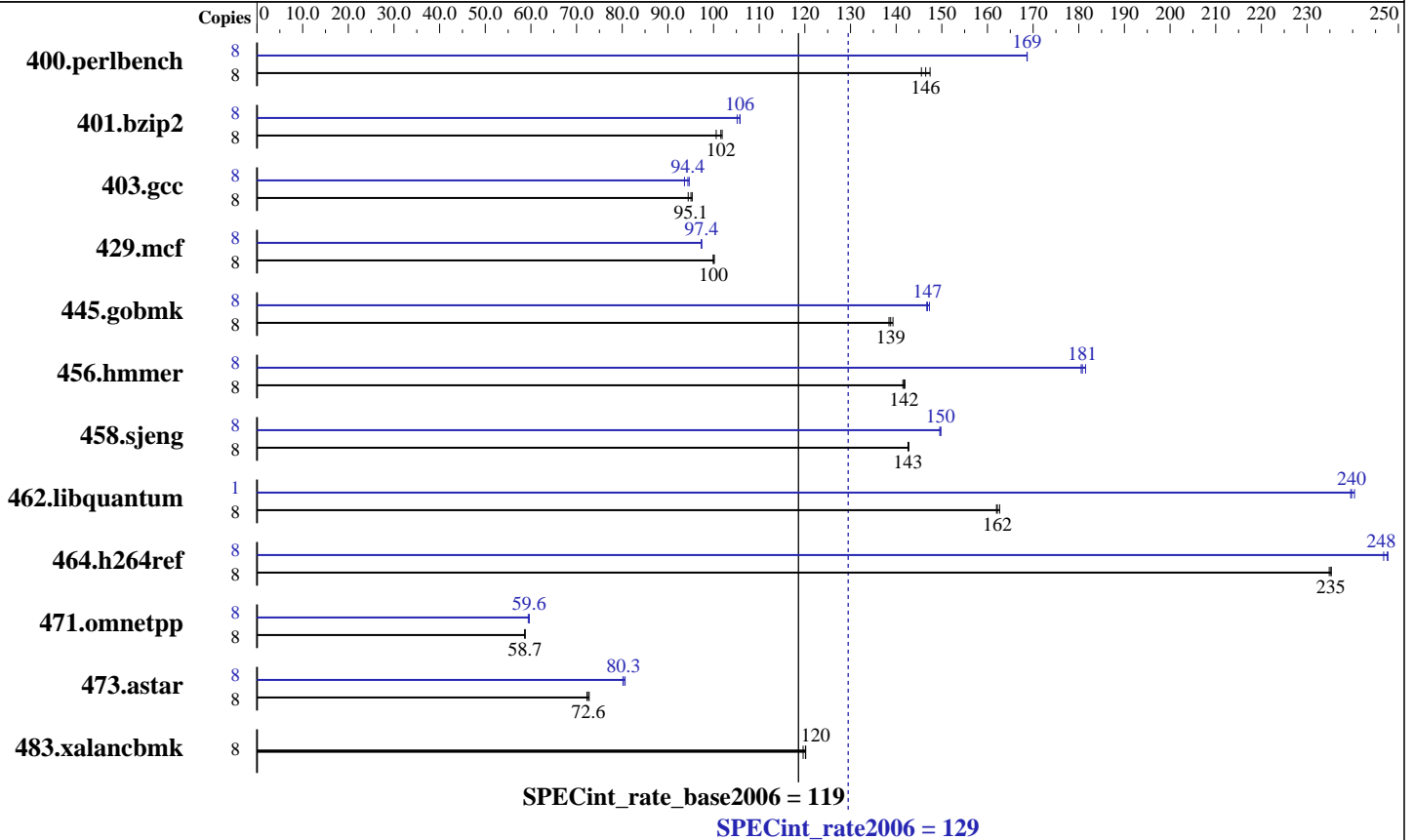
Test date: Apr-2009

Test sponsor: IBM Corporation

Hardware Availability: Oct-2008

Tested by: IBM Corporation

Software Availability: Nov-2008



Hardware

CPU Name: Intel Xeon E5430
 CPU Characteristics: 1333MHz system bus
 CPU MHz: 2666
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (4 x 4 GB PC2-5300F ECC)
 Disk Subsystem: 1 x 250 GB SATA, 7200 RPM
 Other Hardware: None

Software

Operating System: SuSE Linux Enterprise Server 10(x86_64) SP2, Kernel 2.6.16.60-0.21-smp
 Compiler: Intel C++ Compiler 11.0 for Linux Build 20080930 Package ID: l_cproc_p_11.0.066
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502



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Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	534	146	537	146	530	147	8	463	169	463	169	463	169
401.bzip2	8	758	102	768	101	760	102	8	730	106	734	105	730	106
403.gcc	8	682	94.4	678	95.1	676	95.3	8	682	94.4	680	94.7	688	93.6
429.mcf	8	728	100	730	100	730	99.9	8	749	97.4	750	97.3	749	97.4
445.gobmk	8	606	138	602	139	605	139	8	570	147	572	147	571	147
456.hammer	8	527	142	528	141	526	142	8	413	181	413	181	411	181
458.sjeng	8	678	143	679	143	679	143	8	646	150	647	150	647	150
462.libquantum	8	1022	162	1019	163	1023	162	1	86.2	240	86.5	240	86.4	240
464.h264ref	8	753	235	752	235	754	235	8	715	248	715	248	717	247
471.omnetpp	8	852	58.7	852	58.7	852	58.7	8	839	59.6	839	59.6	842	59.4
473.astar	8	777	72.3	774	72.6	772	72.8	8	701	80.2	697	80.6	700	80.3
483.xalancbmk	8	459	120	460	120	461	120	8	459	120	460	120	461	120

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

General Notes

```

taskset was used to bind processes to cores except
for 462.libquantum peak
'ulimit -s unlimited' was used to set the stack size to unlimited prior to run
OMP_NUM_THREADS set to number of processors
KMP_AFFINITY set to "physical,0"
KMP_STACKSIZE set to 64M
Hardware Prefetcher Enable and Adjacent Cache Line Prefetch Disable

```

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc



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Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3 -opt-prefetch

C++ benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/Compiler/11.0/066/bin/intel64/icc

456.hmmer: /opt/intel/Compiler/11.0/066/bin/intel64/icc

C++ benchmarks:

icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX



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Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -ansi-alias -opt-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -opt-prefetch -ansi-alias

403.gcc: -xSSE4.1 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3

429.mcf: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -opt-prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -O2 -ipo
-no-prec-div -ansi-alias

456.hmmer: -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2
-ansi-alias

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll4

462.libquantum: -xSSE4.1 -ipo -O3 -no-prec-div -static
-opt-malloc-options=3 -parallel -par-runtime-control
-opt-prefetch

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmarheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmarheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca



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The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090805.00.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090805.00.xml>

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For other inquiries, please contact webmaster@spec.org.

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