



SPEC® CINT2006 Result

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GIGA-BYTE Technology Co. Ltd.

(Test Sponsor: Intel Corporation)

Gigabyte MA78GM-S2H Motherboard (AMD Phenom X3 8750)

SPECint®2006 = 13.4

SPECint_base2006 = 12.7

CPU2006 license: 13

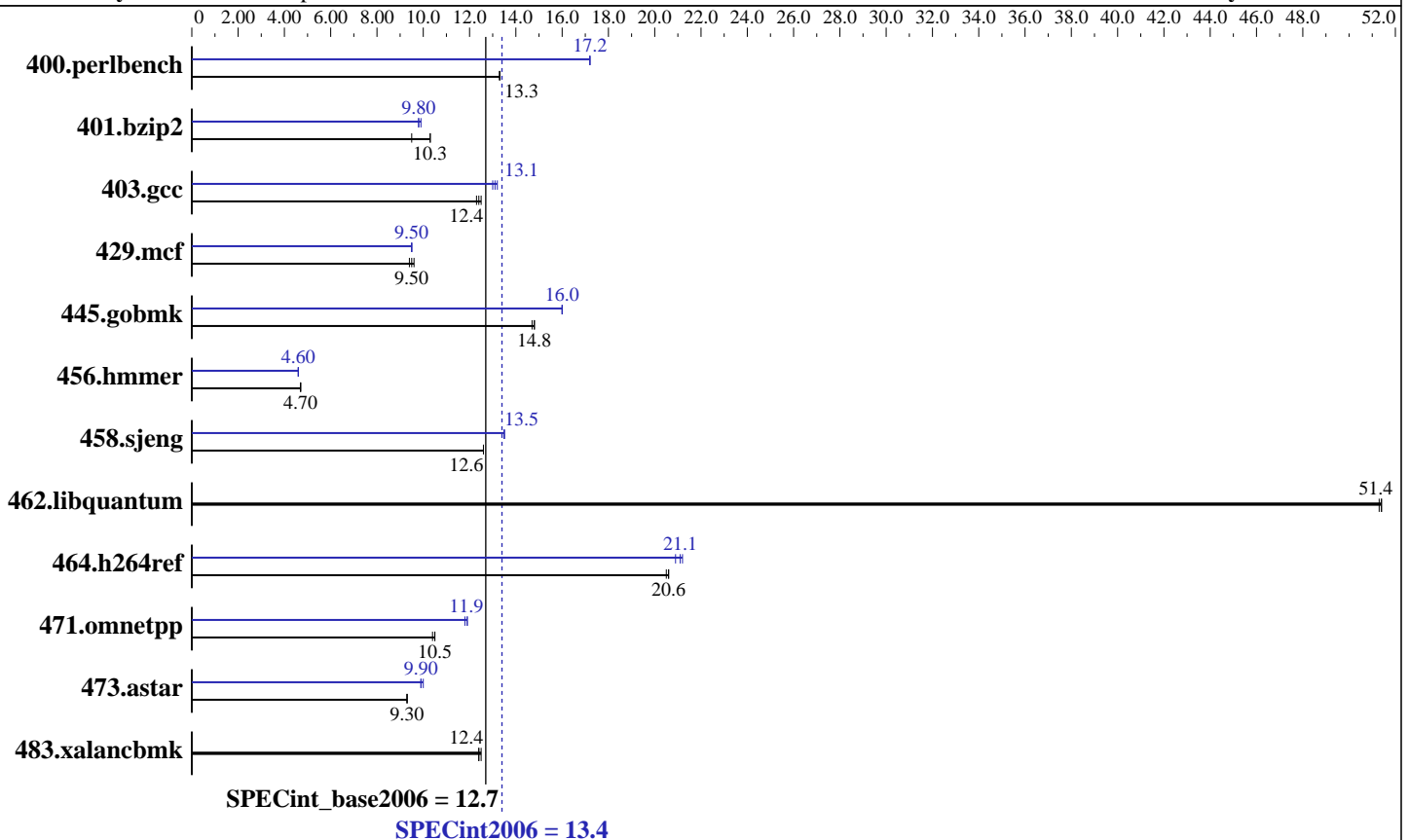
Test sponsor: Intel Corporation

Tested by: Intel Corporation

Test date: Jan-2009

Hardware Availability: Apr-2008

Software Availability: Nov-2008



Hardware

CPU Name: AMD Phenom X3 8750
 CPU Characteristics:
 CPU MHz: 2400
 FPU: Integrated
 CPU(s) enabled: 3 cores, 1 chip, 3 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 64 KB I + 64 KB D on chip per core
 Secondary Cache: 512 KB I+D on chip per core
 L3 Cache: 2 MB I+D on chip per chip
 Other Cache: None
 Memory: 4 GB (4x1GB DDR2-800 CL5)
 Disk Subsystem: Seagate 320 GB SATA, 7200RPM
 Other Hardware: None

Software

Operating System: Windows Vista Ultimate w/ SP1 (64-bit)
 Compiler: Intel C++ Compiler Professional 11.0 for IA32
 Build 20080930 Package ID: w_cproc_p_11.0.054
 Microsoft Visual Studio 2008 (for libraries)
 Auto Parallel: Yes
 File System: NTFS
 System State: Default
 Base Pointers: 32-bit
 Peak Pointers: 32-bit
 Other Software: SmartHeap Library Version 8.1 from
<http://www.microquill.com/>



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	735	13.3	733	13.3	<u>734</u>	<u>13.3</u>	568	17.2	566	17.2	<u>567</u>	<u>17.2</u>
401.bzip2	935	10.3	<u>938</u>	<u>10.3</u>	1011	9.50	987	9.80	971	9.90	<u>986</u>	<u>9.80</u>
403.gcc	644	12.5	<u>650</u>	<u>12.4</u>	656	12.3	617	13.0	610	13.2	<u>613</u>	<u>13.1</u>
429.mcf	<u>961</u>	<u>9.50</u>	953	9.60	966	9.40	961	9.50	<u>962</u>	<u>9.50</u>	963	9.50
445.gobmk	<u>711</u>	<u>14.8</u>	711	14.7	710	14.8	657	16.0	<u>656</u>	<u>16.0</u>	656	16.0
456.hammer	<u>1981</u>	<u>4.70</u>	1980	4.70	1983	4.70	<u>2014</u>	<u>4.60</u>	2014	4.60	2015	4.60
458.sjeng	959	12.6	960	12.6	<u>960</u>	<u>12.6</u>	895	13.5	<u>896</u>	<u>13.5</u>	896	13.5
462.libquantum	404	51.3	403	51.4	<u>404</u>	<u>51.4</u>	404	51.3	403	51.4	<u>404</u>	<u>51.4</u>
464.h264ref	1079	20.5	1073	20.6	<u>1074</u>	<u>20.6</u>	1045	21.2	1057	20.9	<u>1050</u>	<u>21.1</u>
471.omnetpp	<u>596</u>	<u>10.5</u>	594	10.5	599	10.4	527	11.9	528	11.8	<u>527</u>	<u>11.9</u>
473.astar	757	9.30	755	9.30	<u>756</u>	<u>9.30</u>	703	10.0	709	9.90	<u>706</u>	<u>9.90</u>
483.xalancbmk	555	12.4	<u>555</u>	<u>12.4</u>	552	12.5	555	12.4	<u>555</u>	<u>12.4</u>	552	12.5

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

Tested systems can be used with Shin-G ATX case, Antec NeoPower 480W power supply
Binaries were built on Windows Vista Ultimate (32-bit)
OMP_NUM_THREADS set to number of logical processors as seen by the OS
KMP_AFFINITY set to physical,0

Base Compiler Invocation

C benchmarks:
icl -Qvc9 -Qc99

C++ benchmarks:
icl -Qvc9

Base Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword



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Base Optimization Flags

C benchmarks:

```
/arch:SSE2 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qparallel
-Qpar-runtime-control -Qvec-guard-write /F512000000
```

C++ benchmarks:

```
/arch:SSE2 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qcxx-features
/F512000000 shlw32m.lib -link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks:

```
icl -Qvc9 -Qc99
```

C++ benchmarks:

```
icl -Qvc9
```

Peak Portability Flags

```
403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword
```

Peak Optimization Flags

C benchmarks:

```
400.perlbench: /arch:SSE2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qansi-alias -Qopt-prefetch
/F512000000 shlw32m.lib -link /FORCE:MULTIPLE
```

```
401.bzip2: /arch:SSE2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qopt-prefetch -Qansi-alias
/F512000000
```

```
403.gcc: /arch:SSE2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- /F512000000
```

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Peak Optimization Flags (Continued)

429.mcf: /arch:SSE2 -Qipo -O3 -Qprec-div- -Qopt-prefetch /F512000000

445.gobmk: /arch:SSE2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O2 -Qprec-div- -Qansi-alias /F512000000

456.hmmer: /arch:SSE2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias /F512000000

458.sjeng: /arch:SSE2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll4 /F512000000

462.libquantum: basepeak = yes

464.h264ref: Same as 456.hmmer

C++ benchmarks:

471.omnetpp: /arch:SSE2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qansi-alias -Qopt-ra-region-strategy=block /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

473.astar: /arch:SSE2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qansi-alias -Qopt-ra-region-strategy=routine /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-win32-revA.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-win32-revA.xml>



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