SPEC® CINT2006 Result
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GIGA-BYTE Technology Co. Ltd.
(Test Sponsor: Intel Corporation)

Gigabyte MA78GM-S2H Motherboard (AMD Athlon X2 7550)

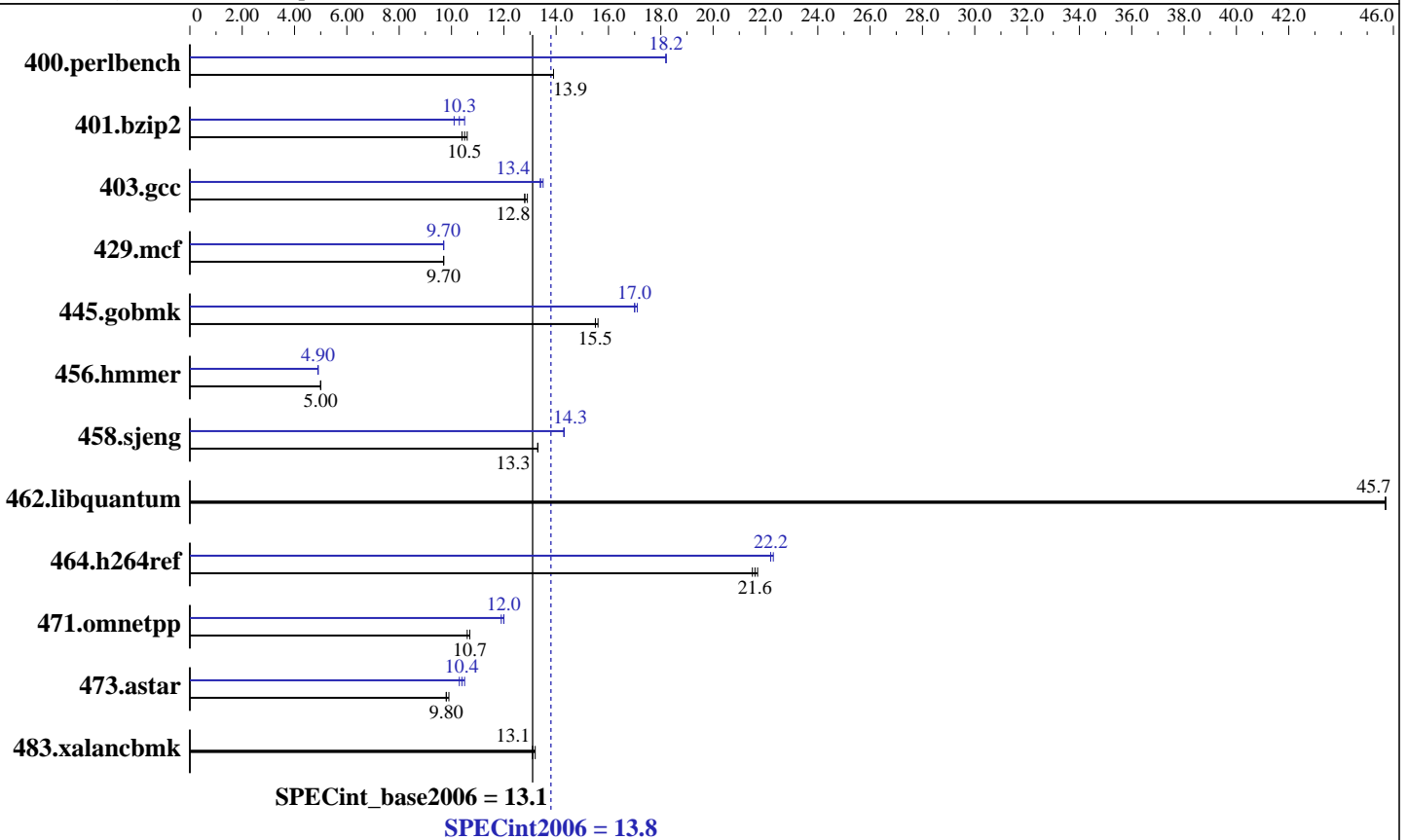$SPECint®2006 =$ 13.8

$SPECint\_base2006 =$ 13.1

CPU2006 license: 13	**Test date:** May-2009
Test sponsor: Intel Corporation	**Hardware Availability:** Dec-2008
Tested by: Intel Corporation	**Software Availability:** Nov-2008

400.perlbench	18.2 / 13.9
401.bzip2	10.3 / 10.5
403.gcc	13.4 / 12.8
429.mcf	9.70 / 9.70
445.gobmk	17.0 / 15.5
456.hmmer	4.90 / 5.00
458.sjeng	14.3 / 13.3
462.libquantum	45.7
464.h264ref	22.2 / 21.6
471.omnetpp	12.0 / 10.7
473.astar	10.4 / 9.80
483.xalancbmk	13.1

SPECint_base2006 = 13.1
SPECint2006 = 13.8

Hardware

CPU Name:	AMD Athlon X2 7550
CPU Characteristics:	
CPU MHz:	2500
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	64 KB I + 64 KB D on chip per core
Secondary Cache:	512 KB I+D on chip per core
L3 Cache:	2 MB I+D on chip per chip
Other Cache:	None
Memory:	4 GB (4x1GB DDR2-800 CL5)
Disk Subsystem:	Seagate 320 GB SATA, 7200RPM
Other Hardware:	None

Software

Operating System:	Windows Vista Ultimate w/ SP1 (64-bit)
Compiler:	Intel C++ Compiler Professional 11.0 for IA32 Build 20080930 Package ID: w_cproc_p_11.0.054 Microsoft Visual Studio 2008 (for libraries)
Auto Parallel:	Yes
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	32-bit
Other Software:	SmartHeap Library Version 8.1 from http://www.microquill.com/

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Results Table

Benchmark	Base						Peak					
	Seconds	**Ratio**	**Seconds**	**Ratio**	**Seconds**	**Ratio**	**Seconds**	**Ratio**	**Seconds**	**Ratio**	**Seconds**	**Ratio**
400.perlbench	701	13.9	**701**	**13.9**	701	13.9	**537**	**18.2**	537	18.2	537	18.2
401.bzip2	**917**	**10.5**	930	10.4	914	10.6	923	10.5	**934**	**10.3**	955	10.1
403.gcc	626	12.9	**627**	**12.8**	630	12.8	**600**	**13.4**	595	13.5	602	13.4
429.mcf	**937**	**9.70**	940	9.70	937	9.70	942	9.70	**943**	**9.70**	943	9.70
445.gobmk	**675**	**15.5**	675	15.5	673	15.6	615	17.1	**617**	**17.0**	617	17.0
456.hmmer	1874	5.00	**1875**	**5.00**	1877	5.00	1910	4.90	1917	4.90	**1914**	**4.90**
458.sjeng	**908**	**13.3**	907	13.3	910	13.3	847	14.3	846	14.3	**846**	**14.3**
462.libquantum	454	45.7	453	45.7	**454**	**45.7**	454	45.7	453	45.7	**454**	**45.7**
464.h264ref	1031	21.5	**1023**	**21.6**	1021	21.7	**995**	**22.2**	991	22.3	996	22.2
471.omnetpp	583	10.7	**586**	**10.7**	589	10.6	520	12.0	**521**	**12.0**	525	11.9
473.astar	712	9.90	**715**	**9.80**	718	9.80	**674**	**10.4**	679	10.3	672	10.5
483.xalancbmk	523	13.2	527	13.1	**525**	**13.1**	523	13.2	527	13.1	**525**	**13.1**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
Tested systems can be used with Shin-G ATX case, Antec NeoPower 480W power supply
Binaries were built on Windows Vista Ultimate (32-bit)
OMP_NUM_THREADS set to number of logical processors as seen by the OS
KMP_AFFINITY set to physical,0
```

Base Compiler Invocation

C benchmarks:
```
icl -Qvc9 -Qc99
```

C++ benchmarks:
```
icl -Qvc9
```

Base Portability Flags

```
        403.gcc: -DSPEC_CPU_WIN32
    464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
  483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword
```

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Base Optimization Flags

C benchmarks:
```
/arch:SSE2 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qparallel
-Qpar-runtime-control -Qvec-guard-write /F512000000
```

C++ benchmarks:
```
/arch:SSE2 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qcxx-features
/F512000000 shlw32m.lib          -link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks:
```
icl -Qvc9 -Qc99
```

C++ benchmarks:
```
icl -Qvc9
```

Peak Portability Flags

```
   403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword
```

Peak Optimization Flags

C benchmarks:

```
400.perlbench: /arch:SSE2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
               -Qipo -O3 -Qprec-div- -Qansi-alias -Qopt-prefetch
               /F512000000 shlw32m.lib          -link /FORCE:MULTIPLE

   401.bzip2: /arch:SSE2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
              -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qansi-alias
              /F512000000

     403.gcc: /arch:SSE2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
              -Qipo -O3 -Qprec-div- /F512000000
```

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Peak Optimization Flags (Continued)

429.mcf: `/arch:SSE2 -Qipo -O3 -Qprec-div- -Qopt-prefetch /F512000000`

445.gobmk: `/arch:SSE2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O2 -Qprec-div- -Qansi-alias /F512000000`

456.hmmer: `/arch:SSE2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias /F512000000`

458.sjeng: `/arch:SSE2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll4 /F512000000`

462.libquantum: `basepeak = yes`

464.h264ref: `Same as 456.hmmer`

C++ benchmarks:

471.omnetpp: `/arch:SSE2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qansi-alias -Qopt-ra-region-strategy=block /F512000000 shlw32m.lib -link /FORCE:MULTIPLE`

473.astar: `/arch:SSE2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qansi-alias -Qopt-ra-region-strategy=routine /F512000000 shlw32m.lib -link /FORCE:MULTIPLE`

483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.0-win32-revA.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic11.0-win32-revA.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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