SPEC® CFP2006 Result

GIGA-BYTE Technology Co. Ltd.

(Test Sponsor: Intel Corporation)

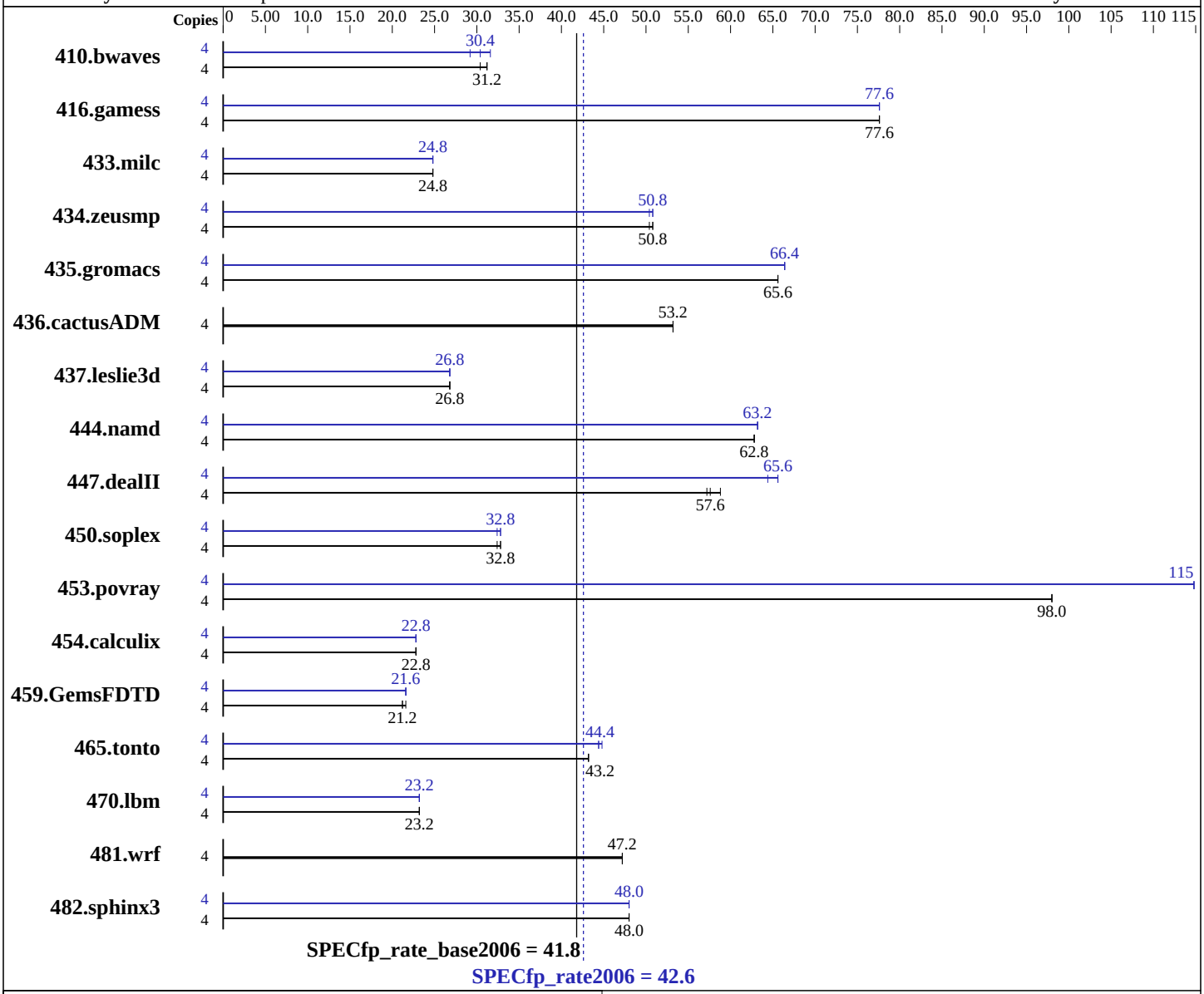
Gigabyte MA78GM-S2H Motherboard (AMD Phenom II X4 955)

SPECfp®_rate2006 = 42.6

SPECfp_rate_base2006 = 41.8

CPU2006 license: 13
Test sponsor: Intel Corporation
Tested by: Intel Corporation

Test date: May-2009
Hardware Availability: Apr-2009
Software Availability: Nov-2008



Hardware

CPU Name:	AMD Phenom II X4 955
CPU Characteristics:	
CPU MHz:	3200
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	64 KB I + 64 KB D on chip per core
Secondary Cache:	512 KB I+D on chip per core

Continued on next page

Software

Operating System:	Windows Vista Ultimate w/ SP1 (64-bit)
Compiler:	Intel C++ Compiler Professional 11.0 for IA32 Build 20080930 Package ID: w_cproc_p_11.0.054 Intel Visual Fortran Compiler Professional 11.0 for IA32 Build 20080930 Package ID: w_cprof_p_11.0.054 Microsoft Visual Studio 2008 (for libraries)
Auto Parallel:	No
File System:	NTFS

Continued on next page

SPEC CFP2006 Result

GIGA-BYTE Technology Co. Ltd.

(Test Sponsor: Intel Corporation)

Gigabyte MA78GM-S2H Motherboard (AMD Phenom II X4 955)

SPECfp_rate2006 = 42.6

SPECfp_rate_base2006 = 41.8

CPU2006 license: 13
Test sponsor: Intel Corporation
Tested by: Intel Corporation

Test date: May-2009
Hardware Availability: Apr-2009
Software Availability: Nov-2008

L3 Cache: 6 MB I+D on chip per chip
Other Cache: None
Memory: 4 GB (4x1GB DDR2-800 CL5)
Disk Subsystem: Seagate 320 GB SATA, 7200RPM
Other Hardware: None

System State: Default
Base Pointers: 32-bit
Peak Pointers: 32-bit
Other Software: SmartHeap Library Version 8.1 from http://www.microquill.com/

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	4	1798	30.4	1748	31.2	**1749**	**31.2**	4	1714	31.6	1868	29.2	**1792**	**30.4**
416.gamess	4	**1009**	**77.6**	1008	77.6	1009	77.6	4	**1010**	**77.6**	1010	77.6	1010	77.6
433.milc	4	1486	24.8	**1486**	**24.8**	1487	24.8	4	1486	24.8	**1485**	**24.8**	1485	24.8
434.zeusmp	4	716	50.8	725	50.4	**717**	**50.8**	4	715	50.8	**718**	**50.8**	723	50.4
435.gromacs	4	436	65.6	436	65.6	**436**	**65.6**	4	**431**	**66.4**	431	66.4	430	66.4
436.cactusADM	4	896	53.2	897	53.2	**896**	**53.2**	4	896	53.2	897	53.2	**896**	**53.2**
437.leslie3d	4	1397	26.8	1398	26.8	**1397**	**26.8**	4	**1398**	**26.8**	1397	26.8	1398	26.8
444.namd	4	**511**	**62.8**	511	62.8	510	62.8	4	**507**	**63.2**	508	63.2	507	63.2
447.dealII	4	**796**	**57.6**	800	57.2	780	58.8	4	**698**	**65.6**	697	65.6	712	64.4
450.soplex	4	**1023**	**32.8**	1024	32.4	1021	32.8	4	**1014**	**32.8**	1026	32.4	1014	32.8
453.povray	4	217	98.0	217	98.0	**217**	**98.0**	4	**185**	**115**	186	115	185	115
454.calculix	4	**1447**	**22.8**	1448	22.8	1445	22.8	4	1448	22.8	1444	22.8	**1447**	**22.8**
459.GemsFDTD	4	**1984**	**21.2**	1982	21.6	1995	21.2	4	**1975**	**21.6**	1983	21.6	1967	21.6
465.tonto	4	**913**	**43.2**	912	43.2	915	43.2	4	885	44.4	**883**	**44.4**	882	44.8
470.lbm	4	2384	23.2	**2384**	**23.2**	2383	23.2	4	2383	23.2	**2383**	**23.2**	2383	23.2
481.wrf	4	**943**	**47.2**	943	47.2	943	47.2	4	**943**	**47.2**	943	47.2	943	47.2
482.sphinx3	4	**1627**	**48.0**	1629	48.0	1625	48.0	4	**1627**	**48.0**	1627	48.0	1624	48.0

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
```

General Notes

```
Tested systems can be used with Shin-G ATX case, Antec NeoPower 480W power supply
Binaries were built on Windows Vista Ultimate (32-bit)
```

Base Compiler Invocation

C benchmarks:

```
icl -Qvc9 -Qc99
```

Continued on next page

Base Compiler Invocation (Continued)

C++ benchmarks:
icl -Qvc9

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icl -Qvc9 -Qc99 ifort

Base Portability Flags

436.cactusADM: -Qlowercase /assume:underscore
444.namd: -TP
447.dealII: -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
453.povray: -DSPEC_CPU_WINDOWS_ICL
454.calculix: -DSPEC_CPU_NOZMODIFIER -Qlowercase
481.wrf: -DSPEC_CPU_WINDOWS_ICL

Base Optimization Flags

C benchmarks:
/arch:SSE2 -Qipo -O3 -Qprec-div- -Qopt-prefetch /F1000000000

C++ benchmarks:
/arch:SSE2 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qcxx-features
/F1000000000 shlw32m.lib -link /FORCE:MULTIPLE

Fortran benchmarks:
/arch:SSE2 -Qipo -O3 -Qprec-div- -Qopt-prefetch /F1000000000

Benchmarks using both Fortran and C:
/arch:SSE2 -Qipo -O3 -Qprec-div- -Qopt-prefetch /F1000000000

Peak Compiler Invocation

C benchmarks:
icl -Qvc9 -Qc99

C++ benchmarks:
icl -Qvc9

Fortran benchmarks:
ifort

Continued on next page

Peak Compiler Invocation (Continued)

Benchmarks using both Fortran and C:
icl -Qvc9 -Qc99 ifort

Peak Portability Flags

436.cactusADM: -Qlowercase /assume:underscore
444.namd: -TP
447.dealII: -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
453.povray: -DSPEC_CPU_WINDOWS_ICL
454.calculix: -DSPEC_CPU_NOZMODIFIER -Qlowercase
481.wrf: -DSPEC_CPU_WINDOWS_ICL

Peak Optimization Flags

C benchmarks:

433.milc: /arch:SSE2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Oa /F1000000000

470.lbm: /arch:SSE2 -Qipo -O3 -Qprec-div- -Qopt-prefetch
/F1000000000

482.sphinx3: /arch:SSE2 -Qipo -O3 -Qprec-div- -Qunroll2 /F1000000000

C++ benchmarks:

444.namd: /arch:SSE2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Oa /F1000000000 shlw32m.lib
-link /FORCE:MULTIPLE

447.dealII: /arch:SSE2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias
-Qscalar-rep- /F1000000000 shlw32m.lib
-link /FORCE:MULTIPLE

450.soplex: /arch:SSE2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- /F1000000000 shlw32m.lib
-link /FORCE:MULTIPLE

453.povray: /arch:SSE2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qunroll4 -Qansi-alias /F1000000000
shlw32m.lib -link /FORCE:MULTIPLE

Fortran benchmarks:

Continued on next page

Peak Optimization Flags (Continued)

410.bwaves: /arch:SSE2 -Qipo -O3 -Qprec-div- -Qopt-prefetch /F1000000000

416.gamess: /arch:SSE2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll2 -Ob0 -Qansi-alias -Qscalar-rep- /F1000000000

434.zeusmp: /arch:SSE2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- /F1000000000

437.leslie3d: /arch:SSE2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qopt-prefetch /F1000000000

459.GemsFDTD: /arch:SSE2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll2 -Ob0 -Qopt-prefetch /F1000000000

465.tonto: /arch:SSE2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll4 -Qauto /F1000000000

Benchmarks using both Fortran and C:

435.gromacs: /arch:SSE2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qopt-prefetch /F1000000000

436.cactusADM: basepeak = yes

454.calculix: /arch:SSE2 -Qipo -O3 -Qprec-div- /F1000000000

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.0-win32-revA.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic11.0-win32-revA.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Wed Jul 23 01:27:57 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 23 June 2009.