



SPEC® CINT2006 Result

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Intel Corporation

SPECint®2006 = 23.0

Intel DQ45CB motherboard (Intel Core 2 Duo E7400)

SPECint_base2006 = 21.1

CPU2006 license: 13

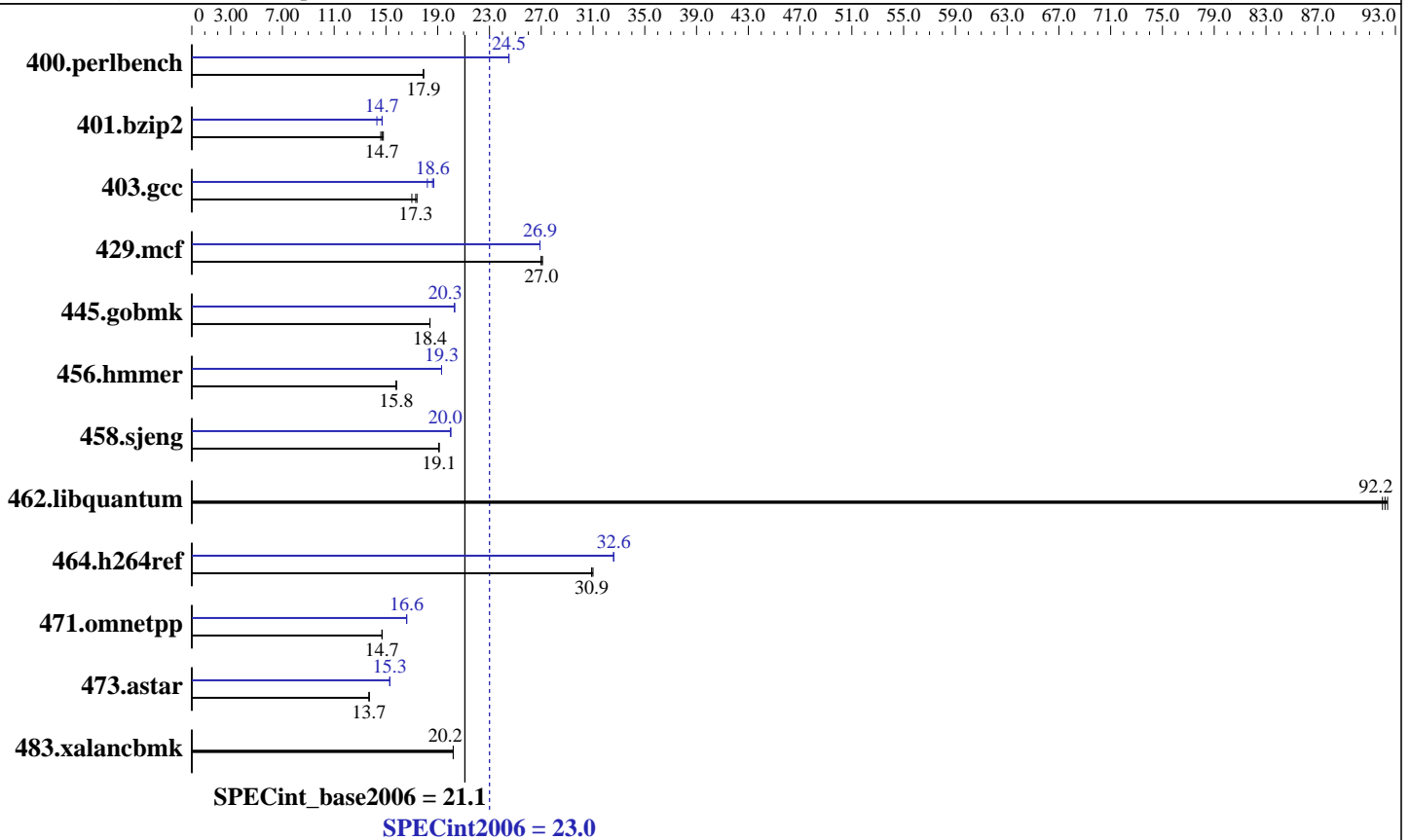
Test date: Apr-2009

Test sponsor: Intel Corporation

Hardware Availability: May-2009

Tested by: Intel Corporation

Software Availability: Nov-2008



Hardware

CPU Name: Intel Core 2 Duo E7400
 CPU Characteristics:
 CPU MHz: 2800
 FPU: Integrated
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 3 MB I+D on chip per chip
 L3 Cache: None
 Other Cache: None
 Memory: 4 GB (4x1GB DDR2-800 CL5)
 Disk Subsystem: Seagate 320 GB SATA, 7200RPM
 Other Hardware: None

Software

Operating System: Windows Vista Ultimate w/ SP1 (64-bit)
 Compiler: Intel C++ Compiler Professional 11.0 for IA32
 Build 20080930 Package ID: w_cproc_p_11.0.054
 Microsoft Visual Studio 2008 (for libraries)
 Auto Parallel: Yes
 File System: NTFS
 System State: Default
 Base Pointers: 32-bit
 Peak Pointers: 32-bit
 Other Software: SmartHeap Library Version 8.1 from
<http://www.microquill.com/>



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	544	17.9	545	17.9	<u>545</u>	<u>17.9</u>	399	24.5	398	24.5	<u>398</u>	<u>24.5</u>
401.bzip2	<u>656</u>	<u>14.7</u>	652	14.8	662	14.6	<u>658</u>	<u>14.7</u>	676	14.3	655	14.7
403.gcc	<u>464</u>	<u>17.3</u>	475	17.0	463	17.4	<u>432</u>	<u>18.6</u>	442	18.2	431	18.7
429.mcf	338	27.0	337	27.1	<u>338</u>	<u>27.0</u>	339	26.9	339	26.9	<u>339</u>	<u>26.9</u>
445.gobmk	570	18.4	570	18.4	<u>570</u>	<u>18.4</u>	518	20.3	517	20.3	<u>518</u>	<u>20.3</u>
456.hammer	589	15.8	589	15.8	<u>589</u>	<u>15.8</u>	<u>483</u>	<u>19.3</u>	483	19.3	483	19.3
458.sjeng	633	19.1	634	19.1	<u>633</u>	<u>19.1</u>	604	20.0	<u>604</u>	<u>20.0</u>	605	20.0
462.libquantum	224	92.4	<u>225</u>	<u>92.2</u>	225	92.0	224	92.4	<u>225</u>	<u>92.2</u>	225	92.0
464.h264ref	716	30.9	<u>715</u>	<u>30.9</u>	715	31.0	<u>680</u>	<u>32.6</u>	680	32.6	680	32.6
471.omnetpp	425	14.7	<u>425</u>	<u>14.7</u>	426	14.7	377	16.6	<u>376</u>	<u>16.6</u>	376	16.6
473.astar	512	13.7	<u>512</u>	<u>13.7</u>	512	13.7	458	15.3	<u>458</u>	<u>15.3</u>	458	15.3
483.xalancbmk	341	20.2	<u>341</u>	<u>20.2</u>	342	20.2	341	20.2	<u>341</u>	<u>20.2</u>	342	20.2

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

Tested systems can be used with Shin-G ATX case,
 Antec Truepower Trio power supply TP3-650
 Binaries were built on Windows Vista Ultimate (32-bit)
 Binaries were built on Windows Vista Ultimate (32-bit)
 OMP_NUM_THREADS set to number of logical processors as seen by the OS
 KMP_AFFINITY set to physical,0

Base Compiler Invocation

C benchmarks:
 icl -Qvc9 -Qc99

C++ benchmarks:
 icl -Qvc9

Base Portability Flags

403.gcc: -DSPEC_CPU_WIN32
 464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
 483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword



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Base Optimization Flags

C benchmarks:

-QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qparallel
-Qpar-runtime-control -Qvec-guard-write /F512000000

C++ benchmarks:

-QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qcxx-features
/F512000000 shlw32m.lib -link /FORCE:MULTIPLE

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks:

icl -Qvc9 -Qc99

C++ benchmarks:

icl -Qvc9

Peak Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
483.xalanbmk: -Qoption,cpp,--no_wchar_t_keyword

Peak Optimization Flags

C benchmarks:

400.perlbench: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qansi-alias -Qopt-prefetch
/F512000000 shlw32m.lib -link /FORCE:MULTIPLE

401.bzip2: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qopt-prefetch -Qansi-alias
/F512000000

403.gcc: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- /F512000000

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Peak Optimization Flags (Continued)

429.mcf: -QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch
/F512000000

445.gobmk: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O2 -Qprec-div- -Qansi-alias /F512000000

456.hmmcr: -QxSSE4.1 -Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias
/F512000000

458.sjeng: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qunroll4 /F512000000

462.libquantum: basepeak = yes

464.h264ref: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias /F512000000

C++ benchmarks:

471.omnetpp: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qansi-alias
-Qopt-ra-region-strategy=block /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE

473.astar: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qansi-alias
-Qopt-ra-region-strategy=routine /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-win32-revA.20090710.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-win32-revA.20090710.xml>



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