SPEC® CINT2006 Result
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Intel Corporation

Intel DQ45CB motherboard (Intel Core 2 Duo E7400)

SPECint®_rate2006 =　38.1

SPECint_rate_base2006 = 36.4
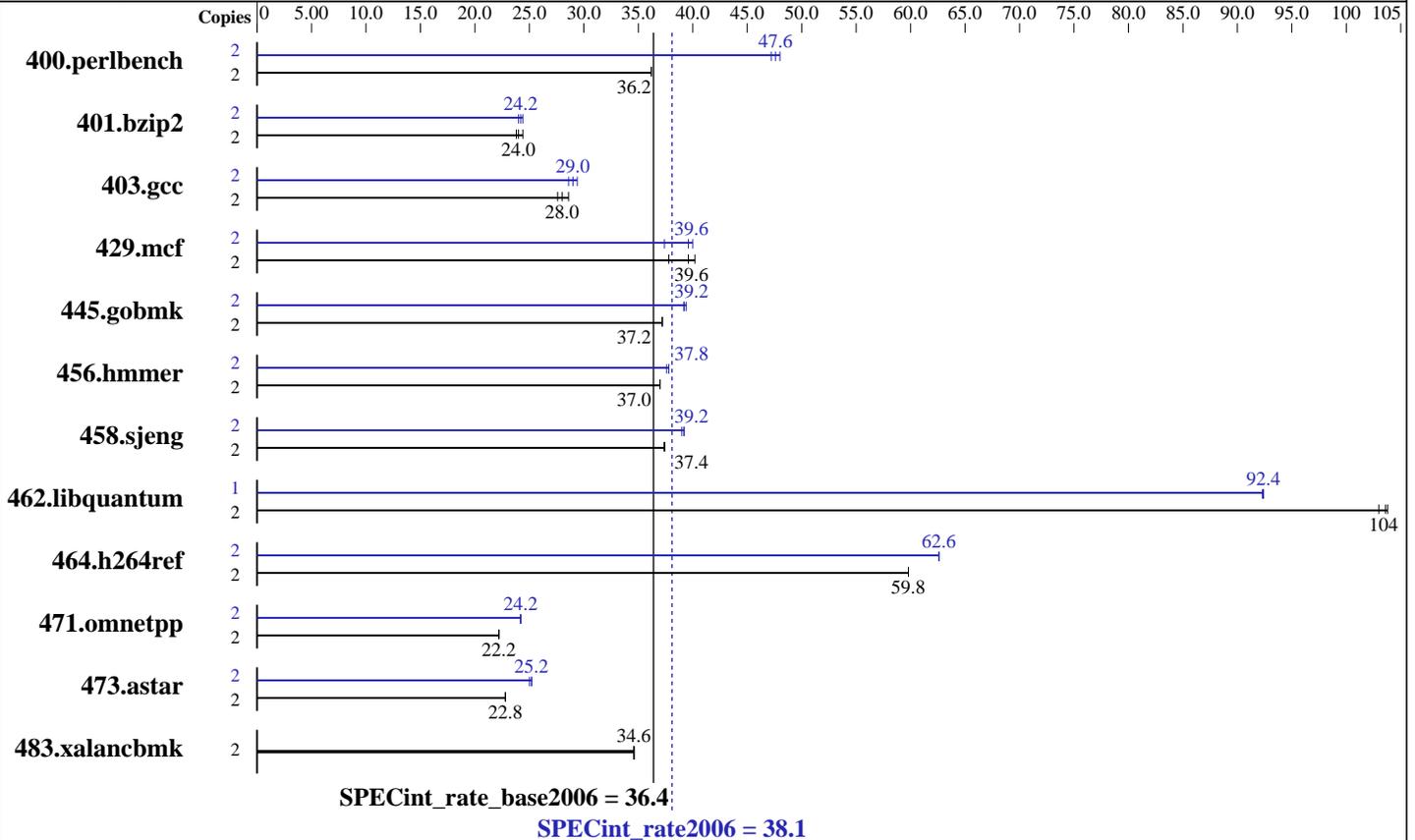
CPU2006 license: 13
Test sponsor: Intel Corporation
Tested by: Intel Corporation

Test date: Apr-2009
Hardware Availability: May-2009
Software Availability: Nov-2008

Copies	
400.perlbench	2 / 47.6
	2 / 36.2
401.bzip2	2 / 24.2
	2 / 24.0
403.gcc	2 / 29.0
	2 / 28.0
429.mcf	2 / 39.6
	2 / 39.6
445.gobmk	2 / 39.2
	2 / 37.2
456.hmmer	2 / 37.8
	2 / 37.0
458.sjeng	2 / 39.2
	2 / 37.4
462.libquantum	1 / 92.4
	2 / 104
464.h264ref	2 / 62.6
	2 / 59.8
471.omnetpp	2 / 24.2
	2 / 22.2
473.astar	2 / 25.2
	2 / 22.8
483.xalancbmk	2 / 34.6

SPECint_rate_base2006 = 36.4

SPECint_rate2006 = 38.1

Hardware

CPU Name: Intel Core 2 Duo E7400
CPU Characteristics:
CPU MHz: 2800
FPU: Integrated
CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
CPU(s) orderable: 1 chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 3 MB I+D on chip per chip
L3 Cache: None
Other Cache: None
Memory: 4 GB (4x1GB DDR2-800 CL5)
Disk Subsystem: Seagate 320 GB SATA, 7200RPM
Other Hardware: None

Software

Operating System: Windows Vista Ultimate w/ SP1 (64-bit)
Compiler: Intel C++ Compiler Professional 11.0 for IA32
Build 20080930 Package ID: w_cproc_p_11.0.054
Microsoft Visual Studio 2008 (for libraries)
Auto Parallel: Yes
File System: NTFS
System State: Default
Base Pointers: 32-bit
Peak Pointers: 32-bit
Other Software: SmartHeap Library Version 8.1 from
http://www.microquill.com/

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Results Table

Benchmark	Copies	Base Seconds	Base Ratio	Base Seconds	Base Ratio	Base Seconds	Base Ratio	Copies	Peak Seconds	Peak Ratio	Peak Seconds	Peak Ratio	Peak Seconds	Peak Ratio
400.perlbench	2	540	36.2	539	36.2	**540**	**36.2**	2	415	47.2	407	48.0	**411**	**47.6**
401.bzip2	2	792	24.4	**804**	**24.0**	808	23.8	2	**800**	**24.2**	790	24.4	806	24.0
403.gcc	2	**574**	**28.0**	564	28.6	582	27.6	2	**555**	**29.0**	561	28.6	548	29.4
429.mcf	2	455	40.2	**460**	**39.6**	484	37.8	2	**462**	**39.6**	487	37.4	456	40.0
445.gobmk	2	564	37.2	**564**	**37.2**	563	37.2	2	**534**	**39.2**	535	39.2	534	39.4
456.hmmer	2	**503**	**37.0**	504	37.0	503	37.0	2	495	37.8	**495**	**37.8**	495	37.6
458.sjeng	2	645	37.4	**646**	**37.4**	646	37.4	2	619	39.0	619	39.2	**619**	**39.2**
462.libquantum	2	399	104	**400**	**104**	402	103	1	224	92.4	**224**	**92.4**	225	92.3
464.h264ref	2	740	59.8	740	59.8	**740**	**59.8**	2	707	62.6	**707**	**62.6**	708	62.6
471.omnetpp	2	**563**	**22.2**	563	22.2	564	22.2	2	**516**	**24.2**	517	24.2	516	24.2
473.astar	2	617	22.8	614	22.8	**615**	**22.8**	2	559	25.0	558	25.2	**558**	**25.2**
483.xalancbmk	2	**399**	**34.6**	399	34.6	399	34.6	2	**399**	**34.6**	399	34.6	399	34.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

General Notes

```
Tested systems can be used with Shin-G ATX case,
Antec Truepower Trio power supply TP3-650
Binaries were built on Windows Vista Ultimate (32-bit)
Binaries were built on Windows Vista Ultimate (32-bit)
OMP_NUM_THREADS set to number of logical processors as seen by the OS
KMP_AFFINITY set to physical,0
submit disabled for 462.libquantum peak
```

Base Compiler Invocation

C benchmarks:
```
icl -Qvc9 -Qc99
```

C++ benchmarks:
```
icl -Qvc9
```

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Test sponsor: Intel Corporation		**Hardware Availability:**	May-2009
Tested by: Intel Corporation		**Software Availability:**	Nov-2008

Base Portability Flags

```
      403.gcc: -DSPEC_CPU_WIN32
   464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
 483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword
```

Base Optimization Flags

C benchmarks:

```
    -QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch /F512000000
```

C++ benchmarks:

```
    -QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qcxx-features
    /F512000000 shlw32m.lib          -link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

```
      403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks:

```
    icl -Qvc9 -Qc99
```

C++ benchmarks:

```
    icl -Qvc9
```

Peak Portability Flags

```
      403.gcc: -DSPEC_CPU_WIN32
   464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
 483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword
```

Peak Optimization Flags

C benchmarks:

```
  400.perlbench: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
                 -Qipo -O3 -Qprec-div- -Qansi-alias -Qopt-prefetch
                 /F512000000 shlw32m.lib          -link /FORCE:MULTIPLE
```

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Peak Optimization Flags (Continued)

```
   401.bzip2: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
              -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qansi-alias
              /F512000000

     403.gcc: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
              -Qipo -O3 -Qprec-div- -Qopt-prefetch /F512000000

     429.mcf: -QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch
              /F512000000

   445.gobmk: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
              -Qipo -O2 -Qprec-div- -Qansi-alias /F512000000

   456.hmmer: -QxSSE4.1 -Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias
              /F512000000

   458.sjeng: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
              -Qipo -O3 -Qprec-div- -Qunroll4 /F512000000

462.libquantum: -QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch
              -Qparallel -Qpar-runtime-control -Qvec-guard-write
              /F512000000

  464.h264ref: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
              -Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias /F512000000
```

C++ benchmarks:

```
  471.omnetpp: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
              -Qipo -O3 -Qprec-div- -Qansi-alias
              -Qopt-ra-region-strategy=block /F512000000 shlw32m.lib
                       -link /FORCE:MULTIPLE

    473.astar: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
              -Qipo -O3 -Qprec-div- -Qansi-alias
              -Qopt-ra-region-strategy=routine /F512000000 shlw32m.lib
                       -link /FORCE:MULTIPLE

 483.xalancbmk: basepeak = yes
```

Peak Other Flags

C benchmarks:

```
     403.gcc: -Dalloca=_alloca
```

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The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-win32-revA.20090710.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-win32-revA.20090710.xml`

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