SPEC® CINT2006 Result

Intel Corporation

Intel DQ45CB motherboard (Intel Core 2 Quad Q8200)

$SPECint®2006 =$ 20.0

$SPECint\_base2006 =$ 18.3

Benchmark results (scale 0 to 110):

Benchmark	SPECint2006 (peak)	SPECint_base2006
400.perlbench	20.6	15.0
401.bzip2	11.6	11.6
403.gcc	16.6	14.2
429.mcf	23.7	24.1
445.gobmk	17.0	15.5
456.hmmer	16.1	13.2
458.sjeng	16.9	16.1
462.libquantum		108
464.h264ref	27.1	25.4
471.omnetpp	15.0	13.3
473.astar	12.7	11.4
483.xalancbmk		17.2

SPECint_base2006 = 18.3
SPECint2006 = 20.0

Hardware

CPU Name:	Intel Core 2 Quad Q8200
CPU Characteristics:	
CPU MHz:	2333
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	4 MB I+D on chip per chip, 2 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	4 GB (4x1GB DDR2-800 CL5)
Disk Subsystem:	Seagate 320 GB SATA, 7200RPM
Other Hardware:	None

Software

Operating System:	Windows Vista Ultimate w/ SP1 (64-bit)
Compiler:	Intel C++ Compiler Professional 11.0 for IA32 Build 20080930 Package ID: w_cproc_p_11.0.054 Microsoft Visual Studio 2008 (for libraries)
Auto Parallel:	Yes
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	32-bit
Other Software:	SmartHeap Library Version 8.1 from http://www.microquill.com/

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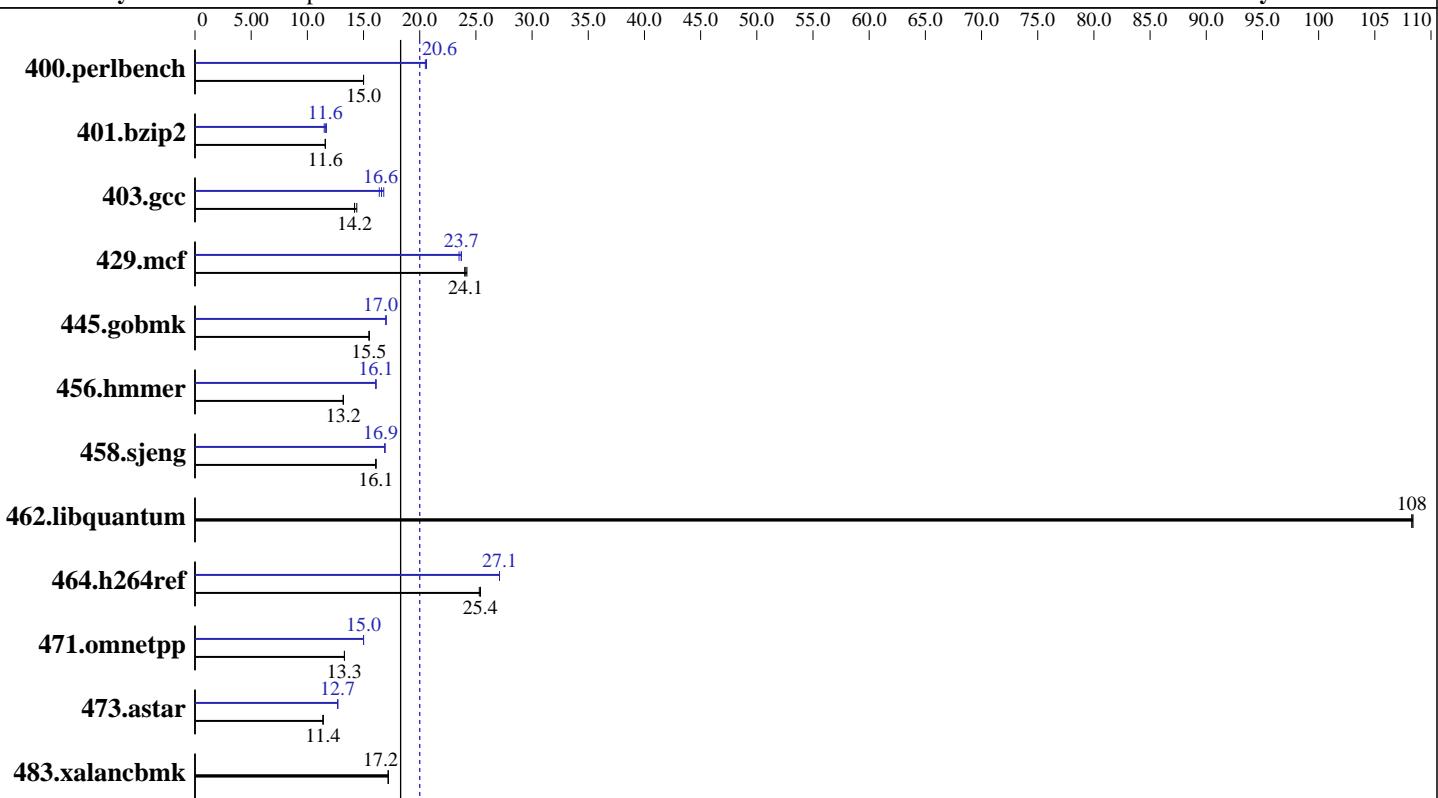
CPU2006 license: 13
Test sponsor: Intel Corporation
Tested by: Intel Corporation

Test date: Apr-2009
Hardware Availability: May-2009
Software Availability: Nov-2008

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	652	15.0	651	15.0	**651**	**15.0**	**475**	**20.6**	476	20.5	475	20.6
401.bzip2	**835**	**11.6**	835	11.6	833	11.6	827	11.7	**829**	**11.6**	839	11.5
403.gcc	560	14.4	568	14.2	**566**	**14.2**	478	16.8	490	16.4	**485**	**16.6**
429.mcf	378	24.2	**378**	**24.1**	379	24.0	384	23.7	**385**	**23.7**	387	23.5
445.gobmk	676	15.5	677	15.5	**677**	**15.5**	**619**	**17.0**	619	17.0	618	17.0
456.hmmer	708	13.2	**708**	**13.2**	708	13.2	581	16.1	**581**	**16.1**	580	16.1
458.sjeng	752	16.1	751	16.1	**751**	**16.1**	716	16.9	716	16.9	**716**	**16.9**
462.libquantum	**191**	**108**	191	108	191	108	**191**	**108**	191	108	191	108
464.h264ref	874	25.3	873	25.4	**873**	**25.4**	**818**	**27.1**	817	27.1	818	27.1
471.omnetpp	471	13.3	471	13.3	**471**	**13.3**	416	15.0	**416**	**15.0**	416	15.0
473.astar	**617**	**11.4**	617	11.4	617	11.4	552	12.7	**552**	**12.7**	552	12.7
483.xalancbmk	400	17.2	401	17.2	**401**	**17.2**	400	17.2	401	17.2	**401**	**17.2**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
Tested systems can be used with Shin-G ATX case,
Antec Truepower Trio power supply TP3-650
Binaries were built on Windows Vista Ultimate (32-bit)
Binaries were built on Windows Vista Ultimate (32-bit)
OMP_NUM_THREADS set to number of logical processors as seen by the OS
KMP_AFFINITY set to physical,0
```

Base Compiler Invocation

C benchmarks:
```
icl -Qvc9 -Qc99
```

C++ benchmarks:
```
icl -Qvc9
```

Base Portability Flags

```
     403.gcc: -DSPEC_CPU_WIN32
  464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword
```

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Base Optimization Flags

C benchmarks:
```
-QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qparallel
-Qpar-runtime-control -Qvec-guard-write /F512000000
```

C++ benchmarks:
```
-QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qcxx-features
/F512000000 shlw32m.lib          -link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks:
```
icl -Qvc9 -Qc99
```

C++ benchmarks:
```
icl -Qvc9
```

Peak Portability Flags

```
       403.gcc: -DSPEC_CPU_WIN32
    464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword
```

Peak Optimization Flags

C benchmarks:

```
400.perlbench: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
               -Qipo -O3 -Qprec-div- -Qansi-alias -Qopt-prefetch
               /F512000000 shlw32m.lib          -link /FORCE:MULTIPLE

    401.bzip2: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
               -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qansi-alias
               /F512000000

      403.gcc: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
               -Qipo -O3 -Qprec-div- /F512000000
```

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Peak Optimization Flags (Continued)

429.mcf: `-QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch /F512000000`

445.gobmk: `-QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O2 -Qprec-div- -Qansi-alias /F512000000`

456.hmmer: `-QxSSE4.1 -Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias /F512000000`

458.sjeng: `-QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll4 /F512000000`

462.libquantum: `basepeak = yes`

464.h264ref: `-QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias /F512000000`

C++ benchmarks:

471.omnetpp: `-QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qansi-alias -Qopt-ra-region-strategy=block /F512000000 shlw32m.lib -link /FORCE:MULTIPLE`

473.astar: `-QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qansi-alias -Qopt-ra-region-strategy=routine /F512000000 shlw32m.lib -link /FORCE:MULTIPLE`

483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.0-win32-revA.20090710.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic11.0-win32-revA.20090710.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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