SPEC® CINT2006 Result
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Intel Corporation

SPECint®_rate2006 = 58.3

Intel DQ45CB motherboard (Intel Core 2 Quad Q8300)

SPECint_rate_base2006 = 56.3
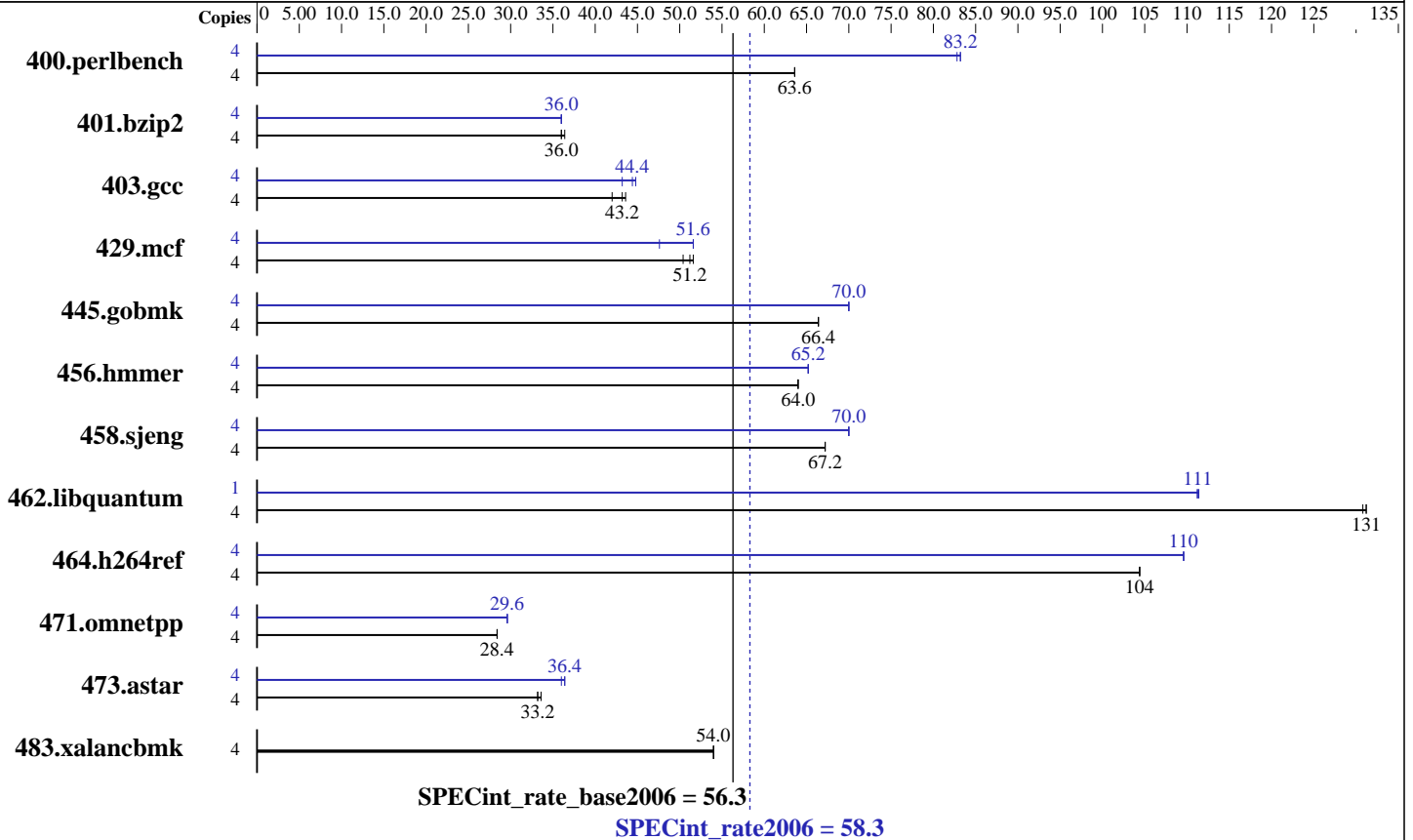
CPU2006 license: 13
Test sponsor: Intel Corporation
Tested by: Intel Corporation

Test date: Apr-2009
Hardware Availability: May-2009
Software Availability: Nov-2008

SPECint_rate_base2006 = 56.3

SPECint_rate2006 = 58.3

Copies	
400.perlbench	4 / 83.2 ; 4 / 63.6
401.bzip2	4 / 36.0 ; 4 / 36.0
403.gcc	4 / 44.4 ; 4 / 43.2
429.mcf	4 / 51.6 ; 4 / 51.2
445.gobmk	4 / 70.0 ; 4 / 66.4
456.hmmer	4 / 65.2 ; 4 / 64.0
458.sjeng	4 / 70.0 ; 4 / 67.2
462.libquantum	1 / 111 ; 4 / 131
464.h264ref	4 / 110 ; 4 / 104
471.omnetpp	4 / 29.6 ; 4 / 28.4
473.astar	4 / 36.4 ; 4 / 33.2
483.xalancbmk	4 / 54.0

Hardware

CPU Name: Intel Core 2 Quad Q8300
CPU Characteristics:
CPU MHz: 2500
FPU: Integrated
CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
CPU(s) orderable: 1 chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 4 MB I+D on chip per chip, 2 MB shared / 2 cores
L3 Cache: None
Other Cache: None
Memory: 4 GB (4x1GB DDR2-800 CL5)
Disk Subsystem: Seagate 320 GB SATA, 7200RPM
Other Hardware: None

Software

Operating System: Windows Vista Ultimate w/ SP1 (64-bit)
Compiler: Intel C++ Compiler Professional 11.0 for IA32
Build 20080930 Package ID: w_cproc_p_11.0.054
Microsoft Visual Studio 2008 (for libraries)
Auto Parallel: Yes
File System: NTFS
System State: Default
Base Pointers: 32-bit
Peak Pointers: 32-bit
Other Software: SmartHeap Library Version 8.1 from
http://www.microquill.com/

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	**616**	**63.6**	616	63.6	616	63.6	4	471	82.8	470	83.2	**470**	**83.2**
401.bzip2	4	**1068**	**36.0**	1074	36.0	1062	36.4	4	1067	36.0	1076	36.0	**1073**	**36.0**
403.gcc	4	770	42.0	**742**	**43.2**	742	43.6	4	747	43.2	**728**	**44.4**	721	44.8
429.mcf	4	**711**	**51.2**	709	51.6	725	50.4	4	769	47.6	**709**	**51.6**	707	51.6
445.gobmk	4	631	66.4	**631**	**66.4**	630	66.4	4	**599**	**70.0**	598	70.0	601	70.0
456.hmmer	4	583	64.0	**583**	**64.0**	583	64.0	4	573	65.2	**573**	**65.2**	573	65.2
458.sjeng	4	**720**	**67.2**	720	67.2	720	67.2	4	**691**	**70.0**	691	70.0	690	70.0
462.libquantum	4	**632**	**131**	633	131	631	131	1	186	111	186	111	**186**	**111**
464.h264ref	4	847	104	846	104	**847**	**104**	4	808	110	807	110	**808**	**110**
471.omnetpp	4	**879**	**28.4**	879	28.4	880	28.4	4	840	29.6	841	29.6	**841**	**29.6**
473.astar	4	**842**	**33.2**	842	33.2	841	33.6	4	**775**	**36.4**	776	36.0	774	36.4
483.xalancbmk	4	512	54.0	511	54.0	**512**	**54.0**	4	512	54.0	511	54.0	**512**	**54.0**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

General Notes

    Tested systems can be used with Shin-G ATX case,
    Antec Truepower Trio power supply TP3-650
    Binaries were built on Windows Vista Ultimate (32-bit)
    Binaries were built on Windows Vista Ultimate (32-bit)
    OMP_NUM_THREADS set to number of logical processors as seen by the OS
    KMP_AFFINITY set to physical,0
    submit disabled for 462.libquantum peak

Base Compiler Invocation

C benchmarks:
    icl -Qvc9 -Qc99

C++ benchmarks:
    icl -Qvc9

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Base Portability Flags

```
       403.gcc: -DSPEC_CPU_WIN32
    464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
   483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword
```

Base Optimization Flags

C benchmarks:
```
    -QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch /F512000000
```

C++ benchmarks:
```
    -QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qcxx-features
    /F512000000 shlw32m.lib          -link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

```
       403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks:
```
    icl -Qvc9 -Qc99
```

C++ benchmarks:
```
    icl -Qvc9
```

Peak Portability Flags

```
       403.gcc: -DSPEC_CPU_WIN32
    464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
   483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword
```

Peak Optimization Flags

C benchmarks:

```
    400.perlbench: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
                   -Qipo -O3 -Qprec-div- -Qansi-alias -Qopt-prefetch
                   /F512000000 shlw32m.lib          -link /FORCE:MULTIPLE
```

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Peak Optimization Flags (Continued)

```
   401.bzip2: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
              -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qansi-alias
              /F512000000

     403.gcc: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
              -Qipo -O3 -Qprec-div- -Qopt-prefetch /F512000000

     429.mcf: -QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch
              /F512000000

   445.gobmk: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
              -Qipo -O2 -Qprec-div- -Qansi-alias /F512000000

   456.hmmer: -QxSSE4.1 -Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias
              /F512000000

   458.sjeng: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
              -Qipo -O3 -Qprec-div- -Qunroll4 /F512000000

462.libquantum: -QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch
              -Qparallel -Qpar-runtime-control -Qvec-guard-write
              /F512000000

  464.h264ref: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
              -Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias /F512000000
```

C++ benchmarks:

```
   471.omnetpp: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
              -Qipo -O3 -Qprec-div- -Qansi-alias
              -Qopt-ra-region-strategy=block /F512000000 shlw32m.lib
                      -link /FORCE:MULTIPLE

   473.astar: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
              -Qipo -O3 -Qprec-div- -Qansi-alias
              -Qopt-ra-region-strategy=routine /F512000000 shlw32m.lib
                      -link /FORCE:MULTIPLE

  483.xalancbmk: basepeak = yes
```

Peak Other Flags

C benchmarks:

```
     403.gcc: -Dalloca=_alloca
```

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The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-win32-revA.20090710.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-win32-revA.20090710.xml`

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