



# SPEC® CINT2006 Result

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## Intel Corporation

SPECint®\_rate2006 = 48.1

Intel DQ45CB motherboard (Intel Core 2 Duo E8600)

SPECint\_rate\_base2006 = 45.7

CPU2006 license: 13

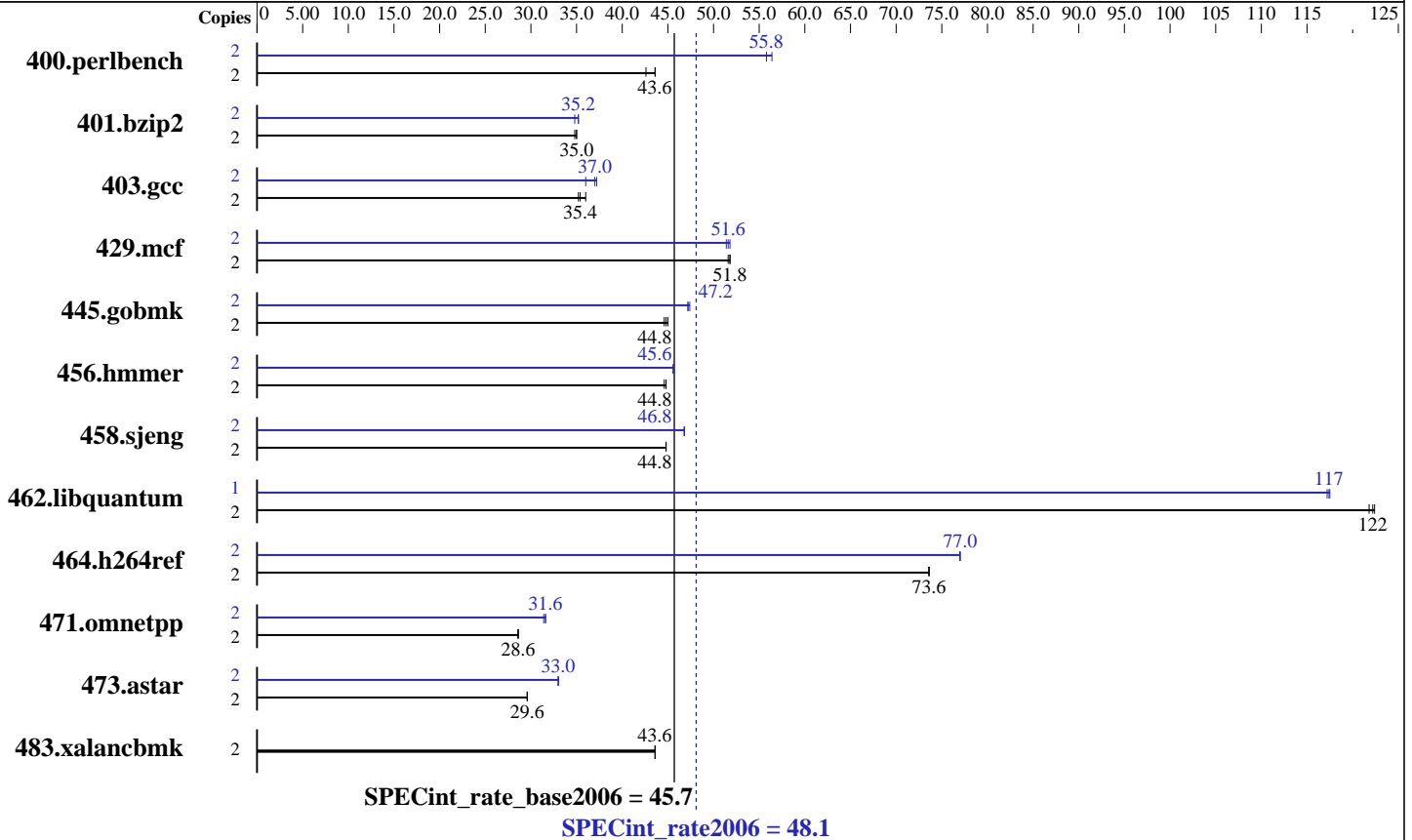
Test date: Apr-2009

Test sponsor: Intel Corporation

Hardware Availability: May-2009

Tested by: Intel Corporation

Software Availability: Nov-2008



### Hardware

CPU Name: Intel Core 2 Duo E8600  
 CPU Characteristics: 3333  
 CPU MHz: 3333  
 FPU: Integrated  
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 6 MB I+D on chip per chip  
 L3 Cache: None  
 Other Cache: None  
 Memory: 4 GB (4x1GB DDR2-800 CL5)  
 Disk Subsystem: Seagate 320 GB SATA, 7200RPM  
 Other Hardware: None

### Software

Operating System: Windows Vista Ultimate w/ SP1 (64-bit)  
 Compiler: Intel C++ Compiler Professional 11.0 for IA32  
 Build 20080930 Package ID: w\_cproc\_p\_11.0.054  
 Microsoft Visual Studio 2008 (for libraries)  
 Auto Parallel: Yes  
 File System: NTFS  
 System State: Default  
 Base Pointers: 32-bit  
 Peak Pointers: 32-bit  
 Other Software: SmartHeap Library Version 8.1 from  
<http://www.microquill.com/>



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	<b>449</b>	<b>43.6</b>	448	43.6	458	42.6	2	<b>350</b>	<b>55.8</b>	346	56.4	351	55.8
401.bzip2	2	556	34.8	552	35.0	<b>553</b>	<b>35.0</b>	2	553	34.8	<b>549</b>	<b>35.2</b>	547	35.2
403.gcc	2	457	35.2	<b>455</b>	<b>35.4</b>	447	36.0	2	<b>435</b>	<b>37.0</b>	446	36.0	433	37.2
429.mcf	2	352	51.8	<b>353</b>	<b>51.8</b>	353	51.6	2	353	51.8	<b>354</b>	<b>51.6</b>	354	51.4
445.gobmk	2	470	44.6	<b>468</b>	<b>44.8</b>	467	45.0	2	<b>445</b>	<b>47.2</b>	443	47.4	445	47.2
456.hammer	2	417	44.8	<b>417</b>	<b>44.8</b>	418	44.6	2	409	45.6	409	45.6	<b>409</b>	<b>45.6</b>
458.sjeng	2	540	44.8	<b>539</b>	<b>44.8</b>	539	44.8	2	517	46.8	<b>517</b>	<b>46.8</b>	517	46.8
462.libquantum	2	340	122	339	122	<b>339</b>	<b>122</b>	1	176	118	177	117	<b>177</b>	<b>117</b>
464.h264ref	2	602	73.6	<b>602</b>	<b>73.6</b>	602	73.6	2	575	77.0	<b>575</b>	<b>77.0</b>	575	77.0
471.omnetpp	2	436	28.6	<b>436</b>	<b>28.6</b>	437	28.6	2	397	31.4	<b>397</b>	<b>31.6</b>	396	31.6
473.astar	2	<b>474</b>	<b>29.6</b>	475	29.6	474	29.6	2	<b>426</b>	<b>33.0</b>	425	33.0	426	33.0
483.xalancbmk	2	316	43.6	316	43.6	<b>316</b>	<b>43.6</b>	2	316	43.6	316	43.6	<b>316</b>	<b>43.6</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.

## General Notes

Tested systems can be used with Shin-G ATX case,  
 Antec Truepower Trio power supply TP3-650  
 Binaries were built on Windows Vista Ultimate (32-bit)  
 Binaries were built on Windows Vista Ultimate (32-bit)  
 OMP\_NUM\_THREADS set to number of logical processors as seen by the OS  
 KMP\_AFFINITY set to physical,0  
 submit disabled for 462.libquantum peak

## Base Compiler Invocation

C benchmarks:  
 icl -Qvc9 -Qc99

C++ benchmarks:  
 icl -Qvc9



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## Base Portability Flags

403.gcc: -DSPEC\_CPU\_WIN32  
464.h264ref: -DSPEC\_CPU\_NO\_INTTYPES -DWIN32  
483.xalancbmk: -Qoption,cpp,--no\_wchar\_t\_keyword

## Base Optimization Flags

C benchmarks:

-QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch /F512000000

C++ benchmarks:

-QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qcxx-features  
/F512000000 shlw32m.lib -link /FORCE:MULTIPLE

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks:

icl -Qvc9 -Qc99

C++ benchmarks:

icl -Qvc9

## Peak Portability Flags

403.gcc: -DSPEC\_CPU\_WIN32  
464.h264ref: -DSPEC\_CPU\_NO\_INTTYPES -DWIN32  
483.xalancbmk: -Qoption,cpp,--no\_wchar\_t\_keyword

## Peak Optimization Flags

C benchmarks:

400.perlbench: -QxSSE4.1(pass 2) -Qprof\_gen(pass 1) -Qprof\_use(pass 2)  
-Qipo -O3 -Qprec-div- -Qansi-alias -Qopt-prefetch  
/F512000000 shlw32m.lib -link /FORCE:MULTIPLE

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## Peak Optimization Flags (Continued)

401.bzp2: -QxSSE4.1(pass 2) -Qprof\_gen(pass 1) -Qprof\_use(pass 2)  
-Qipo -O3 -Qprec-div- -Qopt-prefetch -Qansi-alias  
/F512000000

403.gcc: -QxSSE4.1(pass 2) -Qprof\_gen(pass 1) -Qprof\_use(pass 2)  
-Qipo -O3 -Qprec-div- -Qopt-prefetch /F512000000

429.mcf: -QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch  
/F512000000

445.gobmk: -QxSSE4.1(pass 2) -Qprof\_gen(pass 1) -Qprof\_use(pass 2)  
-Qipo -O2 -Qprec-div- -Qansi-alias /F512000000

456.hmmer: -QxSSE4.1 -Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias  
/F512000000

458.sjeng: -QxSSE4.1(pass 2) -Qprof\_gen(pass 1) -Qprof\_use(pass 2)  
-Qipo -O3 -Qprec-div- -Qunroll4 /F512000000

462.libquantum: -QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch  
-Qparallel -Qpar-runtime-control -Qvec-guard-write  
/F512000000

464.h264ref: -QxSSE4.1(pass 2) -Qprof\_gen(pass 1) -Qprof\_use(pass 2)  
-Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias /F512000000

C++ benchmarks:

471.omnetpp: -QxSSE4.1(pass 2) -Qprof\_gen(pass 1) -Qprof\_use(pass 2)  
-Qipo -O3 -Qprec-div- -Qansi-alias  
-Qopt-ra-region-strategy=block /F512000000 shlw32m.lib  
-link /FORCE:MULTIPLE

473.astar: -QxSSE4.1(pass 2) -Qprof\_gen(pass 1) -Qprof\_use(pass 2)  
-Qipo -O3 -Qprec-div- -Qansi-alias  
-Qopt-ra-region-strategy=routine /F512000000 shlw32m.lib  
-link /FORCE:MULTIPLE

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca



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The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-win32-revA.20090710.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-win32-revA.20090710.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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