



SPEC[®] CFP2006 Result

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Intel Corporation

SPECfp[®]_rate2006 = 25.6

Intel DQ45CB motherboard (Intel Pentium E5200)

SPECfp_rate_base2006 = 24.9

CPU2006 license: 13

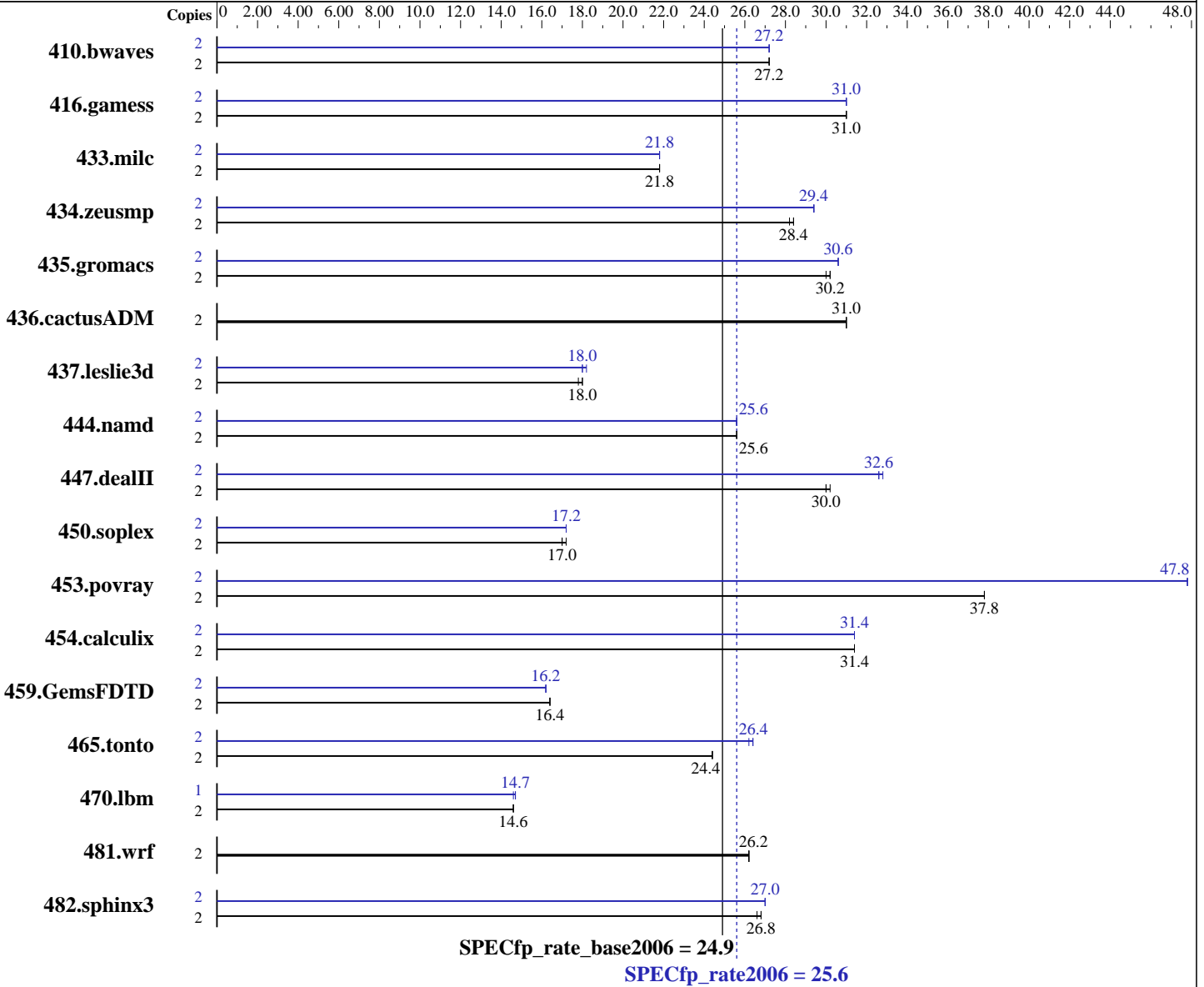
Test sponsor: Intel Corporation

Tested by: Intel Corporation

Test date: Apr-2009

Hardware Availability: May-2009

Software Availability: Nov-2008



Hardware

CPU Name: Intel Pentium E5200
 CPU Characteristics:
 CPU MHz: 2500
 FPU: Integrated
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 2 MB I+D on chip per chip

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Software

Operating System: Windows Vista Ultimate w/ SP1 (64-bit)
 Compiler: Intel C++ Compiler Professional 11.0 for IA32
 Build 20080930 Package ID: w_cproc_p_11.0.054
 Intel Visual Fortran Compiler Professional 11.0 for IA32
 Build 20080930 Package ID: w_cprof_p_11.0.054
 Microsoft Visual Studio 2008 (for libraries)
 Auto Parallel: No
 File System: NTFS

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L3 Cache: None
 Other Cache: None
 Memory: 4 GB (4x1GB DDR2-800 CL5)
 Disk Subsystem: Seagate 320 GB SATA, 7200RPM
 Other Hardware: None

System State: Default
 Base Pointers: 32-bit
 Peak Pointers: 32-bit
 Other Software: SmartHeap Library Version 8.1 from <http://www.microquill.com/>

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	2	1002	27.2	1000	27.2	1000	27.2	2	998	27.2	1000	27.2	999	27.2
416.gamess	2	1266	31.0	1266	31.0	1266	31.0	2	1261	31.0	1261	31.0	1261	31.0
433.milc	2	842	21.8	842	21.8	842	21.8	2	841	21.8	841	21.8	841	21.8
434.zeusmp	2	643	28.4	643	28.4	644	28.2	2	618	29.4	619	29.4	620	29.4
435.gromacs	2	473	30.2	475	30.0	473	30.2	2	468	30.6	467	30.6	467	30.6
436.cactusADM	2	773	31.0	771	31.0	772	31.0	2	773	31.0	771	31.0	772	31.0
437.leslie3d	2	1043	18.0	1048	18.0	1051	17.8	2	1038	18.2	1040	18.0	1041	18.0
444.namd	2	626	25.6	626	25.6	626	25.6	2	626	25.6	626	25.6	626	25.6
447.dealII	2	761	30.0	760	30.2	761	30.0	2	700	32.6	696	32.8	703	32.6
450.soplex	2	978	17.0	976	17.0	975	17.2	2	973	17.2	971	17.2	973	17.2
453.povray	2	282	37.8	282	37.8	281	37.8	2	223	47.8	223	47.8	223	47.8
454.calculix	2	527	31.4	527	31.4	527	31.4	2	526	31.4	526	31.4	526	31.4
459.GemsFDTD	2	1290	16.4	1291	16.4	1295	16.4	2	1318	16.2	1314	16.2	1312	16.2
465.tonto	2	805	24.4	807	24.4	805	24.4	2	751	26.2	748	26.4	748	26.4
470.lbm	2	1883	14.6	1884	14.6	1883	14.6	1	938	14.7	938	14.6	938	14.7
481.wrf	2	853	26.2	852	26.2	851	26.2	2	853	26.2	852	26.2	851	26.2
482.sphinx3	2	1461	26.6	1459	26.8	1456	26.8	2	1448	27.0	1446	27.0	1445	27.0

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

General Notes

Tested systems can be used with Shin-G ATX case,
 Antec Truepower Trio power supply TP3-650
 Binaries were built on Windows Vista Ultimate (32-bit)

Base Compiler Invocation

C benchmarks:
 icl -Qvc9 -Qc99

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Base Compiler Invocation (Continued)

C++ benchmarks:

icl -Qvc9

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icl -Qvc9 -Qc99 ifort

Base Portability Flags

436.cactusADM: -Qlowercase /assume:underscore
444.namd: -TP
447.dealII: -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
453.povray: -DSPEC_CPU_WINDOWS_ICL
454.calculix: -DSPEC_CPU_NOZMODIFIER -Qlowercase
481.wrf: -DSPEC_CPU_WINDOWS_ICL

Base Optimization Flags

C benchmarks:

-QxSSSE3 -Qipo -O3 -Qprec-div- -Qopt-prefetch /F1000000000

C++ benchmarks:

-QxSSSE3 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qcxx-features
/F1000000000 shlw32m.lib -link /FORCE:MULTIPLE

Fortran benchmarks:

-QxSSSE3 -Qipo -O3 -Qprec-div- -Qopt-prefetch /F1000000000

Benchmarks using both Fortran and C:

-QxSSSE3 -Qipo -O3 -Qprec-div- -Qopt-prefetch /F1000000000

Peak Compiler Invocation

C benchmarks:

icl -Qvc9 -Qc99

C++ benchmarks:

icl -Qvc9

Fortran benchmarks:

ifort

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Peak Compiler Invocation (Continued)

Benchmarks using both Fortran and C:

icl -Qvc9 -Qc99 ifort

Peak Portability Flags

436.cactusADM: -Qlowercase /assume:underscore
 444.namd: -TP
 447.dealII: -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
 453.povray: -DSPEC_CPU_WINDOWS_ICL
 454.calculix: -DSPEC_CPU_NOZMODIFIER -Qlowercase
 481.wrf: -DSPEC_CPU_WINDOWS_ICL

Peak Optimization Flags

C benchmarks:

433.milc: -QxSSSE3(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Oa /F1000000000

470.lbm: -QxSSSE3 -Qipo -O3 -Qprec-div- -Qopt-prefetch
/F1000000000

482.sphinx3: -QxSSSE3 -Qipo -O3 -Qprec-div- -Qunroll2 /F1000000000

C++ benchmarks:

444.namd: -QxSSSE3(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Oa /F1000000000 shlw32m.lib
-link /FORCE:MULTIPLE

447.dealII: -QxSSSE3(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias
-Qscalar-rep- /F1000000000 shlw32m.lib
-link /FORCE:MULTIPLE

450.soplex: -QxSSSE3(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- /F1000000000 shlw32m.lib
-link /FORCE:MULTIPLE

453.povray: -QxSSSE3(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qunroll4 -Qansi-alias /F1000000000
shlw32m.lib -link /FORCE:MULTIPLE

Fortran benchmarks:

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Peak Optimization Flags (Continued)

410.bwaves: -QxSSSE3 -Qipo -O3 -Qprec-div- -Qopt-prefetch /F1000000000

416.gamess: -QxSSSE3(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll2 -Ob0 -Qansi-alias -Qscalar-rep- /F1000000000

434.zeusmp: -QxSSSE3(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- /F1000000000

437.leslie3d: -QxSSSE3(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qopt-prefetch /F1000000000

459.GemsFDTD: -QxSSSE3(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll2 -Ob0 -Qopt-prefetch /F1000000000

465.tonto: -QxSSSE3(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll4 -Qauto /F1000000000

Benchmarks using both Fortran and C:

435.gromacs: -QxSSSE3(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qopt-prefetch /F1000000000

436.cactusADM: basepeak = yes

454.calculix: -QxSSSE3 -Qipo -O3 -Qprec-div- /F1000000000

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-win32-revA.20090710.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-win32-revA.20090710.xml>

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For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.

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