SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Intel Corporation

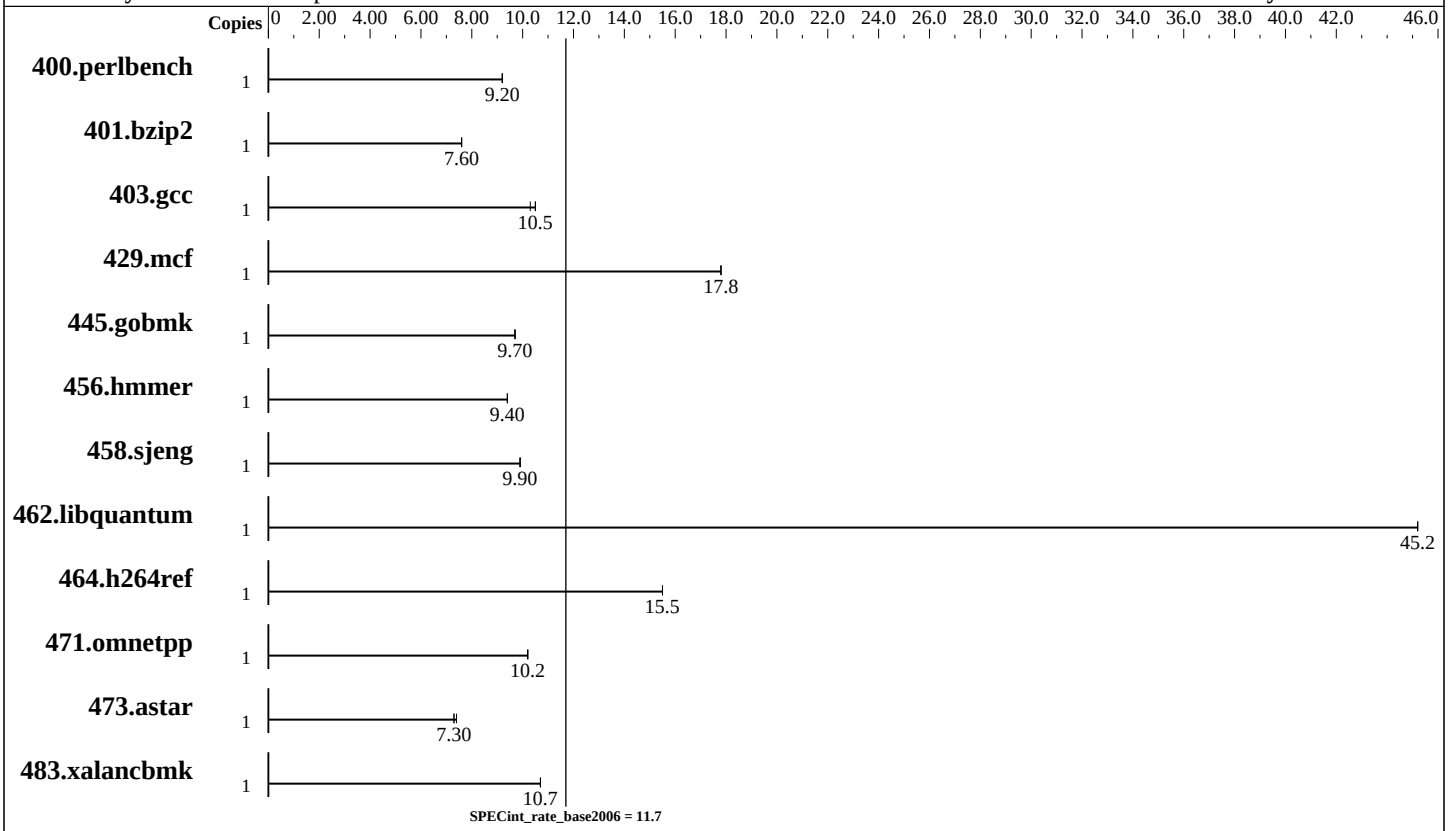
Acer Aspire 3810T (Intel Core 2 Solo ULV SU3500)

SPECint®_rate2006 = Not Run

SPECint_rate_base2006 = 11.7

CPU2006 license: 13
Test sponsor: Intel Corporation
Tested by: Intel Corporation

Test date: May-2009
Hardware Availability: May-2009
Software Availability: Nov-2008



Hardware

CPU Name:	Intel Core 2 Solo ULV SU3500
CPU Characteristics:	
CPU MHz:	1400
FPU:	Integrated
CPU(s) enabled:	1 core, 1 chip, 1 core/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per chip
Secondary Cache:	3 MB I+D on chip per chip
L3 Cache:	None
Other Cache:	None
Memory:	2 GB (2x1GB DDR3-1066 CL7; bios sets it to DDR2-800 CL5)
Disk Subsystem:	ST9250315AS 250 GB SATA, 5400RPM
Other Hardware:	None

Software

Operating System:	Windows Vista Home Premium w/ SP1 (32-bit)
Compiler:	Intel C++ Compiler Professional 11.0 for IA32 Build 20080930 Package ID: w_cproc_p_11.0.054 Microsoft Visual Studio 2008 (for libraries)
Auto Parallel:	No
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	Not Applicable
Other Software:	SmartHeap Library Version 8.1 from http://www.microquill.com/

SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Intel Corporation

Acer Aspire 3810T (Intel Core 2 Solo ULV SU3500)

SPECint_rate2006 = Not Run

SPECint_rate_base2006 = 11.7

CPU2006 license: 13
Test sponsor: Intel Corporation
Tested by: Intel Corporation

Test date: May-2009
Hardware Availability: May-2009
Software Availability: Nov-2008

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	1	1058	9.20	1060	9.20	**1059**	**9.20**							
401.bzip2	1	1264	7.60	1262	7.60	**1263**	**7.60**							
403.gcc	1	784	10.3	**767**	**10.5**	765	10.5							
429.mcf	1	513	17.8	**513**	**17.8**	513	17.8							
445.gobmk	1	**1087**	**9.70**	1087	9.70	1086	9.70							
456.hmmer	1	997	9.40	996	9.40	**996**	**9.40**							
458.sjeng	1	1222	9.90	1223	9.90	**1222**	**9.90**							
462.libquantum	1	458	45.2	**458**	**45.2**	458	45.2							
464.h264ref	1	**1426**	**15.5**	1426	15.5	1427	15.5							
471.omnetpp	1	616	10.2	615	10.2	**616**	**10.2**							
473.astar	1	955	7.40	**955**	**7.30**	955	7.30							
483.xalancbmk	1	**644**	**10.7**	644	10.7	644	10.7							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
```

General Notes

```
The system bus runs at 800 MHz
Binaries were built on Windows Vista Ultimate (32-bit)
```

Base Compiler Invocation

C benchmarks:

```
icl -Qvc9 -Qc99
```

C++ benchmarks:

```
icl -Qvc9
```

Base Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`
483.xalancbmk: `-Qoption,cpp,--no_wchar_t_keyword`

Base Optimization Flags

C benchmarks:
```
-QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch /F512000000
```

C++ benchmarks:
```
-QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qcxx-features
/F512000000 shlw32m.lib            -link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-win32-revA.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-win32-revA.xml`

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Wed Jul 23 01:29:50 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 23 June 2009.