



SPEC® CINT2006 Result

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Intel Corporation

SPECint®2006 = 26.1

Lenovo T400 (Intel Core 2 Duo T9900)

SPECint_base2006 = 23.7

CPU2006 license: 13

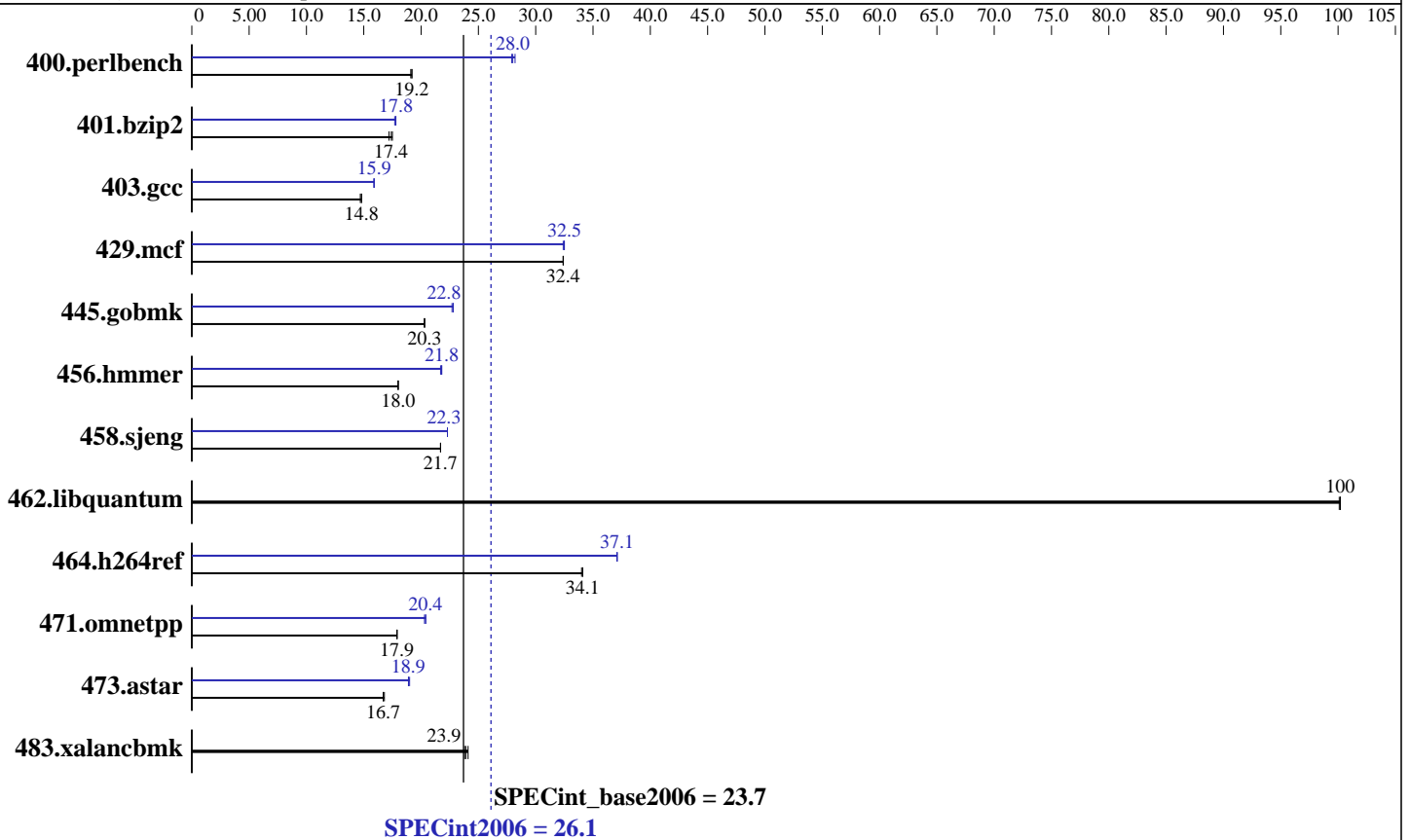
Test date: Mar-2009

Test sponsor: Intel Corporation

Hardware Availability: Mar-2009

Tested by: Intel Corporation

Software Availability: Nov-2008



Hardware

CPU Name: Intel Core 2 Duo T9900
 CPU Characteristics:
 CPU MHz: 3066
 FPU: Integrated
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 6 MB I+D on chip per chip
 L3 Cache: None
 Other Cache: None
 Memory: 2 GB (2x1GB Micron DDR3-1066 CL7)
 Disk Subsystem: Hitachi HTS722020K9SA00 200GB SATA, 7200RPM
 Other Hardware: None

Software

Operating System: Windows XP Professional w/ SP2 (64-bit)
 Compiler: Intel C++ Compiler Professional 11.0 for IA32
 Build 20080930 Package ID: w_cproc_p_11.0.054
 Microsoft Visual Studio 2008 (for libraries)
 Auto Parallel: Yes
 File System: NTFS
 System State: Default
 Base Pointers: 32-bit
 Peak Pointers: 32-bit
 Other Software: SmartHeap Library Version 8.1 from
<http://www.microquill.com/>



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	512	19.1	510	19.2	<u>510</u>	<u>19.2</u>	346	28.2	<u>349</u>	<u>28.0</u>	351	27.9
401.bzip2	560	17.2	<u>555</u>	<u>17.4</u>	552	17.5	<u>543</u>	<u>17.8</u>	544	17.7	543	17.8
403.gcc	546	14.7	545	14.8	<u>546</u>	<u>14.8</u>	505	15.9	505	15.9	<u>505</u>	<u>15.9</u>
429.mcf	282	32.4	281	32.4	<u>281</u>	<u>32.4</u>	282	32.4	281	32.5	<u>281</u>	<u>32.5</u>
445.gobmk	<u>516</u>	<u>20.3</u>	516	20.3	516	20.3	<u>461</u>	<u>22.8</u>	461	22.8	461	22.7
456.hammer	<u>517</u>	<u>18.0</u>	518	18.0	517	18.0	428	21.8	<u>428</u>	<u>21.8</u>	430	21.7
458.sjeng	558	21.7	<u>558</u>	<u>21.7</u>	558	21.7	542	22.3	<u>542</u>	<u>22.3</u>	542	22.3
462.libquantum	207	100	207	100	<u>207</u>	<u>100</u>	207	100	207	100	<u>207</u>	<u>100</u>
464.h264ref	650	34.0	<u>649</u>	<u>34.1</u>	649	34.1	597	37.1	<u>597</u>	<u>37.1</u>	597	37.1
471.omnetpp	350	17.9	<u>349</u>	<u>17.9</u>	349	17.9	<u>307</u>	<u>20.4</u>	307	20.4	307	20.3
473.astar	421	16.7	<u>419</u>	<u>16.7</u>	419	16.8	369	19.0	<u>371</u>	<u>18.9</u>	372	18.9
483.xalancbmk	287	24.1	289	23.8	<u>289</u>	<u>23.9</u>	287	24.1	289	23.8	<u>289</u>	<u>23.9</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

The system bus runs at 1066 MHz
 Binaries were built on Windows Vista Ultimate (32-bit)
 OMP_NUM_THREADS set to number of logical processors as seen by the OS
 KMP_AFFINITY set to physical,0

Base Compiler Invocation

C benchmarks:
 icl -Qvc9 -Qc99

C++ benchmarks:
 icl -Qvc9

Base Portability Flags

403.gcc: -DSPEC_CPU_WIN32
 464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
 483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword



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Base Optimization Flags

C benchmarks:

-QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qparallel
-Qpar-runtime-control -Qvec-guard-write /F512000000

C++ benchmarks:

-QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qcxx-features
/F512000000 shlw32m.lib -link /FORCE:MULTIPLE

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks:

icl -Qvc9 -Qc99

C++ benchmarks:

icl -Qvc9

Peak Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
483.xalanbmk: -Qoption,cpp,--no_wchar_t_keyword

Peak Optimization Flags

C benchmarks:

400.perlbench: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qansi-alias -Qopt-prefetch
/F512000000 shlw32m.lib -link /FORCE:MULTIPLE

401.bzip2: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qopt-prefetch -Qansi-alias
/F512000000

403.gcc: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- /F512000000

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Peak Optimization Flags (Continued)

429.mcf: -QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch /F512000000

445.gobmk: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O2 -Qprec-div- -Qansi-alias /F512000000

456.hmmcr: -QxSSE4.1 -Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias /F512000000

458.sjeng: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll4 /F512000000

462.libquantum: basepeak = yes

464.h264ref: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias /F512000000

C++ benchmarks:

471.omnetpp: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qansi-alias -Qopt-ra-region-strategy=block /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

473.astar: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qansi-alias -Qopt-ra-region-strategy=routine /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-win32-revA.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-win32-revA.xml>



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Tested with SPEC CPU2006 v1.1.
Report generated on Wed Jul 23 01:27:01 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 23 June 2009.