SPEC® CINT2006 Result
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Fujitsu

CELSIUS M470, Intel Xeon W3503

$SPECint®\_rate2006 =$ 45.9

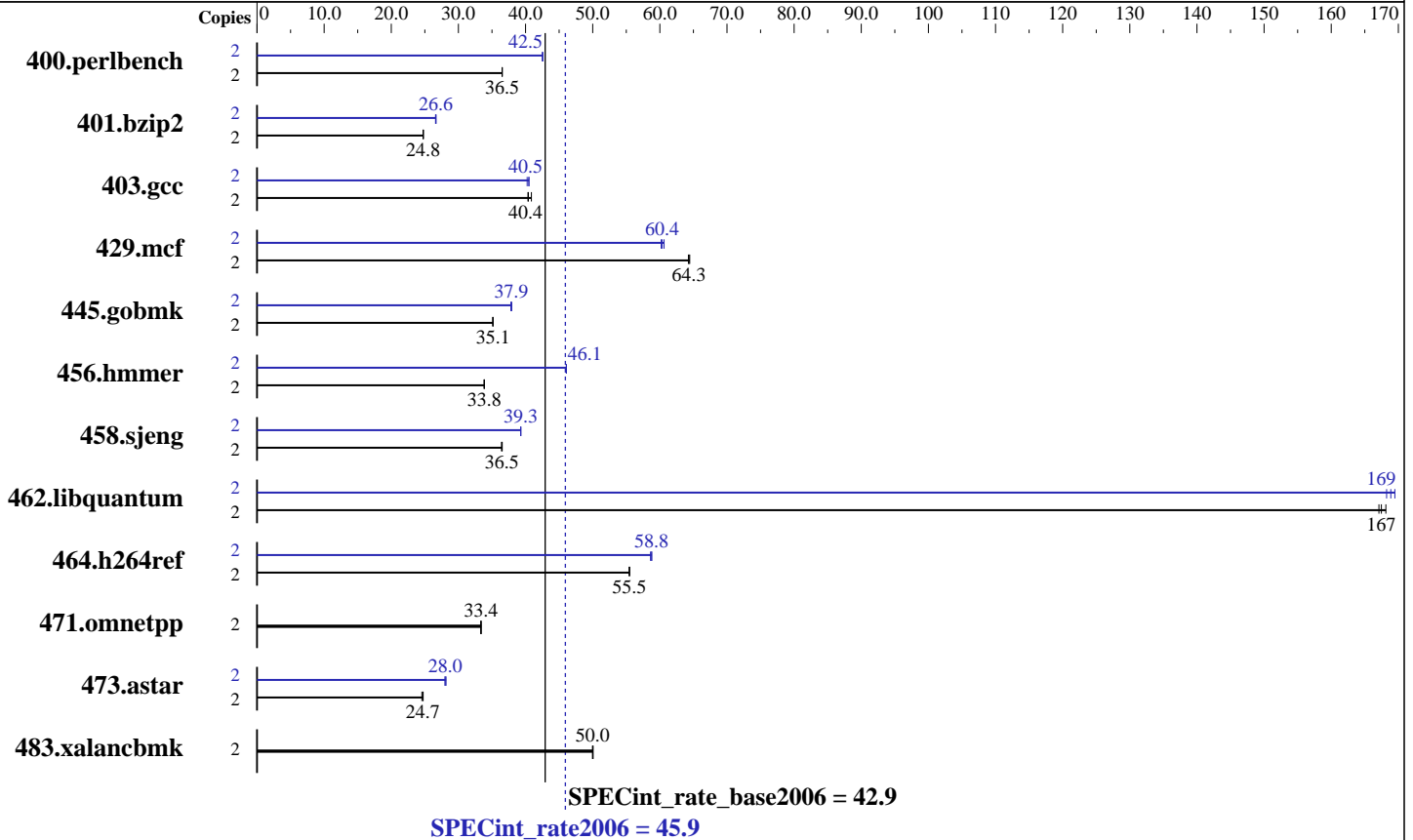$SPECint\_rate\_base2006 =$ 42.9

CPU2006 license: 19

Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Jun-2009

Hardware Availability: Apr-2009

Software Availability: Feb-2009

Benchmark	Copies	Rate (peak)	Rate (base)
400.perlbench	2	42.5	36.5
401.bzip2	2	26.6	24.8
403.gcc	2	40.5	40.4
429.mcf	2	60.4	64.3
445.gobmk	2	37.9	35.1
456.hmmer	2	46.1	33.8
458.sjeng	2	39.3	36.5
462.libquantum	2	169	167
464.h264ref	2	58.8	55.5
471.omnetpp	2		33.4
473.astar	2	28.0	24.7
483.xalancbmk	2		50.0

SPECint_rate_base2006 = 42.9

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Hardware

CPU Name: Intel Xeon W3503

CPU Characteristics:

CPU MHz: 2400

FPU: Integrated

CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip

CPU(s) orderable: 1 chip

Primary Cache: 32 KB I + 32 KB D on chip per core

Secondary Cache: 256 KB I+D on chip per core

L3 Cache: 4 MB I+D on chip per chip

Other Cache: None

Memory: 12 GB (6x2 GB PC3 10600E, 2 rank, CL9, ECC, running at 1066 MHz)

Disk Subsystem: 1 x SATA II, 400 GB, 7200 rpm

Other Hardware: None

Software

Operating System: SuSe Linux Enterprise Server 10 (x86_64) SP2, kernel 2.6.16.60-0.21-smp

Compiler: Intel C++ Compiler 11.0 for Linux Build 20090131 Package ID: l_cproc_p_11.0.080

Auto Parallel: No

File System: ext3

System State: Multi-User Run Level 3

Base Pointers: 32-bit

Peak Pointers: 32/64-bit

Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502

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Results Table

Benchmark	Base Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Peak Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	**535**	**36.5**	535	36.5	535	36.5	2	460	42.5	**459**	**42.5**	459	42.6
401.bzip2	2	779	24.8	**780**	**24.8**	780	24.7	2	724	26.7	**725**	**26.6**	726	26.6
403.gcc	2	394	40.9	399	40.4	**398**	**40.4**	2	400	40.2	397	40.5	**398**	**40.5**
429.mcf	2	**283**	**64.3**	284	64.3	283	64.5	2	**302**	**60.4**	303	60.2	301	60.6
445.gobmk	2	597	35.1	597	35.1	**597**	**35.1**	2	553	37.9	555	37.8	**554**	**37.9**
456.hmmer	2	551	33.8	**551**	**33.8**	551	33.9	2	**405**	**46.1**	405	46.1	405	46.1
458.sjeng	2	664	36.4	663	36.5	**663**	**36.5**	2	616	39.3	**616**	**39.3**	616	39.3
462.libquantum	2	**247**	**167**	246	168	248	167	2	**245**	**169**	245	169	246	168
464.h264ref	2	**798**	**55.5**	799	55.4	797	55.5	2	755	58.6	**753**	**58.8**	753	58.8
471.omnetpp	2	375	33.3	374	33.4	**375**	**33.4**	2	375	33.3	374	33.4	**375**	**33.4**
473.astar	2	**568**	**24.7**	572	24.6	567	24.7	2	**501**	**28.0**	499	28.2	501	28.0
483.xalancbmk	2	276	50.1	276	50.0	**276**	**50.0**	2	276	50.1	276	50.0	**276**	**50.0**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Platform Notes

BIOS configuration:
Memory speed set to "Max Performance" (Switch in "Advanced Memory Options")

General Notes

For information about Fujitsu please visit:
http://www.fujitsu.com

Base Compiler Invocation

C benchmarks:
    icc

C++ benchmarks:
    icpc

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Base Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3 -opt-prefetch
```

C++ benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc
```

```
401.bzip2: /opt/intel/Compiler/11.0/080/bin/intel64/icc
```

```
456.hmmer: /opt/intel/Compiler/11.0/080/bin/intel64/icc
```

```
458.sjeng: /opt/intel/Compiler/11.0/080/bin/intel64/icc
```

C++ benchmarks (except as noted below):
```
icpc
```

```
473.astar: /opt/intel/Compiler/11.0/080/bin/intel64/icpc
```

Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
```

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CPU2006 license: 19		**Test date:**	Jun-2009
Test sponsor:	Fujitsu	**Hardware Availability:**	Apr-2009
Tested by:	Fujitsu	**Software Availability:**	Feb-2009

Peak Portability Flags (Continued)

```
       458.sjeng: -DSPEC_CPU_LP64
   462.libquantum: -DSPEC_CPU_LINUX
        473.astar: -DSPEC_CPU_LP64
    483.xalancbmk: -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
    400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                   -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                   -prof-use(pass 2) -ansi-alias -opt-prefetch

       401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                   -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                   -prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

          403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc
                   -opt-malloc-options=3

          429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                   -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                   -prof-use(pass 2) -opt-prefetch

       445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
                   -ipo -no-prec-div -ansi-alias

       456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
                   -ansi-alias -auto-ilp32

       458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                   -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                   -prof-use(pass 2) -unroll4 -auto-ilp32

   462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static
                   -opt-malloc-options=3 -opt-prefetch

      464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                   -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                   -prof-use(pass 2) -unroll2 -ansi-alias
```

C++ benchmarks:

```
      471.omnetpp: basepeak = yes

        473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                   -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
                   -ansi-alias -opt-ra-region-strategy=routine -auto-ilp32
                   -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap64
```

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Peak Optimization Flags (Continued)

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.16.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.16.xml

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