



SPEC® CINT2006 Result

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IBM Corporation

SPECint®2006 = 26.8

IBM System x iDataPlex dx360 (Intel Xeon E5462)

SPECint_base2006 = 23.6

CPU2006 license: 11

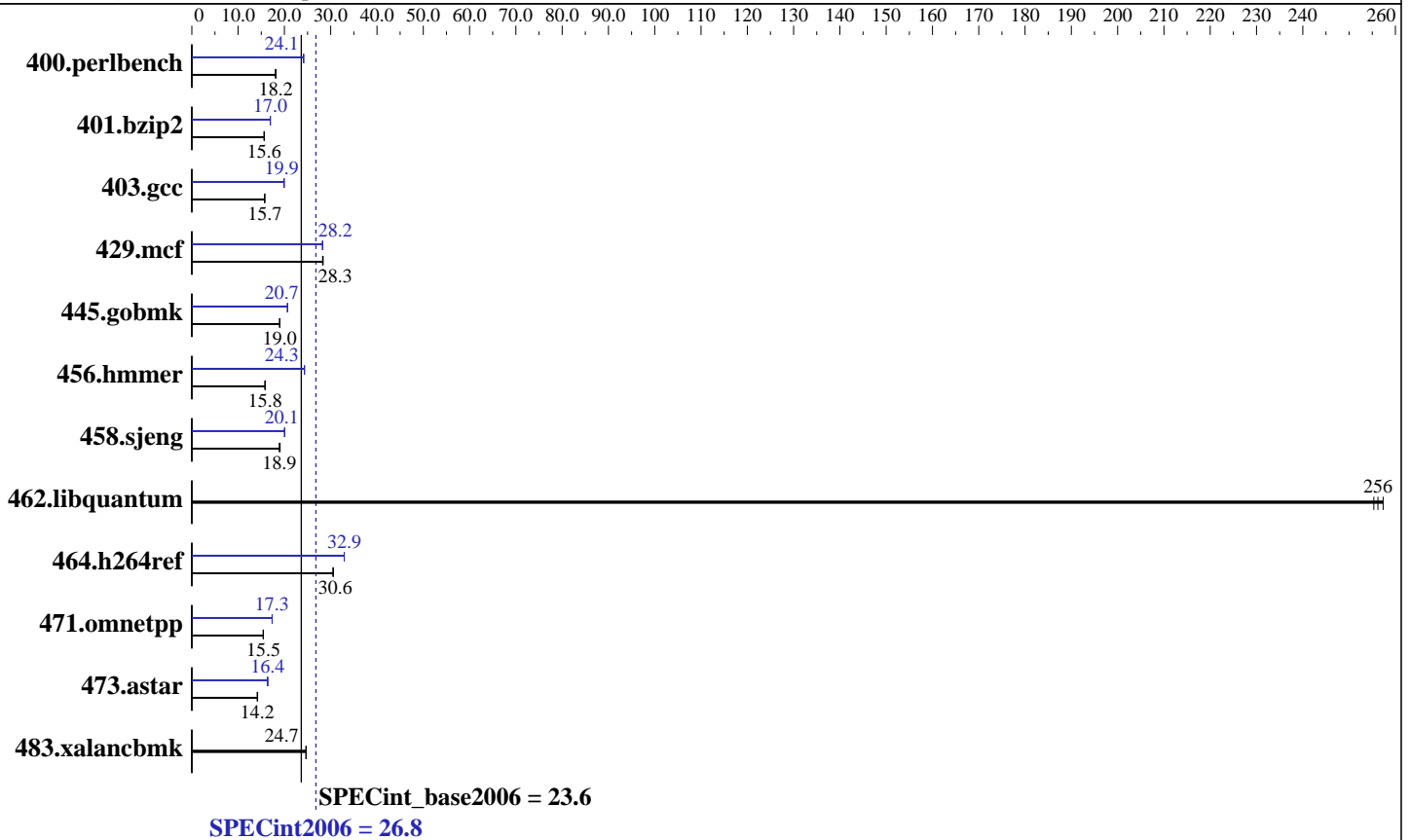
Test date: Apr-2009

Test sponsor: IBM Corporation

Hardware Availability: Nov-2008

Tested by: IBM Corporation

Software Availability: Feb-2009



Hardware

CPU Name: Intel Xeon E5462
 CPU Characteristics: 1600MHz system bus
 CPU MHz: 2800
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (8 x 2 GB DDR2-6400F ECC)
 Disk Subsystem: 1 x 250 GB SATA, 7200 RPM
 Other Hardware: None

Software

Operating System: SuSE Linux Enterprise Server 10(x86_64) SP2, Kernel 2.6.16.60-0.21-smp
 Compiler: Intel C++ Compiler 11.0 for Linux Build 20080930 Package ID: l_cproc_p_11.0.066
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	537	18.2	542	18.0	<u>537</u>	<u>18.2</u>	405	24.1	<u>405</u>	<u>24.1</u>	404	24.2
401.bzip2	616	15.7	619	15.6	<u>617</u>	<u>15.6</u>	569	17.0	<u>569</u>	<u>17.0</u>	570	16.9
403.gcc	510	15.8	<u>511</u>	<u>15.7</u>	513	15.7	404	19.9	<u>404</u>	<u>19.9</u>	404	19.9
429.mcf	323	28.2	321	28.4	<u>322</u>	<u>28.3</u>	323	28.3	323	28.2	<u>323</u>	<u>28.2</u>
445.gobmk	552	19.0	<u>552</u>	<u>19.0</u>	553	19.0	508	20.7	507	20.7	<u>508</u>	<u>20.7</u>
456.hammer	590	15.8	590	15.8	<u>590</u>	<u>15.8</u>	384	24.3	<u>384</u>	<u>24.3</u>	384	24.3
458.sjeng	634	19.1	<u>639</u>	<u>18.9</u>	640	18.9	<u>603</u>	<u>20.1</u>	605	20.0	602	20.1
462.libquantum	<u>80.9</u>	<u>256</u>	80.5	257	81.1	255	<u>80.9</u>	<u>256</u>	80.5	257	81.1	255
464.h264ref	<u>724</u>	<u>30.6</u>	724	30.6	726	30.5	672	32.9	<u>672</u>	<u>32.9</u>	672	32.9
471.omnetpp	<u>404</u>	<u>15.5</u>	406	15.4	404	15.5	360	17.3	360	17.4	<u>360</u>	<u>17.3</u>
473.astar	494	14.2	<u>495</u>	<u>14.2</u>	497	14.1	<u>428</u>	<u>16.4</u>	431	16.3	428	16.4
483.xalancbmk	279	24.7	<u>279</u>	<u>24.7</u>	280	24.6	279	24.7	<u>279</u>	<u>24.7</u>	280	24.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

Hardware Sector Prefetch Enable and Adjacent Sector Prefetch Enable
OMP_NUM_THREADS set to number of processors
KMP_AFFINITY set to "physical,0"

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xSSE4.1 -ipo -O3 -no-prec-div -static -parallel
-par-runtime-control -opt-prefetch

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Base Optimization Flags (Continued)

C++ benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/Compiler/11.0/066/bin/intel64/icc

456.hmmer: /opt/intel/Compiler/11.0/066/bin/intel64/icc

C++ benchmarks:

icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32

401.bzip2: -DSPEC_CPU_LP64

456.hmmer: -DSPEC_CPU_LP64

462.libquantum: -DSPEC_CPU_LINUX

483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -ansi-alias -opt-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -auto-ilp32 -opt-prefetch
-ansi-alias

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Peak Optimization Flags (Continued)

403.gcc: -xSSE4.1 -ipo -O3 -no-prec-div -static -inline-alloc
-opt-malloc-options=3

429.mcf: -xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -O2 -ipo
-no-prec-div -ansi-alias

456.hmmer: -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2
-ansi-alias -auto-ilp32

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll4

462.libquantum: basepeak = yes

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.17.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.17.xml>



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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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