



# SPEC<sup>®</sup> CINT2006 Result

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## Fujitsu

### SPECint<sup>®</sup>\_rate2006 = 248

PRIMERGY RX200 S5, Intel Xeon X5560, 2.80 GHz

### SPECint\_rate\_base2006 = 230

CPU2006 license: 19

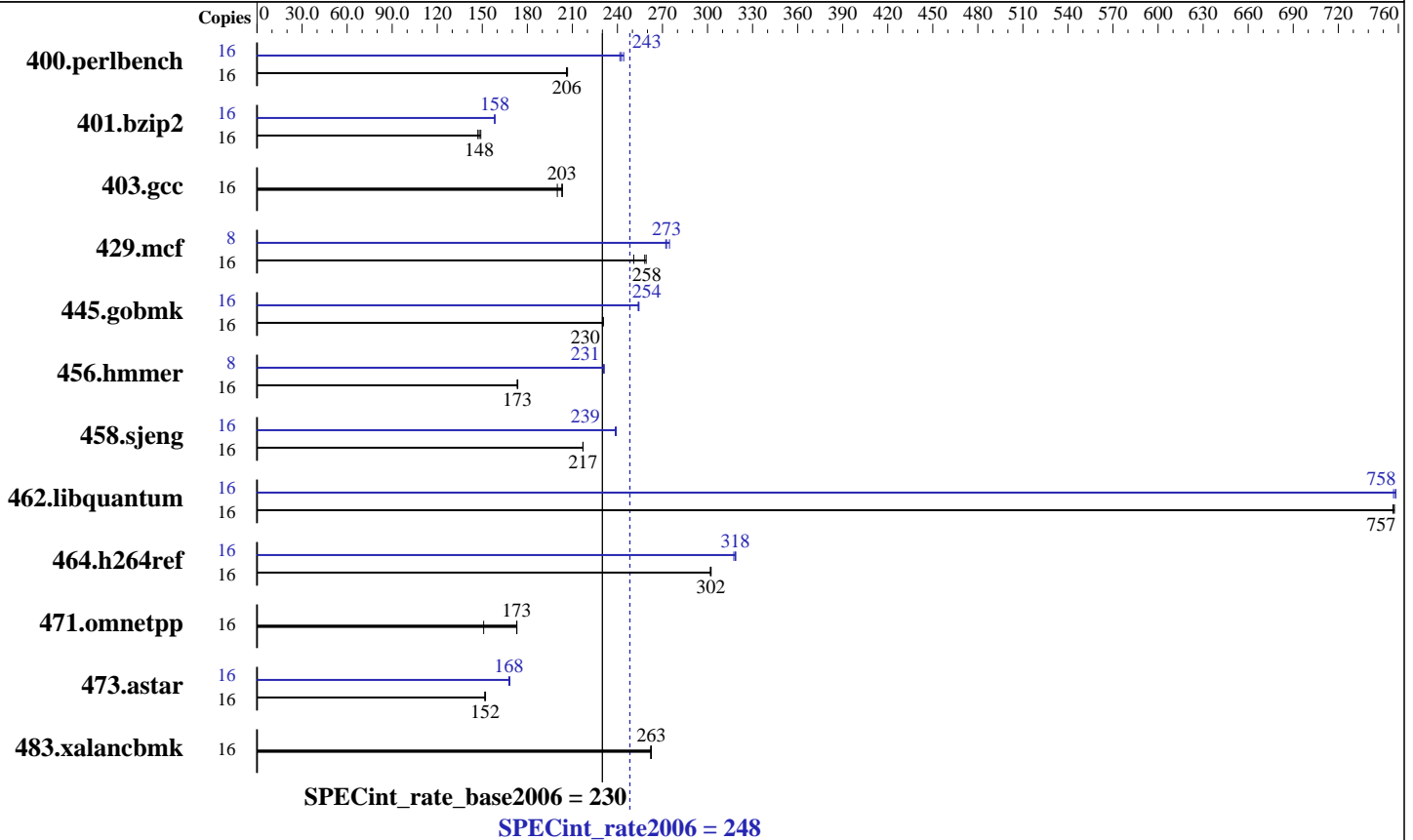
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Apr-2009

Hardware Availability: Apr-2009

Software Availability: Feb-2009



### Hardware

CPU Name: Intel Xeon X5560  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.20 GHz  
 CPU MHz: 2800  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 8 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 24 GB (6x4 GB PC3-10600R, 2 rank, CL9-9-9, ECC)  
 Disk Subsystem: 1 x SATA, 250 GB, 7200 RPM  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 10 (x86\_64) SP2, Kernel 2.6.16.60-0.21-smp  
 Compiler: Intel C++ Compiler 11.0 for Linux Build 20090131 Package ID: l\_cproc\_p\_11.0.080  
 Auto Parallel: No  
 File System: ext3  
 System State: Multi-User Run Level 3  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	16	759	206	<b><u>758</u></b>	<b><u>206</u></b>	756	207	16	647	242	<b><u>644</u></b>	<b><u>243</u></b>	640	244
401.bzip2	16	1036	149	1050	147	<b><u>1043</u></b>	<b><u>148</u></b>	16	975	158	976	158	<b><u>975</u></b>	<b><u>158</u></b>
403.gcc	16	<b><u>635</u></b>	<b><u>203</u></b>	644	200	633	203	16	<b><u>635</u></b>	<b><u>203</u></b>	644	200	633	203
429.mcf	16	582	251	<b><u>565</u></b>	<b><u>258</u></b>	563	259	8	268	272	<b><u>268</u></b>	<b><u>273</u></b>	266	275
445.gobmk	16	728	231	<b><u>729</u></b>	<b><u>230</u></b>	730	230	16	661	254	<b><u>661</u></b>	<b><u>254</u></b>	660	254
456.hammer	16	860	174	862	173	<b><u>861</u></b>	<b><u>173</u></b>	8	<b><u>323</u></b>	<b><u>231</u></b>	323	231	323	231
458.sjeng	16	892	217	<b><u>892</u></b>	<b><u>217</u></b>	892	217	16	812	239	809	239	<b><u>810</u></b>	<b><u>239</u></b>
462.libquantum	16	438	756	438	757	<b><u>438</u></b>	<b><u>757</u></b>	16	438	757	437	758	<b><u>437</u></b>	<b><u>758</u></b>
464.h264ref	16	1171	302	1173	302	<b><u>1172</u></b>	<b><u>302</u></b>	16	<b><u>1112</u></b>	<b><u>318</u></b>	1115	318	1111	319
471.omnetpp	16	663	151	<b><u>578</u></b>	<b><u>173</u></b>	578	173	16	663	151	<b><u>578</u></b>	<b><u>173</u></b>	578	173
473.astar	16	741	152	738	152	<b><u>739</u></b>	<b><u>152</u></b>	16	<b><u>669</u></b>	<b><u>168</u></b>	669	168	667	168
483.xalancbmk	16	421	262	420	263	<b><u>421</u></b>	<b><u>263</u></b>	16	421	262	420	263	<b><u>421</u></b>	<b><u>263</u></b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## General Notes

For information about Fujitsu please visit: <http://www.fujitsu.com>

## Base Compiler Invocation

C benchmarks:  
icc  
C++ benchmarks:  
icpc

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -static -inline-alloc  
-opt-malloc-options=3 -opt-prefetch

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## Base Optimization Flags (Continued)

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/spec/cpu2006.1.1/lib -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/Compiler/11.0/080/bin/intel64/icc

456.hmmer: /opt/intel/Compiler/11.0/080/bin/intel64/icc

458.sjeng: /opt/intel/Compiler/11.0/080/bin/intel64/icc

C++ benchmarks (except as noted below):

icpc

473.astar: /opt/intel/Compiler/11.0/080/bin/intel64/icpc

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32

401.bzip2: -DSPEC\_CPU\_LP64

456.hmmer: -DSPEC\_CPU\_LP64

458.sjeng: -DSPEC\_CPU\_LP64

462.libquantum: -DSPEC\_CPU\_LINUX

473.astar: -DSPEC\_CPU\_LP64

483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -ansi-alias -opt-prefetch

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## Peak Optimization Flags (Continued)

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

403.gcc: basepeak = yes

429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2  
-ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2  
-ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static  
-opt-malloc-options=3 -opt-prefetch

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: basepeak = yes

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=routine -auto-ilp32  
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap64

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca



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The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.14.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.14.xml>

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