SPEC® CFP2006 Result
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Fujitsu

PRIMERGY TX200 S5, Intel Xeon X5560, 2.80 GHz

$SPECfp®\_rate2006 =$ 196

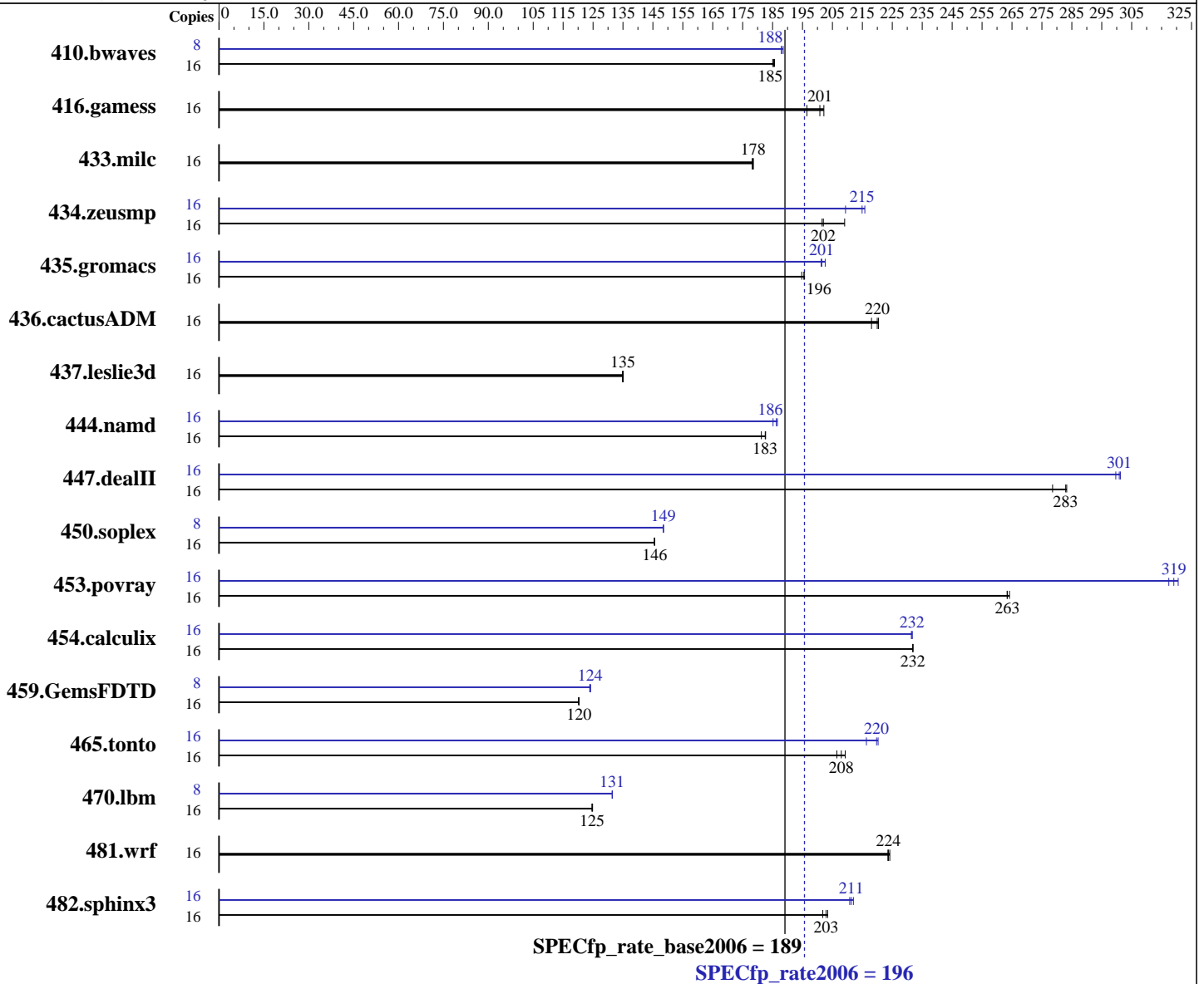$SPECfp\_rate\_base2006 =$ 189

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: May-2009
Hardware Availability: Jun-2009
Software Availability: Feb-2009

Benchmark	Copies	Result
410.bwaves	8	188
	16	185
416.gamess	16	201
433.milc	16	178
434.zeusmp	16	215
	16	202
435.gromacs	16	201
	16	196
436.cactusADM	16	220
437.leslie3d	16	135
444.namd	16	186
	16	183
447.dealII	16	301
	16	283
450.soplex	8	149
	16	146
453.povray	16	319
	16	263
454.calculix	16	232
	16	232
459.GemsFDTD	8	124
	16	120
465.tonto	16	220
	16	208
470.lbm	8	131
	16	125
481.wrf	16	224
482.sphinx3	16	211
	16	203

SPECfp_rate_base2006 = 189

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Hardware

CPU Name: Intel Xeon X5560
CPU Characteristics: Intel Turbo Boost Technology up to 3.20 GHz
CPU MHz: 2800
FPU: Integrated
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core

Continued on next page

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16.60-0.21-smp
Compiler: Intel C++ and Fortran Compiler 11.0 for Linux Build 20090131 Package ID: l_cproc_p_11.0.080, l_cprof_p_11.0.080
Auto Parallel: No
File System: ext3
System State: Multi-User Run Level 3
Base Pointers: 64-bit

Continued on next page

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L3 Cache:	8 MB I+D on chip per chip	Peak Pointers:	32/64-bit
Other Cache:	None	Other Software:	Binutils 2.18.50.0.7.20080502
Memory:	48 GB (6x8 GB PC3-10600R, 2 rank, CL9-9-9, ECC)		
Disk Subsystem:	1 x SATA, 250 GB, 7200 RPM		
Other Hardware:	None		

Results Table

Benchmark	Base						Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	16	1171	186	**1173**	**185**	1175	185	8	**578**	**188**	577	189	578	188
416.gamess	16	**1560**	**201**	1595	196	1550	202	16	**1560**	**201**	1595	196	1550	202
433.milc	16	823	179	824	178	**823**	**178**	16	823	179	824	178	**823**	**178**
434.zeusmp	16	696	209	722	202	**721**	**202**	16	675	216	**677**	**215**	695	209
435.gromacs	16	584	196	**584**	**196**	587	195	16	568	201	**567**	**201**	564	203
436.cactusADM	16	877	218	**869**	**220**	868	220	16	877	218	**869**	**220**	868	220
437.leslie3d	16	1116	135	1113	135	**1114**	**135**	16	1116	135	1113	135	**1114**	**135**
444.namd	16	702	183	**703**	**183**	708	181	16	688	187	**689**	**186**	693	185
447.dealII	16	657	279	**647**	**283**	646	283	16	608	301	611	300	**608**	**301**
450.soplex	16	917	146	**917**	**146**	917	145	8	449	148	449	149	**449**	**149**
453.povray	16	323	263	**323**	**263**	322	264	16	266	321	268	317	**267**	**319**
454.calculix	16	569	232	**569**	**232**	569	232	16	**570**	**232**	571	231	570	232
459.GemsFDTD	16	**1411**	**120**	1411	120	1413	120	8	683	124	685	124	**684**	**124**
465.tonto	16	**757**	**208**	762	206	752	209	16	715	220	728	216	**716**	**220**
470.lbm	16	1761	125	**1763**	**125**	1764	125	8	836	131	**836**	**131**	836	131
481.wrf	16	800	224	**799**	**224**	797	224	16	800	224	**799**	**224**	797	224
482.sphinx3	16	1533	203	**1537**	**203**	1545	202	16	1471	212	**1476**	**211**	1480	211

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

    The config file option 'submit' was used.
     numactl was used to bind copies to the cores

Operating System Notes

     'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

General Notes

     For information about Fujitsu please visit: http://www.fujitsu.com

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Base Compiler Invocation

C benchmarks:
    icc

C++ benchmarks:
    icpc

Fortran benchmarks:
    ifort

Benchmarks using both Fortran and C:
    icc ifort

Base Portability Flags

```
       410.bwaves: -DSPEC_CPU_LP64
       416.gamess: -DSPEC_CPU_LP64
         433.milc: -DSPEC_CPU_LP64
       434.zeusmp: -DSPEC_CPU_LP64
     435.gromacs: -DSPEC_CPU_LP64 -nofor_main
   436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
      437.leslie3d: -DSPEC_CPU_LP64
         444.namd: -DSPEC_CPU_LP64
        447.dealII: -DSPEC_CPU_LP64
       450.soplex: -DSPEC_CPU_LP64
       453.povray: -DSPEC_CPU_LP64
      454.calculix: -DSPEC_CPU_LP64 -nofor_main
    459.GemsFDTD: -DSPEC_CPU_LP64
        465.tonto: -DSPEC_CPU_LP64
         470.lbm: -DSPEC_CPU_LP64
          481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
      482.sphinx3: -DSPEC_CPU_LP64
```

Base Optimization Flags

C benchmarks:
    -xSSE4.2 -ipo -O3 -no-prec-div -static

C++ benchmarks:
    -xSSE4.2 -ipo -O3 -no-prec-div -static

Fortran benchmarks:
    -xSSE4.2 -ipo -O3 -no-prec-div -static

Benchmarks using both Fortran and C:
    -xSSE4.2 -ipo -O3 -no-prec-div -static

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Peak Compiler Invocation

C benchmarks (except as noted below):
        icc

        482.sphinx3: `icc -m32`

C++ benchmarks (except as noted below):
        icpc

         450.soplex: `icpc -m32`

Fortran benchmarks:
        ifort

Benchmarks using both Fortran and C:
        icc ifort

Peak Portability Flags

        410.bwaves: `-DSPEC_CPU_LP64`
        416.gamess: `-DSPEC_CPU_LP64`
          433.milc: `-DSPEC_CPU_LP64`
        434.zeusmp: `-DSPEC_CPU_LP64`
        435.gromacs: `-DSPEC_CPU_LP64 -nofor_main`
      436.cactusADM: `-DSPEC_CPU_LP64 -nofor_main`
        437.leslie3d: `-DSPEC_CPU_LP64`
          444.namd: `-DSPEC_CPU_LP64`
         447.dealII: `-DSPEC_CPU_LP64`
         453.povray: `-DSPEC_CPU_LP64`
        454.calculix: `-DSPEC_CPU_LP64 -nofor_main`
      459.GemsFDTD: `-DSPEC_CPU_LP64`
          465.tonto: `-DSPEC_CPU_LP64`
           470.lbm: `-DSPEC_CPU_LP64`
           481.wrf: `-DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

        433.milc: `basepeak = yes`

        470.lbm: `-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch`
               `-auto-ilp32`

        482.sphinx3: `-xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2`

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Peak Optimization Flags (Continued)

C++ benchmarks:

 444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
 -fno-alias -auto-ilp32

 447.dealII: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
 -unroll2 -ansi-alias -scalar-rep-

 450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
 -opt-malloc-options=3

 453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
 -unroll4 -ansi-alias

Fortran benchmarks:

 410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

 416.gamess: basepeak = yes

 434.zeusmp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)

 437.leslie3d: basepeak = yes

 459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
 -unroll2 -Ob0 -opt-prefetch

 465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
 -unroll4 -auto

Benchmarks using both Fortran and C:

 435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
 -opt-prefetch -auto-ilp32

 436.cactusADM: basepeak = yes

 454.calculix: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32

 481.wrf: basepeak = yes

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The flags file that was used to format this result can be browsed at

http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20090710.15.html

You can also download the XML flags source by saving the following link:

http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20090710.15.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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