



SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Cisco Systems

SPECint®_rate2006 = 242

Cisco B200-M1 (Intel Xeon X5550, 2.66 GHz)

SPECint_rate_base2006 = 226

CPU2006 license: 9019

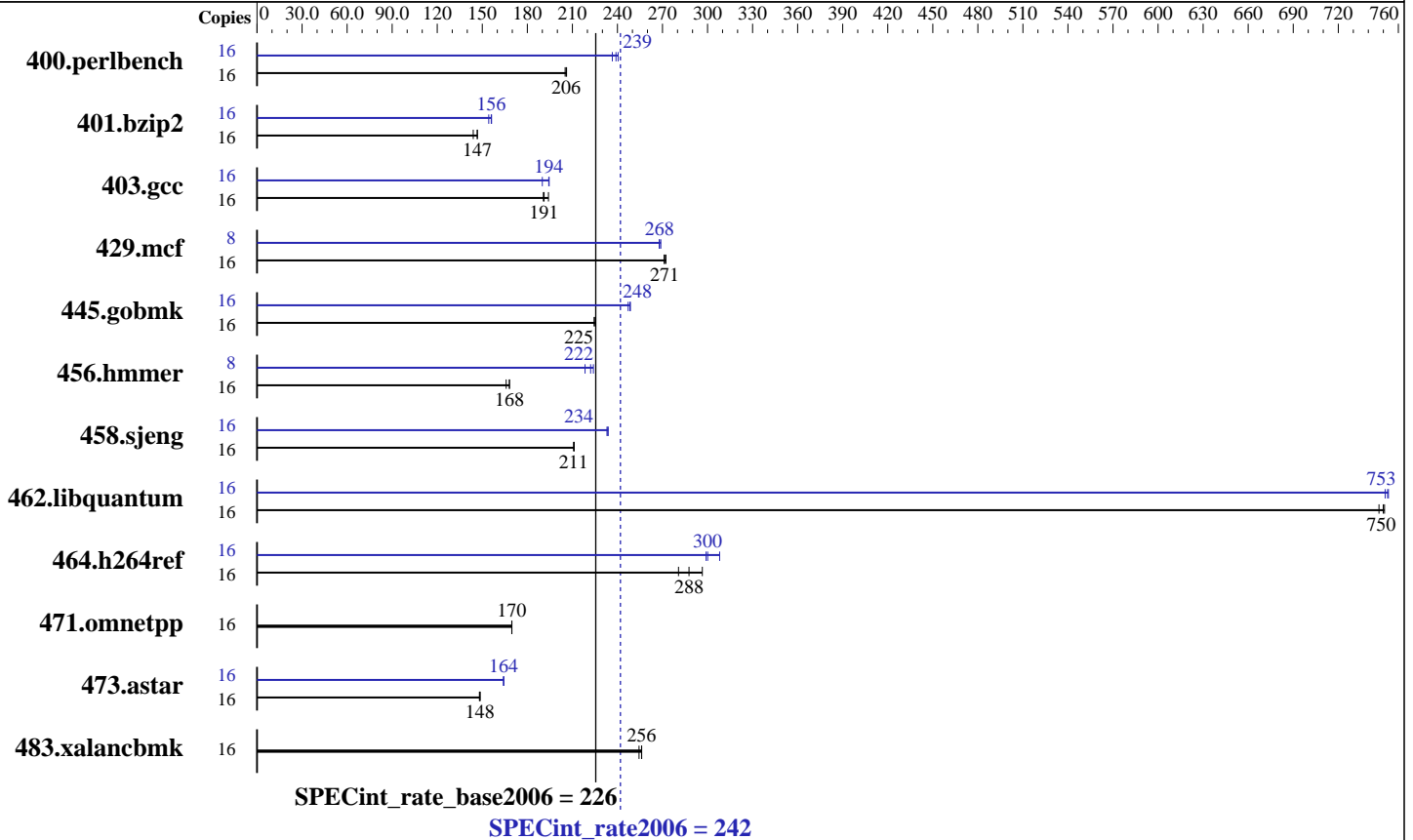
Test date: May-2009

Test sponsor: Cisco Systems

Hardware Availability: May-2009

Tested by: Cisco Systems

Software Availability: May-2009



Hardware

CPU Name: Intel Xeon X5550
 CPU Characteristics: Intel Turbo Boost Technology up to 3.06 GHz
 CPU MHz: 2667
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core
 CPU(s) orderable: 1, 2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 8 MB I+D on chip per chip
 Other Cache: None
 Memory: 24 GB (6 x 4GB DDR3 1333 MHz)
 Disk Subsystem: 73 GB SAS ST973451SS, 15000 RPM
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64), Kernel 2.6.27.19-5-default
 Compiler: Intel C++ Compiler 11.0 for Linux Build 20090131 Package ID: l_cproc_p_11.0.080
 Auto Parallel: No
 File System: ext3
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	16	<u>759</u>	<u>206</u>	758	206	762	205	16	650	240	661	237	<u>654</u>	<u>239</u>
401.bzip2	16	1052	147	<u>1053</u>	<u>147</u>	1072	144	16	1001	154	<u>990</u>	<u>156</u>	989	156
403.gcc	16	675	191	664	194	<u>674</u>	<u>191</u>	16	<u>663</u>	<u>194</u>	678	190	662	195
429.mcf	16	<u>537</u>	<u>271</u>	538	271	536	272	8	271	269	272	268	<u>272</u>	<u>268</u>
445.gobmk	16	<u>747</u>	<u>225</u>	744	225	748	225	16	679	247	<u>676</u>	<u>248</u>	675	249
456.hammer	16	888	168	<u>888</u>	<u>168</u>	900	166	8	342	218	<u>336</u>	<u>222</u>	333	224
458.sjeng	16	<u>918</u>	<u>211</u>	917	211	918	211	16	831	233	828	234	<u>829</u>	<u>234</u>
462.libquantum	16	444	747	<u>442</u>	<u>750</u>	442	751	16	441	751	<u>440</u>	<u>753</u>	440	753
464.h264ref	16	1194	297	1262	281	<u>1231</u>	<u>288</u>	16	1149	308	<u>1180</u>	<u>300</u>	1184	299
471.omnetpp	16	589	170	589	170	<u>589</u>	<u>170</u>	16	589	170	589	170	<u>589</u>	<u>170</u>
473.astar	16	758	148	<u>757</u>	<u>148</u>	756	149	16	<u>684</u>	<u>164</u>	684	164	684	164
483.xalancbmk	16	<u>431</u>	<u>256</u>	434	254	431	256	16	<u>431</u>	<u>256</u>	434	254	431	256

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
 numactl --localalloc --physcpubind=\$BIND was used to bind copies to the cores using following bind list:
 bind = 0, 1, 2, 3, 4, 5, 6, 7, 8, 9, 10, 11, 12, 13, 14, 15

Operating System Notes

ulimit -s unlimited was used to set the stack size

General Notes

Submitted_by: "Ven Immani (immaniv)" <immaniv@cisco.com>
 Submitted: Wed Jun 10 17:31:06 EDT 2009
 Submission: cpu2006-20090601-07562.sub

Submitted_by: "Ven Immani (immaniv)" <immaniv@cisco.com>
 Submitted: Wed Jun 10 17:35:52 EDT 2009
 Submission: cpu2006-20090601-07562.sub

Base Compiler Invocation

C benchmarks:
icc

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Base Compiler Invocation (Continued)

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -inline-alloc
-opt-malloc-options=3 -opt-prefetch

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/Compiler/11.0/080/bin/intel64/icc
456.hmmer: /opt/intel/Compiler/11.0/080/bin/intel64/icc
458.sjeng: /opt/intel/Compiler/11.0/080/bin/intel64/icc

C++ benchmarks (except as noted below):
icpc

473.astar: /opt/intel/Compiler/11.0/080/bin/intel64/icpc



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Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
 401.bzip2: -DSPEC_CPU_LP64
 456.hmmer: -DSPEC_CPU_LP64
 458.sjeng: -DSPEC_CPU_LP64
 462.libquantum: -DSPEC_CPU_LINUX
 473.astar: -DSPEC_CPU_LP64
 483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
 -prof-use(pass 2) -ansi-alias -opt-prefetch

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
 -prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-alloc
 -opt-malloc-options=3

429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
 -prof-use(pass 2) -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
 -ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
 -ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
 -prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static
 -opt-malloc-options=3 -opt-prefetch

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
 -prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: basepeak = yes

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Peak Optimization Flags (Continued)

```
473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
          -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
          -ansi-alias -opt-ra-region-strategy=routine -auto-ilp32
          -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap64
```

```
483.xalancbmk: basepeak = yes
```

Peak Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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