



SPEC[®] CINT2006 Result

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Hewlett-Packard Company

SPECint[®]_rate2006 = 252

ProLiant DL180 G6
(2.93 GHz, Intel Xeon X5570)

SPECint_rate_base2006 = 235

CPU2006 license: 3

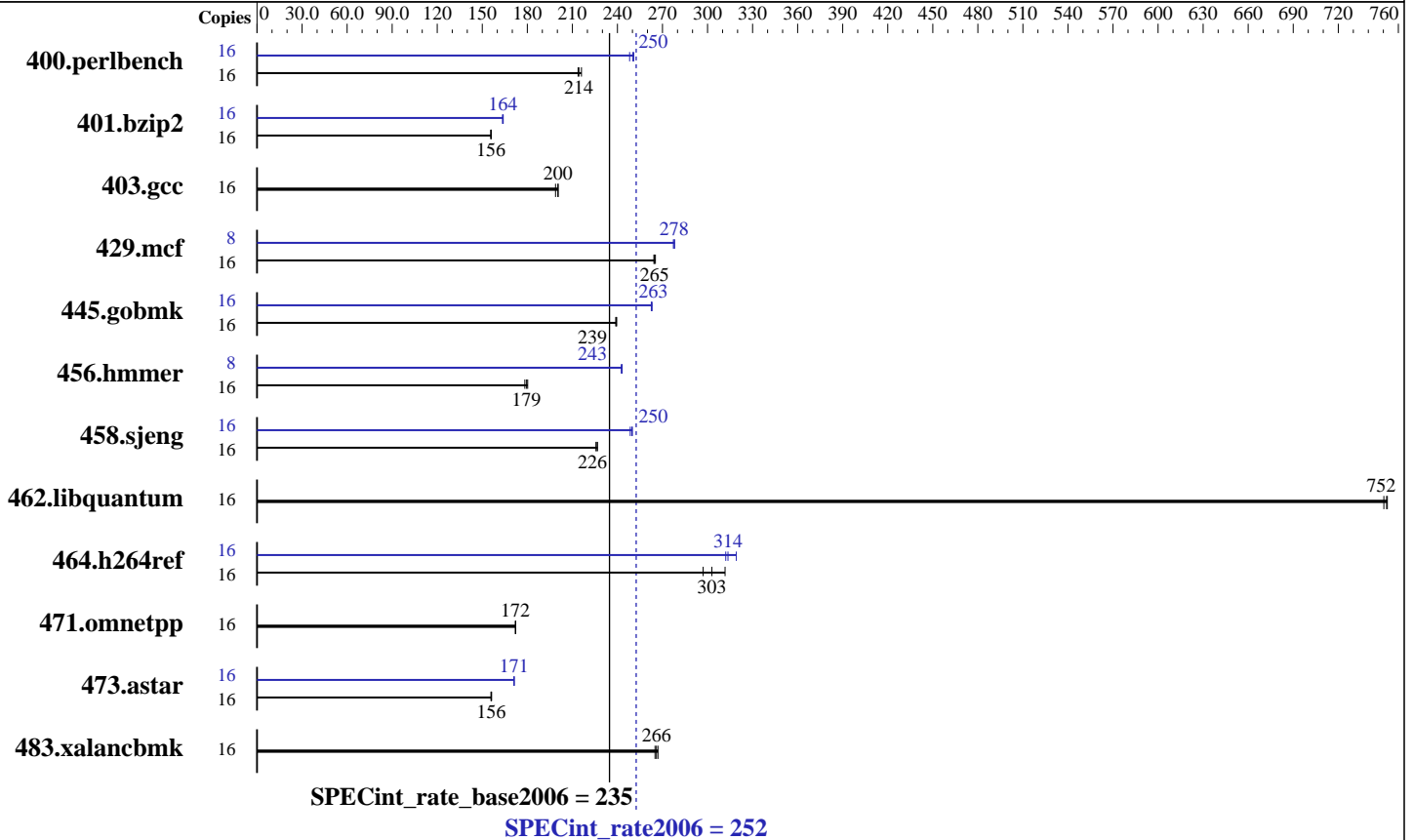
Test date: Apr-2009

Test sponsor: Hewlett-Packard Company

Hardware Availability: Mar-2009

Tested by: Hewlett-Packard Company

Software Availability: Feb-2009



Hardware

CPU Name: Intel Xeon X5570
 CPU Characteristics: Intel Turbo Boost Technology up to 3.33 GHz
 CPU MHz: 2933
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 8 MB I+D on chip per chip
 Other Cache: None
 Memory: 48 GB (6x8 GB PC3-10600R CL9)
 Disk Subsystem: 1x160 GB 7.2K LFF SATA
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 5.3
 Kernel 2.6.18-128.el5
 Compiler: Intel C++ Compiler 11.0 for Linux
 Build 20090131 Package ID: l_cproc_p_11.0.080
 Auto Parallel: No
 File System: ext3
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1
 Binutils 2.18.50.0.7.20080502



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	16	724	216	<u>729</u>	<u>214</u>	731	214	16	630	248	623	251	<u>625</u>	<u>250</u>
401.bzip2	16	990	156	<u>991</u>	<u>156</u>	992	156	16	944	163	942	164	<u>943</u>	<u>164</u>
403.gcc	16	642	201	<u>643</u>	<u>200</u>	648	199	16	642	201	<u>643</u>	<u>200</u>	648	199
429.mcf	16	550	265	552	264	<u>551</u>	<u>265</u>	8	262	278	<u>263</u>	<u>278</u>	263	277
445.gobmk	16	703	239	<u>702</u>	<u>239</u>	701	240	16	638	263	<u>639</u>	<u>263</u>	639	263
456.hammer	16	837	178	828	180	<u>832</u>	<u>179</u>	8	<u>307</u>	<u>243</u>	308	243	307	243
458.sjeng	16	854	227	<u>857</u>	<u>226</u>	858	226	16	<u>776</u>	<u>250</u>	779	249	775	250
462.libquantum	16	442	750	<u>441</u>	<u>752</u>	440	753	16	442	750	<u>441</u>	<u>752</u>	440	753
464.h264ref	16	<u>1169</u>	<u>303</u>	1136	312	1192	297	16	1109	319	<u>1129</u>	<u>314</u>	1134	312
471.omnetpp	16	581	172	581	172	<u>581</u>	<u>172</u>	16	581	172	581	172	<u>581</u>	<u>172</u>
473.astar	16	721	156	<u>720</u>	<u>156</u>	719	156	16	656	171	<u>656</u>	<u>171</u>	656	171
483.xalancbmk	16	417	265	<u>415</u>	<u>266</u>	413	267	16	417	265	<u>415</u>	<u>266</u>	413	267

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Platform Notes

BIOS configuration
Power Efficiency Mode set to Custom
C1E Support Disabled
Intel C-STATE tech Disabled
Intel SpeedStep tech Enabled

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc



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Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3 -opt-prefetch

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/Compiler/11.0/080/bin/intel64/icc

456.hmmer: /opt/intel/Compiler/11.0/080/bin/intel64/icc

458.sjeng: /opt/intel/Compiler/11.0/080/bin/intel64/icc

C++ benchmarks (except as noted below):

icpc

473.astar: /opt/intel/Compiler/11.0/080/bin/intel64/icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64

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Peak Portability Flags (Continued)

458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -ansi-alias -opt-prefetch

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

403.gcc: basepeak = yes

429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
-ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
-ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: basepeak = yes

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=routine -auto-ilp32
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap64

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Peak Optimization Flags (Continued)

483.xalanbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/HP-Intel-Linux-Settings-flags.20090710.html>
<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.10.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/HP-Intel-Linux-Settings-flags.20090710.xml>
<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.10.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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