



SPEC® CINT2006 Result

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Hewlett-Packard Company

SPECint®_rate2006 = 251

ProLiant DL360 G6
(2.93 GHz, Intel Xeon X5570)

SPECint_rate_base2006 = 233

CPU2006 license: 3

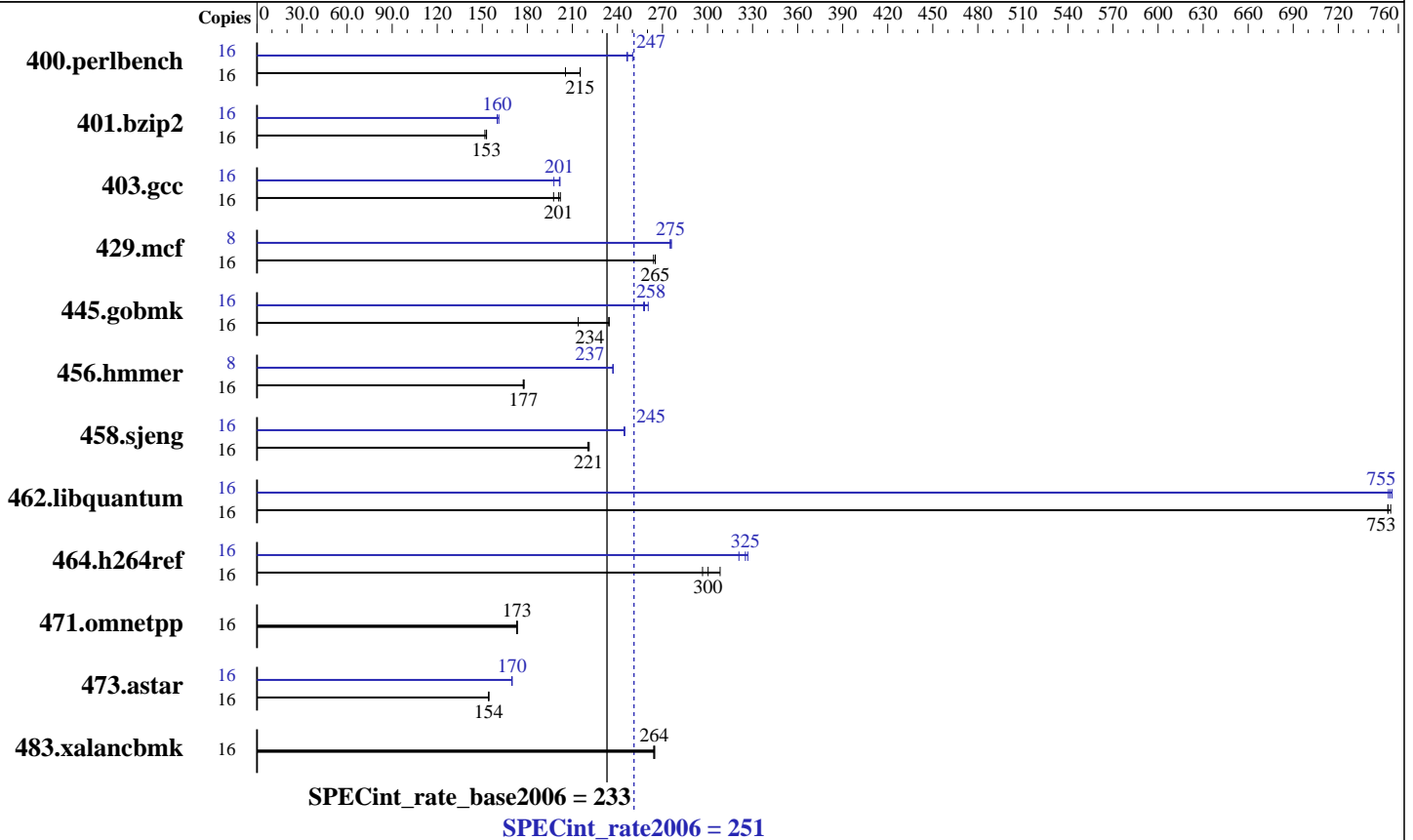
Test date: May-2009

Test sponsor: Hewlett-Packard Company

Hardware Availability: Mar-2009

Tested by: Hewlett-Packard Company

Software Availability: Feb-2009



Hardware

CPU Name: Intel Xeon X5570
 CPU Characteristics: Intel Turbo Boost Technology up to 3.33 GHz
 CPU MHz: 2933
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 8 MB I+D on chip per chip
 Other Cache: None
 Memory: 24 GB (6x4 GB PC3-10600R CL9)
 Disk Subsystem: 2x146 GB 10 K SAS
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 5.3
 Kernel 2.6.18-128.el5
 Compiler: Intel C++ Compiler 11.0 for Linux
 Build 20090131 Package ID: l_cproc_p_11.0.080
 Auto Parallel: No
 File System: ext3
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1
 Binutils 2.18.50.0.7.20080502



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Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	16	761	205	<u>727</u>	<u>215</u>	726	215	16	635	246	625	250	<u>634</u>	<u>247</u>
401.bzip2	16	1017	152	1010	153	<u>1011</u>	<u>153</u>	16	<u>965</u>	<u>160</u>	965	160	959	161
403.gcc	16	638	202	652	197	<u>641</u>	<u>201</u>	16	<u>640</u>	<u>201</u>	638	202	651	198
429.mcf	16	550	265	553	264	<u>550</u>	<u>265</u>	8	<u>265</u>	<u>275</u>	264	276	265	275
445.gobmk	16	785	214	716	235	<u>717</u>	<u>234</u>	16	<u>650</u>	<u>258</u>	644	261	652	257
456.hammer	16	842	177	<u>842</u>	<u>177</u>	839	178	8	315	237	315	237	<u>315</u>	<u>237</u>
458.sjeng	16	<u>877</u>	<u>221</u>	875	221	879	220	16	791	245	<u>791</u>	<u>245</u>	792	244
462.libquantum	16	439	755	440	753	<u>440</u>	<u>753</u>	16	440	753	439	756	<u>439</u>	<u>755</u>
464.h264ref	16	1148	308	<u>1179</u>	<u>300</u>	1193	297	16	<u>1089</u>	<u>325</u>	1083	327	1103	321
471.omnetpp	16	578	173	<u>577</u>	<u>173</u>	577	173	16	578	173	<u>577</u>	<u>173</u>	577	173
473.astar	16	<u>728</u>	<u>154</u>	728	154	728	154	16	<u>661</u>	<u>170</u>	661	170	662	170
483.xalancbmk	16	418	264	417	265	<u>417</u>	<u>264</u>	16	418	264	417	265	<u>417</u>	<u>264</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Platform Notes

BIOS configuration:
HP Power Profile set to Maximum Performance
HP Power Regulator set to HP Static High Performance Mode
Thermal Configuration set to Increased Cooling

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc



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Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3 -opt-prefetch

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/Compiler/11.0/080/bin/intel64/icc

456.hmmer: /opt/intel/Compiler/11.0/080/bin/intel64/icc

458.sjeng: /opt/intel/Compiler/11.0/080/bin/intel64/icc

C++ benchmarks (except as noted below):

icpc

473.astar: /opt/intel/Compiler/11.0/080/bin/intel64/icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64

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Peak Portability Flags (Continued)

458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -ansi-alias -opt-prefetch
401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32
403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-alloc
-opt-malloc-options=3
429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -opt-prefetch
445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
-ipo -no-prec-div -ansi-alias
456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
-ansi-alias -auto-ilp32
458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -unroll4 -auto-ilp32
462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static
-opt-malloc-options=3 -opt-prefetch
464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: basepeak = yes
473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=routine -auto-ilp32
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap64

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Peak Optimization Flags (Continued)

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/HP-Intel-Linux-Settings-flags.20090710.html>

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.10.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/HP-Intel-Linux-Settings-flags.20090710.xml>

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.10.xml>

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For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.

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