SPEC® CINT2006 Result

IBM Corporation

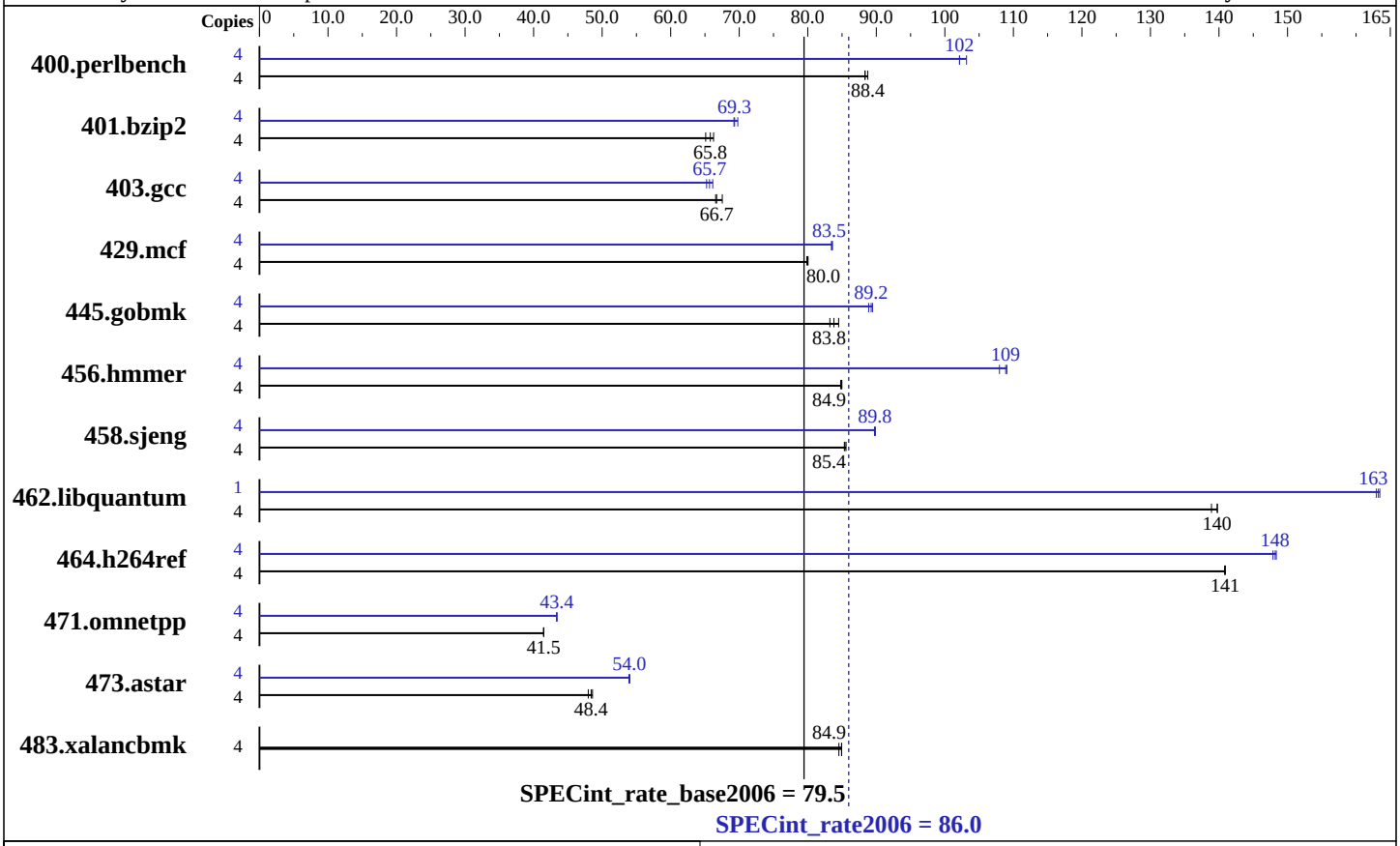
IBM System x3200 M2 (Intel Xeon X3380)

SPECint®_rate2006 = 86.0

SPECint_rate_base2006 = 79.5

CPU2006 license: 11
Test sponsor: IBM Corporation
Tested by: IBM Corporation

Test date: Apr-2009
Hardware Availability: Jun-2009
Software Availability: Nov-2008



Hardware

CPU Name:	Intel Xeon X3380
CPU Characteristics:	1333 MHz system bus
CPU MHz:	3167
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	12 MB I+D on chip per chip, 6 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	8 GB (4 x 2 GB PC2-6400E ECC)
Disk Subsystem:	1 x 250 GB SATA, 7200 RPM
Other Hardware:	None

Software

Operating System:	SuSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
Compiler:	Intel C++ Compiler Professional 11.0 for Linux Build 20080930 Package ID: l_cproc_p_11.0.066
Auto Parallel:	Yes
File System:	ReiserFS
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	442	88.4	**442**	**88.4**	440	88.8	4	379	103	383	102	**383**	**102**
401.bzip2	4	**587**	**65.8**	593	65.1	583	66.3	4	553	69.8	**557**	**69.3**	558	69.2
403.gcc	4	**483**	**66.7**	477	67.5	484	66.6	4	487	66.1	494	65.2	**490**	**65.7**
429.mcf	4	**456**	**80.0**	456	80.0	457	79.9	4	437	83.4	436	83.6	**437**	**83.5**
445.gobmk	4	**501**	**83.8**	504	83.2	497	84.5	4	472	88.9	469	89.5	**470**	**89.2**
456.hmmer	4	**439**	**84.9**	440	84.8	439	85.0	4	342	109	346	108	**343**	**109**
458.sjeng	4	565	85.6	567	85.3	**567**	**85.4**	4	539	89.7	539	89.9	**539**	**89.8**
462.libquantum	4	**593**	**140**	597	139	593	140	1	**127**	**163**	127	163	127	163
464.h264ref	4	**628**	**141**	628	141	629	141	4	599	148	**597**	**148**	597	148
471.omnetpp	4	603	41.5	**603**	**41.5**	603	41.5	4	576	43.4	577	43.4	**576**	**43.4**
473.astar	4	**580**	**48.4**	578	48.6	585	48.0	4	**520**	**54.0**	520	54.0	521	53.9
483.xalancbmk	4	325	84.9	326	84.5	**325**	**84.9**	4	325	84.9	326	84.5	**325**	**84.9**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
 taskset was used to bind processes to cores except for 462.libquantum peak
```

General Notes

```
'ulimit -s unlimited' was used to set the stack size to unlimited prior to run
 OMP_NUM_THREADS set to number of processors
 KMP_AFFINITY set to "physical,0"
 KMP_STACKSIZE set to 64M
 Adjacent Sector Prefetch Disabled
```

Base Compiler Invocation

C benchmarks:
`icc`

C++ benchmarks:
`icpc`

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`

Continued on next page

Base Portability Flags (Continued)

462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xSSE4.1 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3 -opt-prefetch

C++ benchmarks:
-xSSE4.1 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/Compiler/11.0/066/bin/intel64/icc

456.hmmer: /opt/intel/Compiler/11.0/066/bin/intel64/icc

C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xSSE4.1 -ipo -O3 -no-prec-div -static -ansi-alias -opt-prefetch`

401.bzip2: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias`

403.gcc: `-xSSE4.1 -ipo -O3 -no-prec-div -static -inline-calloc -opt-malloc-options=3`

429.mcf: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch`

445.gobmk: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xSSE4.1 -O2 -ipo -no-prec-div -ansi-alias`

456.hmmer: `-xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2 -ansi-alias`

458.sjeng: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xSSE4.1 -ipo -O3 -no-prec-div -static -unroll4`

462.libquantum: `-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-malloc-options=3 -parallel -par-runtime-control -opt-prefetch`

464.h264ref: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2 -ansi-alias`

C++ benchmarks:

471.omnetpp: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xSSE4.1 -ipo -O3 -no-prec-div -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap`

473.astar: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xSSE4.1 -ipo -O3 -no-prec-div -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap`

483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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