



# SPEC<sup>®</sup> CFP2006 Result

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## Fujitsu

SPECfp<sup>®</sup>\_rate2006 = 188

PRIMERGY TX200 S5, Intel Xeon X5550, 2.67 GHz

SPECfp\_rate\_base2006 = 182

CPU2006 license: 19

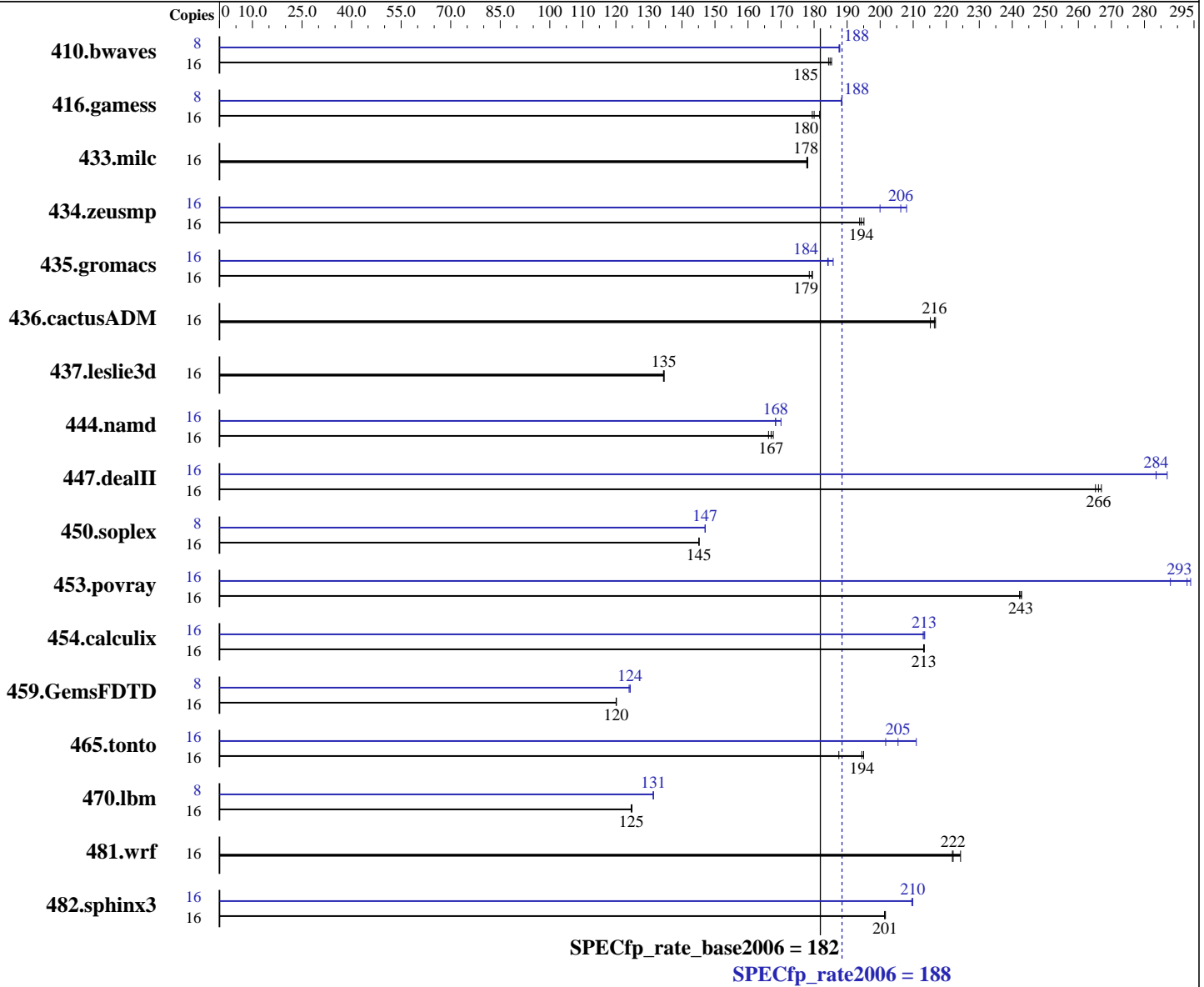
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: May-2009

Hardware Availability: Jun-2009

Software Availability: Feb-2009



### Hardware

CPU Name: Intel Xeon X5550  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.07 GHz  
 CPU MHz: 2667  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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### Software

Operating System: SUSE Linux Enterprise Server 10 (x86\_64) SP2, Kernel 2.6.16.60-0.21-smpp  
 Compiler: Intel C++ and Fortran Compiler 11.0 for Linux Build 20090131 Package ID: l\_cproc\_p\_11.0.080, l\_cprof\_p\_11.0.080  
 Auto Parallel: No  
 File System: ext3  
 System State: Multi-User Run Level 3  
 Base Pointers: 64-bit

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L3 Cache: 8 MB I+D on chip per chip  
Other Cache: None  
Memory: 48 GB (6x8 GB PC3-10600R, 2 rank, CL9-9-9, ECC)  
Disk Subsystem: 1 x SATA, 250 GB, 7200 RPM  
Other Hardware: None

Peak Pointers: 32/64-bit  
Other Software: Binutils 2.18.50.0.7.20080502

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	16	1173	185	<u>1177</u>	<u>185</u>	1179	184	8	580	188	579	188	<u>579</u>	<u>188</u>
416.gamess	16	<u>1740</u>	<u>180</u>	1746	179	1724	182	8	832	188	<u>831</u>	<u>188</u>	831	188
433.milc	16	825	178	<u>826</u>	<u>178</u>	826	178	16	825	178	<u>826</u>	<u>178</u>	826	178
434.zeusmp	16	746	195	751	194	<u>749</u>	<u>194</u>	16	728	200	700	208	<u>706</u>	<u>206</u>
435.gromacs	16	636	180	<u>637</u>	<u>179</u>	640	179	16	620	184	615	186	<u>620</u>	<u>184</u>
436.cactusADM	16	888	215	882	217	<u>884</u>	<u>216</u>	16	888	215	882	217	<u>884</u>	<u>216</u>
437.leslie3d	16	<u>1118</u>	<u>135</u>	1119	134	1117	135	16	<u>1118</u>	<u>135</u>	1119	134	1117	135
444.namd	16	765	168	<u>768</u>	<u>167</u>	772	166	16	755	170	<u>762</u>	<u>168</u>	762	168
447.dealII	16	690	265	686	267	<u>688</u>	<u>266</u>	16	<u>646</u>	<u>284</u>	638	287	646	284
450.soplex	16	919	145	<u>919</u>	<u>145</u>	919	145	8	454	147	<u>454</u>	<u>147</u>	454	147
453.povray	16	<u>351</u>	<u>243</u>	351	243	351	242	16	290	294	296	288	<u>291</u>	<u>293</u>
454.calculix	16	619	213	619	213	<u>619</u>	<u>213</u>	16	<u>619</u>	<u>213</u>	620	213	618	213
459.GemsFDTD	16	<u>1413</u>	<u>120</u>	1414	120	1413	120	8	<u>683</u>	<u>124</u>	685	124	683	124
465.tonto	16	807	195	<u>810</u>	<u>194</u>	840	187	16	781	202	746	211	<u>766</u>	<u>205</u>
470.lbm	16	1760	125	<u>1763</u>	<u>125</u>	1763	125	8	837	131	<u>837</u>	<u>131</u>	837	131
481.wrf	16	<u>805</u>	<u>222</u>	806	222	797	224	16	<u>805</u>	<u>222</u>	806	222	797	224
482.sphinx3	16	1549	201	<u>1548</u>	<u>201</u>	1547	202	16	1486	210	<u>1486</u>	<u>210</u>	1488	210

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
numactl was used to bind copies to the cores

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

## General Notes

For information about Fujitsu please visit: <http://www.fujitsu.com>



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## Base Compiler Invocation

C benchmarks:  
icc  
C++ benchmarks:  
icpc  
Fortran benchmarks:  
ifort  
Benchmarks using both Fortran and C:  
icc ifort

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -static  
C++ benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -static  
Fortran benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -static  
Benchmarks using both Fortran and C:  
-xSSE4.2 -ipo -O3 -no-prec-div -static



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## Peak Compiler Invocation

C benchmarks (except as noted below):

icc

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):

icpc

450.soplex: icpc -m32

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icc ifort

## Peak Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
 416.gamess: -DSPEC\_CPU\_LP64  
 433.milc: -DSPEC\_CPU\_LP64  
 434.zeusmp: -DSPEC\_CPU\_LP64  
 435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
 436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
 437.leslie3d: -DSPEC\_CPU\_LP64  
 444.namd: -DSPEC\_CPU\_LP64  
 447.dealII: -DSPEC\_CPU\_LP64  
 453.povray: -DSPEC\_CPU\_LP64  
 454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
 459.GemsFDTD: -DSPEC\_CPU\_LP64  
 465.tonto: -DSPEC\_CPU\_LP64  
 470.lbm: -DSPEC\_CPU\_LP64  
 481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch  
-auto-ilp32

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2

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## Peak Optimization Flags (Continued)

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-fno-alias -auto-ilp32

447.dealIII: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-unroll2 -ansi-alias -scalar-rep-

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-opt-malloc-options=3

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-unroll2 -Ob0 -ansi-alias -scalar-rep-

434.zeusmp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)

437.leslie3d: basepeak = yes

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-unroll2 -Ob0 -opt-prefetch

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-unroll4 -auto

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32

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## Peak Optimization Flags (Continued)

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20090710.04.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20090710.04.xml>

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