



SPEC[®] CINT2006 Result

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Supermicro Motherboard X8DTN+

SPECint[®]_rate2006 = 200

SPECint_rate_base2006 = 186

CPU2006 license: 001176

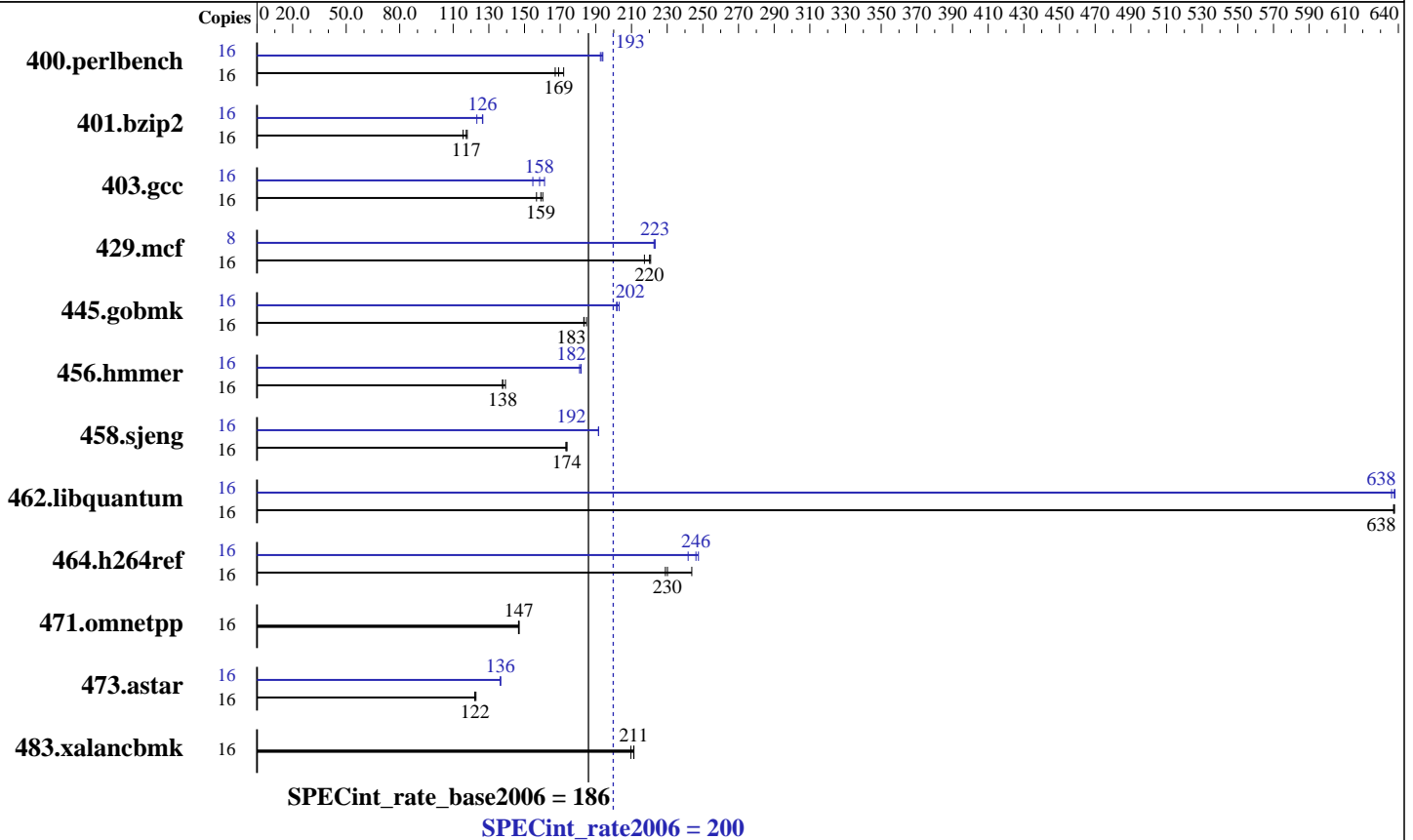
Test sponsor: Supermicro

Tested by: Supermicro

Test date: Apr-2009

Hardware Availability: Mar-2009

Software Availability: Feb-2009



Hardware

CPU Name: Intel Xeon E5520
 CPU Characteristics: Intel Turbo Boost Technology up to 2.53 GHz
 CPU MHz: 2267
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 8 MB I+D on chip per chip
 Other Cache: None
 Memory: 24 GB (6 x 4GB DDR3-1066 RDIMM, CL7)
 Disk Subsystem: 1 x 500 GB SATA II, 7200 RPM
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16.60-0.21-smp
 Compiler: Intel C++ Compiler 11.0 for Linux Build 20090131 Package ID: l_cproc_p_11.0.080
 Auto Parallel: No
 File System: ReiserFS
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	16	935	167	<u>924</u>	<u>169</u>	909	172	16	812	193	<u>808</u>	<u>193</u>	806	194
401.bzip2	16	1336	116	1310	118	<u>1316</u>	<u>117</u>	16	1219	127	<u>1221</u>	<u>126</u>	1253	123
403.gcc	16	804	160	<u>809</u>	<u>159</u>	822	157	16	799	161	833	155	<u>813</u>	<u>158</u>
429.mcf	16	672	217	<u>663</u>	<u>220</u>	661	221	8	<u>327</u>	<u>223</u>	328	223	327	223
445.gobmk	16	908	185	<u>915</u>	<u>183</u>	916	183	16	826	203	833	202	<u>831</u>	<u>202</u>
456.hammer	16	1072	139	<u>1084</u>	<u>138</u>	1084	138	16	825	181	<u>822</u>	<u>182</u>	822	182
458.sjeng	16	1118	173	1114	174	<u>1115</u>	<u>174</u>	16	1011	191	1011	192	<u>1011</u>	<u>192</u>
462.libquantum	16	520	637	<u>520</u>	<u>638</u>	520	638	16	520	638	521	636	<u>520</u>	<u>638</u>
464.h264ref	16	1452	244	1546	229	<u>1538</u>	<u>230</u>	16	<u>1439</u>	<u>246</u>	1430	248	1465	242
471.omnetpp	16	682	147	680	147	<u>681</u>	<u>147</u>	16	682	147	680	147	<u>681</u>	<u>147</u>
473.astar	16	915	123	920	122	<u>918</u>	<u>122</u>	16	823	136	822	137	<u>823</u>	<u>136</u>
483.xalanbmk	16	522	211	<u>523</u>	<u>211</u>	527	210	16	522	211	<u>523</u>	<u>211</u>	527	210

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalanbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -inline-alloc
-opt-malloc-options=3 -opt-prefetch

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Base Optimization Flags (Continued)

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/Compiler/11.0/080/bin/intel64/icc

456.hmmer: /opt/intel/Compiler/11.0/080/bin/intel64/icc

458.sjeng: /opt/intel/Compiler/11.0/080/bin/intel64/icc

C++ benchmarks (except as noted below):

icpc

473.astar: /opt/intel/Compiler/11.0/080/bin/intel64/icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32

401.bzip2: -DSPEC_CPU_LP64

456.hmmer: -DSPEC_CPU_LP64

458.sjeng: -DSPEC_CPU_LP64

462.libquantum: -DSPEC_CPU_LINUX

473.astar: -DSPEC_CPU_LP64

483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -ansi-alias -opt-prefetch

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Peak Optimization Flags (Continued)

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-alloc
-opt-malloc-options=3

429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
-ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
-ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static
-opt-malloc-options=3 -opt-prefetch

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: basepeak = yes

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=routine -auto-ilp32
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap64

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca



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The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.12.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.12.xml>

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For other inquiries, please contact webmaster@spec.org.

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