



SPEC® CINT2006 Result

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SGI

SGI Altix XE 270 (Intel Xeon X5570, 2.93 GHz)

SPECint®2006 = 35.9

SPECint_base2006 = 32.0

CPU2006 license: 4

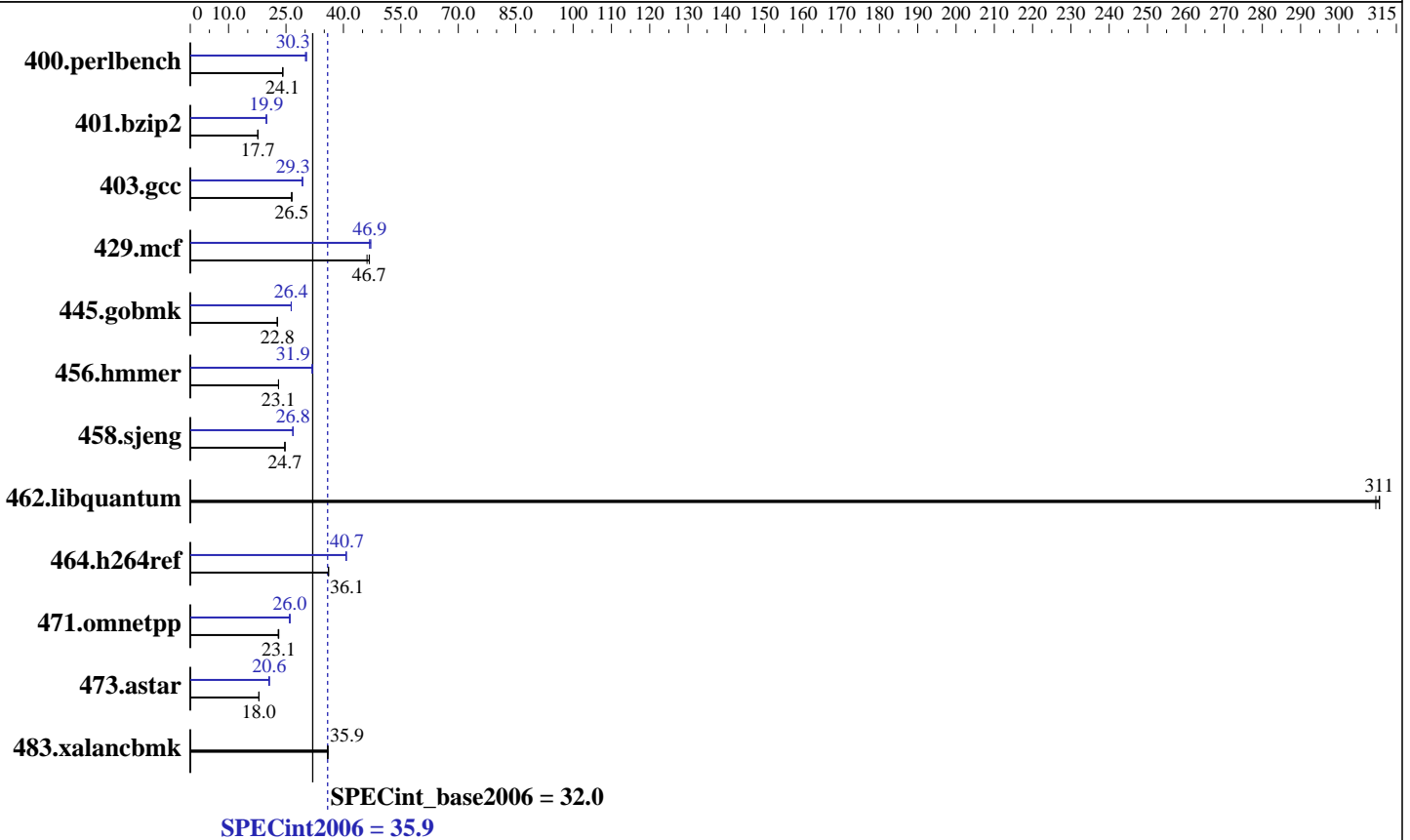
Test sponsor: SGI

Tested by: SGI

Test date: Apr-2009

Hardware Availability: Mar-2009

Software Availability: Feb-2009



Hardware

CPU Name: Intel Xeon X5570
 CPU Characteristics: Quad Core, 2.93 GHz
 Intel Turbo Boost Technology up to 3.33 GHz
 CPU MHz: 2934
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 8 MB I+D on chip per chip
 Other Cache: None
 Memory: 24 GB (6*4GB DDR3-1333 CL9 RDIMMs)
 Disk Subsystem: 3 TB RAID 0
 6 x 500 GB SATA (Seagate Barracuda 7200 rpm)
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP2,
 Kernel 2.6.16.60-0.30-smp
 Compiler: Intel C++ Compiler Professional 11.0 for Linux
 Build 20090131 Package ID: l_cproc_p_11.0.080
 Auto Parallel: Yes
 File System: xfs
 System State: Multi-user, run level 3
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1
 Binutils 2.18.50.0.7.20080502



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Results Table

| Benchmark | Base | | | | | | Peak | | | | | |
|----------------|------------|-------------|------------|-------------|-------------|-------------|------------|-------------|------------|-------------|-------------|-------------|
| | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 404 | 24.2 | 405 | 24.1 | 405 | 24.1 | 325 | 30.1 | 322 | 30.3 | 322 | 30.3 |
| 401.bzip2 | 547 | 17.6 | 546 | 17.7 | 546 | 17.7 | 485 | 19.9 | 485 | 19.9 | 485 | 19.9 |
| 403.gcc | 304 | 26.5 | 304 | 26.5 | 303 | 26.6 | 274 | 29.3 | 275 | 29.3 | 275 | 29.3 |
| 429.mcf | 198 | 46.2 | 195 | 46.8 | 195 | 46.7 | 195 | 46.9 | 195 | 46.9 | 193 | 47.2 |
| 445.gobmk | 461 | 22.8 | 461 | 22.8 | 461 | 22.7 | 398 | 26.4 | 398 | 26.4 | 398 | 26.4 |
| 456.hammer | 405 | 23.1 | 405 | 23.1 | 405 | 23.1 | 293 | 31.9 | 293 | 31.9 | 293 | 31.8 |
| 458.sjeng | 489 | 24.7 | 489 | 24.8 | 490 | 24.7 | 451 | 26.8 | 451 | 26.8 | 451 | 26.8 |
| 462.libquantum | 66.7 | 311 | 66.9 | 310 | 66.7 | 311 | 66.7 | 311 | 66.9 | 310 | 66.7 | 311 |
| 464.h264ref | 613 | 36.1 | 612 | 36.1 | 613 | 36.1 | 544 | 40.7 | 544 | 40.7 | 543 | 40.8 |
| 471.omnetpp | 271 | 23.1 | 271 | 23.1 | 272 | 23.0 | 240 | 26.0 | 241 | 25.9 | 240 | 26.0 |
| 473.astar | 392 | 17.9 | 391 | 18.0 | 391 | 18.0 | 341 | 20.6 | 342 | 20.5 | 340 | 20.7 |
| 483.xalancbmk | 192 | 35.8 | 192 | 35.9 | 192 | 36.0 | 192 | 35.8 | 192 | 35.9 | 192 | 36.0 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

SGI ProPack 6 for Linux Service Pack 2 installed
Adjacent cacheline prefetch enabled
OMP_NUM_THREADS set to number of physical cores
KMP_AFFINITY set to "granularity=fine,scatter"

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX



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Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel
-par-runtime-control -opt-prefetch

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/Compiler/11.0/080/bin/intel64/icc

456.hmmer: /opt/intel/Compiler/11.0/080/bin/intel64/icc

458.sjeng: /opt/intel/Compiler/11.0/080/bin/intel64/icc

C++ benchmarks (except as noted below):

icpc

473.astar: /opt/intel/Compiler/11.0/080/bin/intel64/icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32

401.bzip2: -DSPEC_CPU_LP64

456.hmmer: -DSPEC_CPU_LP64

458.sjeng: -DSPEC_CPU_LP64

462.libquantum: -DSPEC_CPU_LINUX

473.astar: -DSPEC_CPU_LP64

483.xalancbmk: -DSPEC_CPU_LINUX



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Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -ansi-alias -opt-prefetch

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -auto-ilp32 -opt-prefetch -ansi-alias

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-alloc
-opt-malloc-options=3

429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
-ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
-ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=routine -auto-ilp32
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap64

483.xalanbmk: basepeak = yes



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Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.02.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.02.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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