# SPEC® CINT2006 Result

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## Itautec

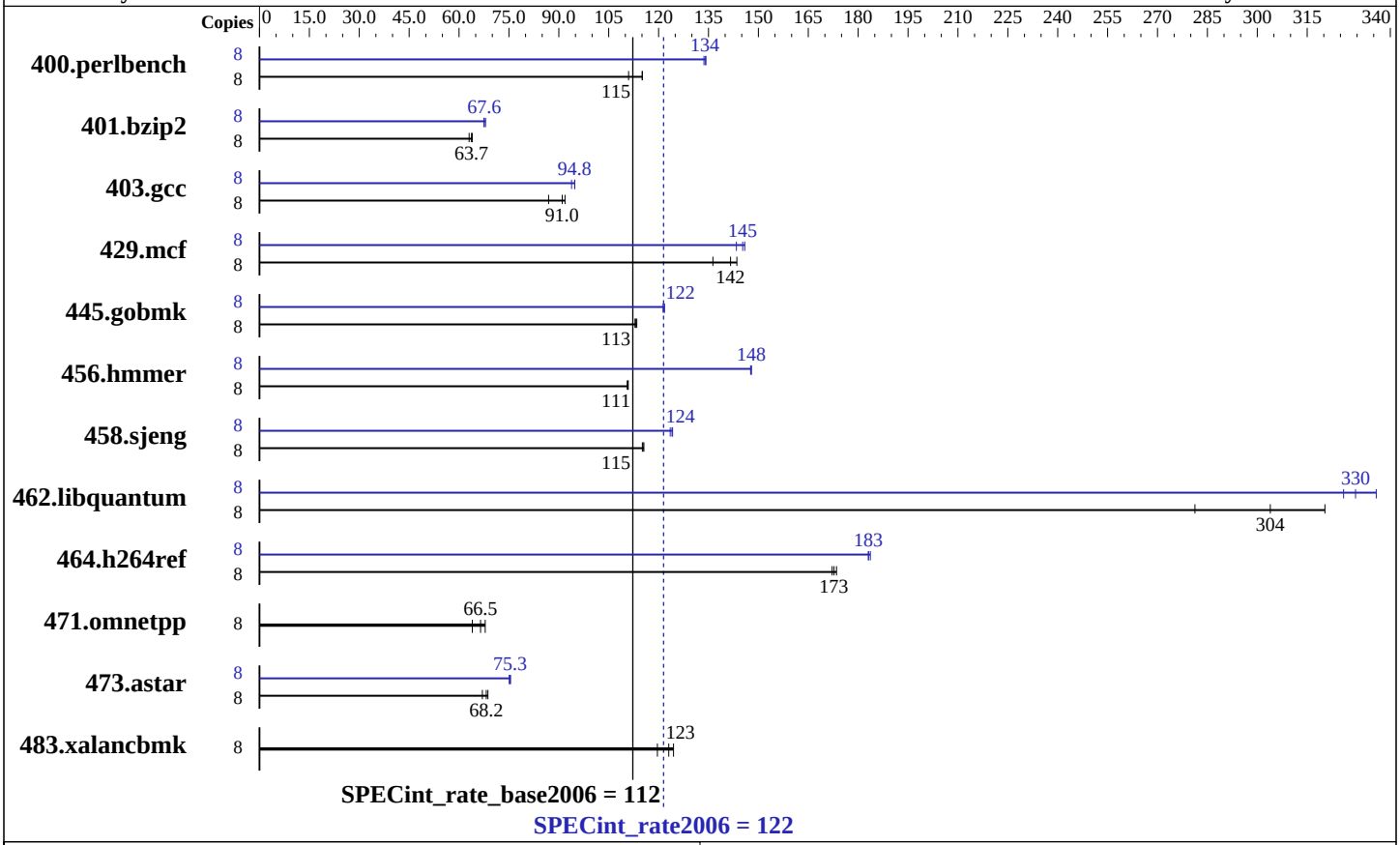
## Servidor Itautec LX203 (Intel Xeon E5504)

SPECint®_rate2006 = 122

SPECint_rate_base2006 = 112

**CPU2006 license:** 9001
**Test sponsor:** Itautec
**Tested by:** Itautec

**Test date:** Apr-2009
**Hardware Availability:** Mar-2009
**Software Availability:** Feb-2009




## Hardware

| | |
|---|---|
| CPU Name: | Intel Xeon E5504 |
| CPU Characteristics: | |
| CPU MHz: | 2000 |
| FPU: | Integrated |
| CPU(s) enabled: | 8 cores, 2 chips, 4 cores/chip |
| CPU(s) orderable: | 1, 2 chips |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 256 KB I+D on chip per core |
| L3 Cache: | 4 MB I+D on chip per chip |
| Other Cache: | None |
| Memory: | 24 GB (6 x 4GB DDR3-1333, CL 9, ECC, running at 800MHz) |
| Disk Subsystem: | 1 x 160 GB SATA-2, 7200RPM |
| Other Hardware: | None |

## Software

| | |
|---|---|
| Operating System: | SuSe Linux SLES10 SP2, Kernel 2.6.16.60-0.34-smp for x86_64 |
| Compiler: | Intel C++ Compiler 11.0 for Linux Build 20090131 Package ID: l_cproc_p_11.0.081 |
| Auto Parallel: | No |
| File System: | ReiserFS |
| System State: | Run level 3 (multi-user) |
| Base Pointers: | 32-bit |
| Peak Pointers: | 32/64-bit |
| Other Software: | Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502 |

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## Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 8 | 704 | 111 | **679** | **115** | 679 | 115 | 8 | 585 | 134 | **583** | **134** | 582 | 134 |
| 401.bzip2 | 8 | 1207 | 64.0 | 1225 | 63.0 | **1211** | **63.7** | 8 | 1136 | 68.0 | 1144 | 67.5 | **1142** | **67.6** |
| 403.gcc | 8 | 741 | 86.9 | 701 | 91.9 | **708** | **91.0** | 8 | 686 | 93.8 | 679 | 94.8 | **680** | **94.8** |
| 429.mcf | 8 | 535 | 136 | **515** | **142** | 508 | 144 | 8 | 509 | 143 | **502** | **145** | 500 | 146 |
| 445.gobmk | 8 | 743 | 113 | **742** | **113** | 740 | 113 | 8 | 692 | 121 | 689 | 122 | **690** | **122** |
| 456.hmmer | 8 | 675 | 111 | **673** | **111** | 673 | 111 | 8 | **505** | **148** | 505 | 148 | 505 | 148 |
| 458.sjeng | 8 | **839** | **115** | 841 | 115 | 838 | 116 | 8 | **781** | **124** | 779 | 124 | 784 | 123 |
| 462.libquantum | 8 | 589 | 281 | **545** | **304** | 517 | 320 | 8 | 509 | 326 | **503** | **330** | 494 | 336 |
| 464.h264ref | 8 | 1028 | 172 | **1025** | **173** | 1020 | 173 | 8 | 967 | 183 | **967** | **183** | 964 | 184 |
| 471.omnetpp | 8 | 781 | 64.0 | **752** | **66.5** | 737 | 67.9 | 8 | 781 | 64.0 | **752** | **66.5** | 737 | 67.9 |
| 473.astar | 8 | 838 | 67.0 | **824** | **68.2** | 818 | 68.7 | 8 | 748 | 75.1 | **745** | **75.3** | 744 | 75.5 |
| 483.xalancbmk | 8 | 461 | 120 | **449** | **123** | 443 | 124 | 8 | 461 | 120 | **449** | **123** | 443 | 124 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

```
The config file option 'submit' was used.
 numactl was used to bind copies to the cores
```

## General Notes

```
This result was measured on the Servidor Itautec LX203.
The Servidor Itautec LX203 and Servidor Itautec LX213 are electronically equivalent.
```

## Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

## Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

## Base Optimization Flags

C benchmarks:
`-xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc -opt-malloc-options=3 -opt-prefetch`

C++ benchmarks:
`-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs -L/opt/sh/SmartHeap_8.1/lib -lsmartheap`

## Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

## Peak Compiler Invocation

C benchmarks (except as noted below):
`icc`

401.bzip2: `/opt/intel/Compiler/11.0/081/bin/intel64/icc`

456.hmmer: `/opt/intel/Compiler/11.0/081/bin/intel64/icc`

458.sjeng: `/opt/intel/Compiler/11.0/081/bin/intel64/icc`

C++ benchmarks (except as noted below):
`icpc`

473.astar: `/opt/intel/Compiler/11.0/081/bin/intel64/icpc`

## Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
401.bzip2: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
458.sjeng: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LINUX`
473.astar: `-DSPEC_CPU_LP64`
483.xalancbmk: `-DSPEC_CPU_LINUX`

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -ansi-alias -opt-prefetch

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc -opt-malloc-options=3

429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2 -ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2 -ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-malloc-options=3 -opt-prefetch

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: basepeak = yes

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias -opt-ra-region-strategy=routine -auto-ilp32 -Wl,-z,muldefs -L/opt/sh/SmartHeap_8.1/lib -lsmartheap64

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.09.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.09.xml`


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For other inquiries, please contact webmaster@spec.org.

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