SPEC® CINT2006 Result
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Fujitsu

PRIMERGY BX920 S1, Intel Xeon E5504, 2.0 GHz

SPECint®_rate2006 =　135

SPECint_rate_base2006 =　126

CPU2006 license: 19	**Test date:** Feb-2009
Test sponsor: Fujitsu	**Hardware Availability:** May-2009
Tested by: Fujitsu	**Software Availability:** Feb-2009

Copies 0 15.0 35.0 55.0 75.0 95.0 110 125 140 155 170 185 200 215 230 245 260 275 290 305 320 335 350 365 380 395 410 425 450

400.perlbench — 8 / 136, 8 / 119
401.bzip2 — 8 / 74.8, 8 / 68.8
403.gcc — 8 / 102, 8 / 102
429.mcf — 8 / 167
445.gobmk — 8 / 123, 8 / 114
456.hmmer — 8 / 151, 8 / 112
458.sjeng — 8 / 128, 8 / 120
462.libquantum — 8 / 445
464.h264ref — 8 / 186, 8 / 176
471.omnetpp — 8 / 88.7
473.astar — 8 / 82.7, 8 / 74.7
483.xalancbmk — 8 / 145

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Hardware

CPU Name:	Intel Xeon E5504
CPU Characteristics:	
CPU MHz:	2000
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	4 MB I+D on chip per chip
Other Cache:	None
Memory:	36 GB (9x4 GB PC3-10600R, 2 rank, CL9-9-9, ECC, see add'l detail in notes)
Disk Subsystem:	1 x SAS, 73 GB, 10000 RPM
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16.60-0.21-smp
Compiler:	Intel C++ Compiler 11.0 for Linux Build 20090131 Package ID: l_cproc_p_11.0.080
Auto Parallel:	No
File System:	ext3
System State:	Multi-User Run Level 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap Library, Version 8.1 Binutils 2.18.50.0.7.20080502

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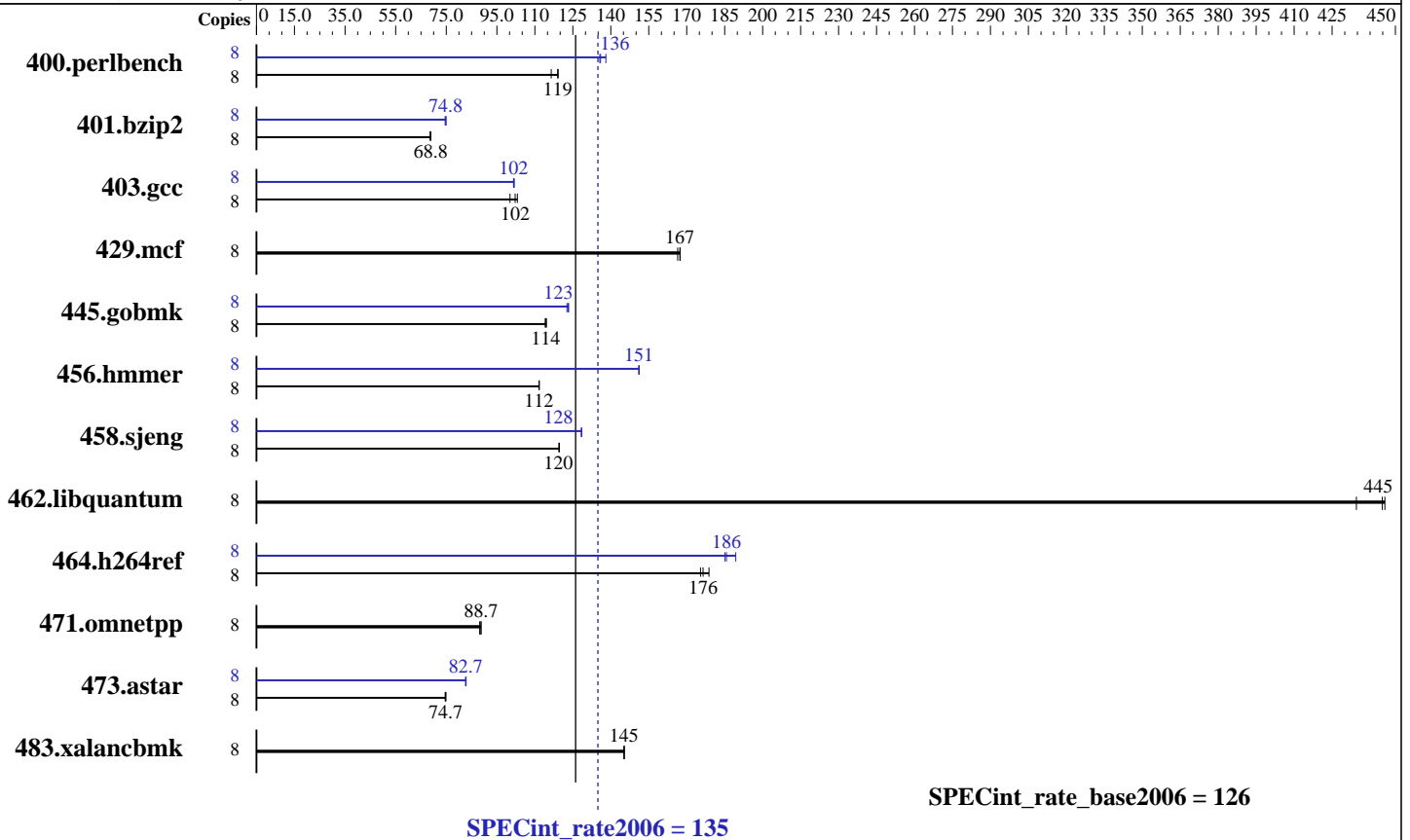
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Results Table

Benchmark	Base Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Peak Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	671	116	656	119	**657**	**119**	8	576	136	566	138	**575**	**136**
401.bzip2	8	1120	68.9	1125	68.6	**1122**	**68.8**	8	1031	74.9	1036	74.5	**1032**	**74.8**
403.gcc	8	643	100	**630**	**102**	625	103	8	632	102	634	102	**634**	**102**
429.mcf	8	438	166	436	167	**436**	**167**	8	438	166	436	167	**436**	**167**
445.gobmk	8	733	115	**734**	**114**	736	114	8	680	123	684	123	**682**	**123**
456.hmmer	8	**668**	**112**	668	112	668	112	8	494	151	**494**	**151**	494	151
458.sjeng	8	**809**	**120**	810	120	809	120	8	**754**	**128**	754	128	753	128
462.libquantum	8	**373**	**445**	381	435	372	446	8	**373**	**445**	381	435	372	446
464.h264ref	8	**1003**	**176**	1009	175	990	179	8	957	185	**953**	**186**	935	189
471.omnetpp	8	567	88.2	563	88.7	**564**	**88.7**	8	567	88.2	563	88.7	**564**	**88.7**
473.astar	8	750	74.9	**752**	**74.7**	753	74.5	8	**679**	**82.7**	680	82.6	678	82.8
483.xalancbmk	8	380	145	**380**	**145**	380	145	8	380	145	**380**	**145**	380	145

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Platform Notes

The system automatically configures the memory to run at 800 MHz.

General Notes

For information about Fujitsu please visit: http://www.fujitsu.com

Base Compiler Invocation

C benchmarks:
    icc

C++ benchmarks:
    icpc

Base Portability Flags

    400.perlbench: -DSPEC_CPU_LINUX_IA32
    462.libquantum: -DSPEC_CPU_LINUX

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Base Portability Flags (Continued)

483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
    -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc
    -opt-malloc-options=3 -opt-prefetch

C++ benchmarks:
    -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
    -L/spec/cpu2006.1.1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
    icc

401.bzip2: /opt/intel/Compiler/11.0/080/bin/intel64/icc

456.hmmer: /opt/intel/Compiler/11.0/080/bin/intel64/icc

458.sjeng: /opt/intel/Compiler/11.0/080/bin/intel64/icc

C++ benchmarks (except as noted below):
    icpc

473.astar: /opt/intel/Compiler/11.0/080/bin/intel64/icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX

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Peak Portability Flags (Continued)

    473.astar: `-DSPEC_CPU_LP64`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

    400.perlbench: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2)
        `-O3`(pass 2) `-no-prec-div`(pass 2) `-static`(pass 2)
        `-prof-use`(pass 2) `-ansi-alias` `-opt-prefetch`

    401.bzip2: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2)
        `-O3`(pass 2) `-no-prec-div`(pass 2) `-static`(pass 2)
        `-prof-use`(pass 2) `-opt-prefetch` `-ansi-alias` `-auto-ilp32`

    403.gcc: `-xSSE4.2` `-ipo` `-O3` `-no-prec-div` `-static` `-inline-calloc`
        `-opt-malloc-options=3`

    429.mcf: basepeak = yes

    445.gobmk: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-prof-use`(pass 2) `-O2`
        `-ipo` `-no-prec-div` `-ansi-alias`

    456.hmmer: `-xSSE4.2` `-ipo` `-O3` `-no-prec-div` `-static` `-unroll2`
        `-ansi-alias` `-auto-ilp32`

    458.sjeng: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2)
        `-O3`(pass 2) `-no-prec-div`(pass 2) `-static`(pass 2)
        `-prof-use`(pass 2) `-unroll4` `-auto-ilp32`

    462.libquantum: basepeak = yes

    464.h264ref: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2)
        `-O3`(pass 2) `-no-prec-div`(pass 2) `-static`(pass 2)
        `-prof-use`(pass 2) `-unroll2` `-ansi-alias`

C++ benchmarks:

    471.omnetpp: basepeak = yes

    473.astar: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2)
        `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2)
        `-ansi-alias` `-opt-ra-region-strategy=routine` `-auto-ilp32`
        `-Wl,-z,muldefs` `-L/spec/cpu2006.1.1/lib` `-lsmartheap64`

    483.xalancbmk: basepeak = yes

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Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.02.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.02.xml`

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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