



SPEC® CFP2006 Result

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Fujitsu

PRIMERGY BX920 S1, Intel Xeon E5540, 2.53 GHz

SPECfp®_rate2006 = 168

CPU2006 license: 19

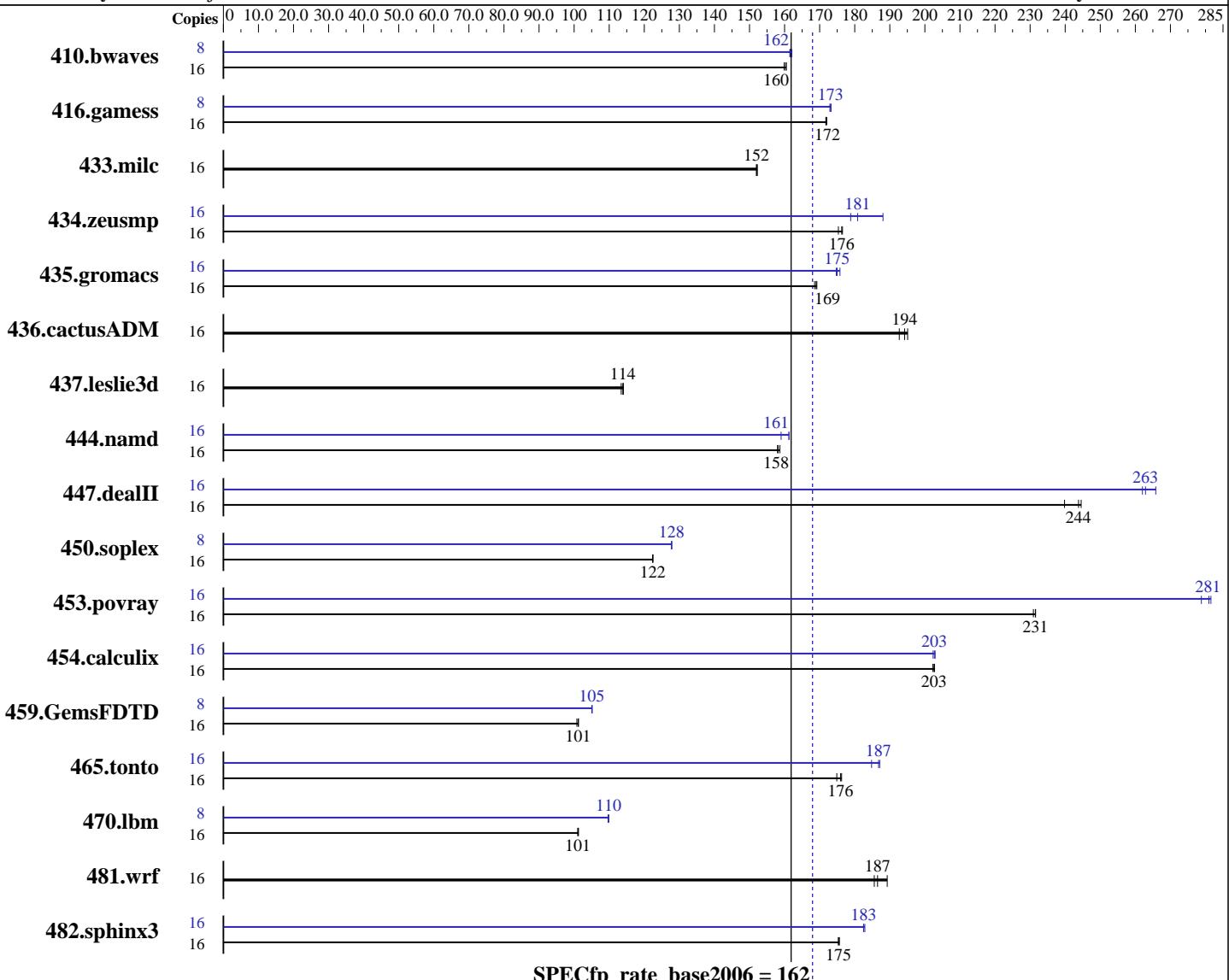
Test date: Mar-2009

Test sponsor: Fujitsu

Hardware Availability: May-2009

Tested by: Fujitsu

Software Availability: Feb-2009



Hardware

CPU Name: Intel Xeon E5540
CPU Characteristics: Intel Turbo Boost Technology up to 2.80 GHz
CPU MHz: 2533
FPU: Integrated
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP2 with patch Linux kernel 20090119, Kernel 2.6.16.60-0.34-smp
Compiler: Intel C++ and Fortran Compiler 11.0 for Linux Build 20090131 Package ID: l_cproc_p_11.0.080, l_cprof_p_11.0.080
Auto Parallel: No
File System: ext3
System State: Multi-User Run Level 3

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L3 Cache:	8 MB I+D on chip per chip	Base Pointers:	64-bit
Other Cache:	None	Peak Pointers:	32/64-bit
Memory:	48 GB (6x4 GB + 3x8 GB PC3-10600R, 2 rank, CL9-9-9, ECC, see add'l detail in notes)	Other Software:	Binutils 2.18.50.0.7.20080502
Disk Subsystem:	1 x SAS, 73 GB, 10000 RPM		
Other Hardware:	None		

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	16	1360	160	1355	161	<u>1357</u>	<u>160</u>	8	673	161	671	162	<u>673</u>	<u>162</u>
416.gamess	16	1821	172	1824	172	<u>1824</u>	<u>172</u>	8	<u>905</u>	<u>173</u>	906	173	904	173
433.milc	16	965	152	967	152	<u>966</u>	<u>152</u>	16	965	152	967	152	<u>966</u>	<u>152</u>
434.zeusmp	16	<u>826</u>	<u>176</u>	825	177	830	175	16	774	188	<u>805</u>	<u>181</u>	814	179
435.gromacs	16	675	169	678	169	<u>676</u>	<u>169</u>	16	654	175	<u>653</u>	<u>175</u>	650	176
436.cactusADM	16	<u>984</u>	<u>194</u>	992	193	980	195	16	<u>984</u>	<u>194</u>	992	193	980	195
437.leslie3d	16	1326	113	1319	114	<u>1321</u>	<u>114</u>	16	1326	113	1319	114	<u>1321</u>	<u>114</u>
444.namd	16	809	159	<u>811</u>	<u>158</u>	812	158	16	807	159	796	161	<u>796</u>	<u>161</u>
447.dealII	16	<u>751</u>	<u>244</u>	763	240	748	245	16	699	262	<u>696</u>	<u>263</u>	689	266
450.soplex	16	1090	122	1090	122	<u>1090</u>	<u>122</u>	8	522	128	<u>522</u>	<u>128</u>	522	128
453.povray	16	369	231	368	232	<u>368</u>	<u>231</u>	16	<u>303</u>	<u>281</u>	302	282	305	279
454.calculix	16	651	203	<u>652</u>	<u>203</u>	653	202	16	650	203	652	202	<u>651</u>	<u>203</u>
459.GemsFDTD	16	1677	101	<u>1678</u>	<u>101</u>	1684	101	8	807	105	808	105	<u>808</u>	<u>105</u>
465.tonto	16	900	175	893	176	<u>895</u>	<u>176</u>	16	852	185	841	187	<u>843</u>	<u>187</u>
470.lbm	16	2171	101	<u>2175</u>	<u>101</u>	2176	101	8	1000	110	<u>1001</u>	<u>110</u>	1002	110
481.wrf	16	944	189	963	186	<u>958</u>	<u>187</u>	16	944	189	963	186	<u>958</u>	<u>187</u>
482.sphinx3	16	<u>1778</u>	<u>175</u>	1775	176	1779	175	16	1705	183	<u>1708</u>	<u>183</u>	1709	183

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Platform Notes

The system automatically configures the memory to run at 1066 MHz.



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General Notes

For information about Fujitsu please visit: <http://www.fujitsu.com>

Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static

Fortran benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static

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Base Optimization Flags (Continued)

Benchmarks using both Fortran and C:

-xSSE4.2 -ipo -O3 -no-prec-div -static

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):

icpc

450.soplex: icpc -m32

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icc ifort

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

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Peak Optimization Flags (Continued)

433.milc: basepeak = yes

470.lbm: -xsse4.2 -ipo -O3 -no-prec-div -static -opt-prefetch
-auto-ilp32

482.sphinx3: -xsse4.2 -ipo -O3 -no-prec-div -static -unroll2

C++ benchmarks:

444.namd: -xsse4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-fno-alias -auto-ilp32

447.dealII: -xsse4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll12 -ansi-alias -scalar-rep-

450.soplex: -xsse4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-malloc-options=3

453.povray: -xsse4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll14 -ansi-alias

Fortran benchmarks:

410.bwaves: -xsse4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

416.gamess: -xsse4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll12 -Ob0 -ansi-alias -scalar-rep-

434.zeusmp: -xsse4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)

437.leslie3d: basepeak = yes

459.GemsFDTD: -xsse4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll12 -Ob0 -opt-prefetch

465.tonto: -xsse4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll14 -auto

Benchmarks using both Fortran and C:

435.gromacs: -xsse4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32

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Peak Optimization Flags (Continued)

436.cactusADM: basepeak = yes

454.calculix: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20090710.04.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20090710.04.xml>

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