SPEC® CFP2006 Result
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Fujitsu

PRIMERGY BX920 S1, Intel Xeon X5550, 2.66 GHz

$SPECfp^{®}\_rate2006 =$ 190
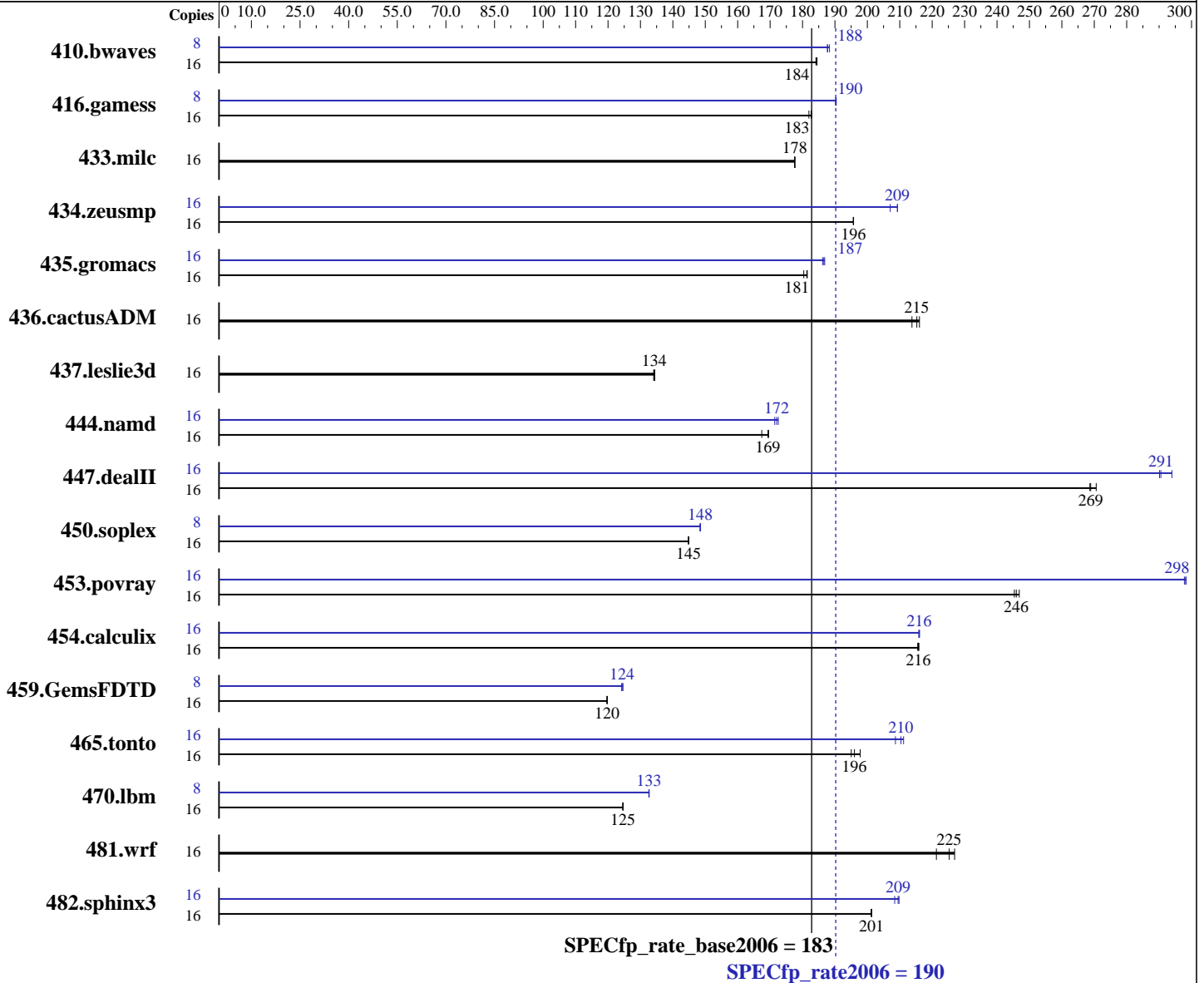
SPECfp_rate_base2006 = 183

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: Mar-2009
Hardware Availability: May-2009
Software Availability: Feb-2009

SPECfp_rate_base2006 = 183

SPECfp_rate2006 = 190

Hardware	
CPU Name:	Intel Xeon X5550
CPU Characteristics:	Intel Turbo Boost Technology up to 3.06 GHz
CPU MHz:	2667
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip, 2 threads/core
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

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Software	
Operating System:	SUSE Linux Enterprise Server 10 (x86_64) SP2 with patch Linux kernel 20090119, Kernel 2.6.16.60-0.34-smp
Compiler:	Intel C++ and Fortran Compiler 11.0 for Linux Build 20090131 Package ID: l_cproc_p_11.0.080, l_cprof_p_11.0.080
Auto Parallel:	No
File System:	ext3
System State:	Multi-User Run Level 3

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L3 Cache:	8 MB I+D on chip per chip
Other Cache:	None
Memory:	48 GB (6x8 GB PC3-10600R, 2 rank, CL9-9-9, ECC)
Disk Subsystem:	1 x SAS, 73 GB, 10000 RPM
Other Hardware:	None

Base Pointers:	64-bit
Peak Pointers:	32/64-bit
Other Software:	Binutils 2.18.50.0.7.20080502

Results Table

Benchmark	Base Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Peak Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	16	1179	184	**1179**	**184**	1181	184	8	**579**	**188**	577	188	579	188
416.gamess	16	1721	182	**1715**	**183**	1714	183	8	823	190	**823**	**190**	823	190
433.milc	16	**827**	**178**	827	178	827	178	16	**827**	**178**	827	178	827	178
434.zeusmp	16	744	196	744	196	**744**	**196**	16	**696**	**209**	696	209	703	207
435.gromacs	16	630	181	633	180	**630**	**181**	16	613	186	**612**	**187**	611	187
436.cactusADM	16	885	216	**888**	**215**	894	214	16	885	216	**888**	**215**	894	214
437.leslie3d	16	1119	134	1122	134	**1120**	**134**	16	1119	134	1122	134	**1120**	**134**
444.namd	16	766	167	**758**	**169**	757	170	16	749	171	**746**	**172**	744	173
447.dealII	16	**681**	**269**	676	271	681	269	16	**630**	**291**	631	290	623	294
450.soplex	16	921	145	**921**	**145**	921	145	8	449	149	**449**	**148**	450	148
453.povray	16	347	245	**346**	**246**	345	247	16	285	298	**285**	**298**	286	298
454.calculix	16	611	216	**612**	**216**	612	216	16	**611**	**216**	611	216	611	216
459.GemsFDTD	16	1419	120	1417	120	**1418**	**120**	8	**683**	**124**	681	125	683	124
465.tonto	16	796	198	807	195	**803**	**196**	16	**748**	**210**	755	209	746	211
470.lbm	16	1763	125	**1764**	**125**	1765	125	8	828	133	**829**	**133**	829	133
481.wrf	16	788	227	**793**	**225**	808	221	16	788	227	**793**	**225**	808	221
482.sphinx3	16	1549	201	1550	201	**1549**	**201**	16	1487	210	**1489**	**209**	1496	208

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

    The config file option 'submit' was used.
     numactl was used to bind copies to the cores

Operating System Notes

    'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

General Notes

    For information about Fujitsu please visit: http://www.fujitsu.com

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Base Compiler Invocation

C benchmarks:
```
icc
```

C++ benchmarks:
```
icpc
```

Fortran benchmarks:
```
ifort
```

Benchmarks using both Fortran and C:
```
icc ifort
```

Base Portability Flags

```
    410.bwaves: -DSPEC_CPU_LP64
    416.gamess: -DSPEC_CPU_LP64
      433.milc: -DSPEC_CPU_LP64
    434.zeusmp: -DSPEC_CPU_LP64
   435.gromacs: -DSPEC_CPU_LP64 -nofor_main
 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
   437.leslie3d: -DSPEC_CPU_LP64
     444.namd: -DSPEC_CPU_LP64
     447.dealII: -DSPEC_CPU_LP64
    450.soplex: -DSPEC_CPU_LP64
    453.povray: -DSPEC_CPU_LP64
   454.calculix: -DSPEC_CPU_LP64 -nofor_main
 459.GemsFDTD: -DSPEC_CPU_LP64
     465.tonto: -DSPEC_CPU_LP64
      470.lbm: -DSPEC_CPU_LP64
      481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
   482.sphinx3: -DSPEC_CPU_LP64
```

Base Optimization Flags

C benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -static
```

C++ benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -static
```

Fortran benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -static
```

Benchmarks using both Fortran and C:
```
-xSSE4.2 -ipo -O3 -no-prec-div -static
```

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Peak Compiler Invocation

C benchmarks (except as noted below):
    `icc`

    482.sphinx3: `icc -m32`

C++ benchmarks (except as noted below):
    `icpc`

    450.soplex: `icpc -m32`

Fortran benchmarks:
    `ifort`

Benchmarks using both Fortran and C:
    `icc ifort`

Peak Portability Flags

          410.bwaves: `-DSPEC_CPU_LP64`
          416.gamess: `-DSPEC_CPU_LP64`
            433.milc: `-DSPEC_CPU_LP64`
          434.zeusmp: `-DSPEC_CPU_LP64`
         435.gromacs: `-DSPEC_CPU_LP64 -nofor_main`
       436.cactusADM: `-DSPEC_CPU_LP64 -nofor_main`
         437.leslie3d: `-DSPEC_CPU_LP64`
            444.namd: `-DSPEC_CPU_LP64`
           447.dealII: `-DSPEC_CPU_LP64`
           453.povray: `-DSPEC_CPU_LP64`
         454.calculix: `-DSPEC_CPU_LP64 -nofor_main`
       459.GemsFDTD: `-DSPEC_CPU_LP64`
            465.tonto: `-DSPEC_CPU_LP64`
             470.lbm: `-DSPEC_CPU_LP64`
             481.wrf: `-DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

      433.milc: `basepeak = yes`

      470.lbm: `-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch`
            `-auto-ilp32`

    482.sphinx3: `-xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2`

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Peak Optimization Flags (Continued)

C++ benchmarks:

    444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
              -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
              -fno-alias -auto-ilp32

    447.dealII: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
                -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
                -unroll2 -ansi-alias -scalar-rep-

    450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
                -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
                -opt-malloc-options=3

    453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
                -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
                -unroll4 -ansi-alias

Fortran benchmarks:

    410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

    416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
                -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
                -unroll2 -Ob0 -ansi-alias -scalar-rep-

    434.zeusmp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
                -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)

    437.leslie3d: basepeak = yes

    459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
                  -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
                  -unroll2 -Ob0 -opt-prefetch

    465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
               -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
               -unroll4 -auto

Benchmarks using both Fortran and C:

    435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
                 -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
                 -opt-prefetch -auto-ilp32

    436.cactusADM: basepeak = yes

    454.calculix: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32

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Peak Optimization Flags (Continued)

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20090710.04.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20090710.04.xml`

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
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