



# SPEC® CINT2006 Result

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## Sun Microsystems

**SPECint®2006 = 35.0**

Sun Blade X6270 (Intel Xeon X5570 2.93GHz)

**SPECint\_base2006 = 30.4**

CPU2006 license: 6

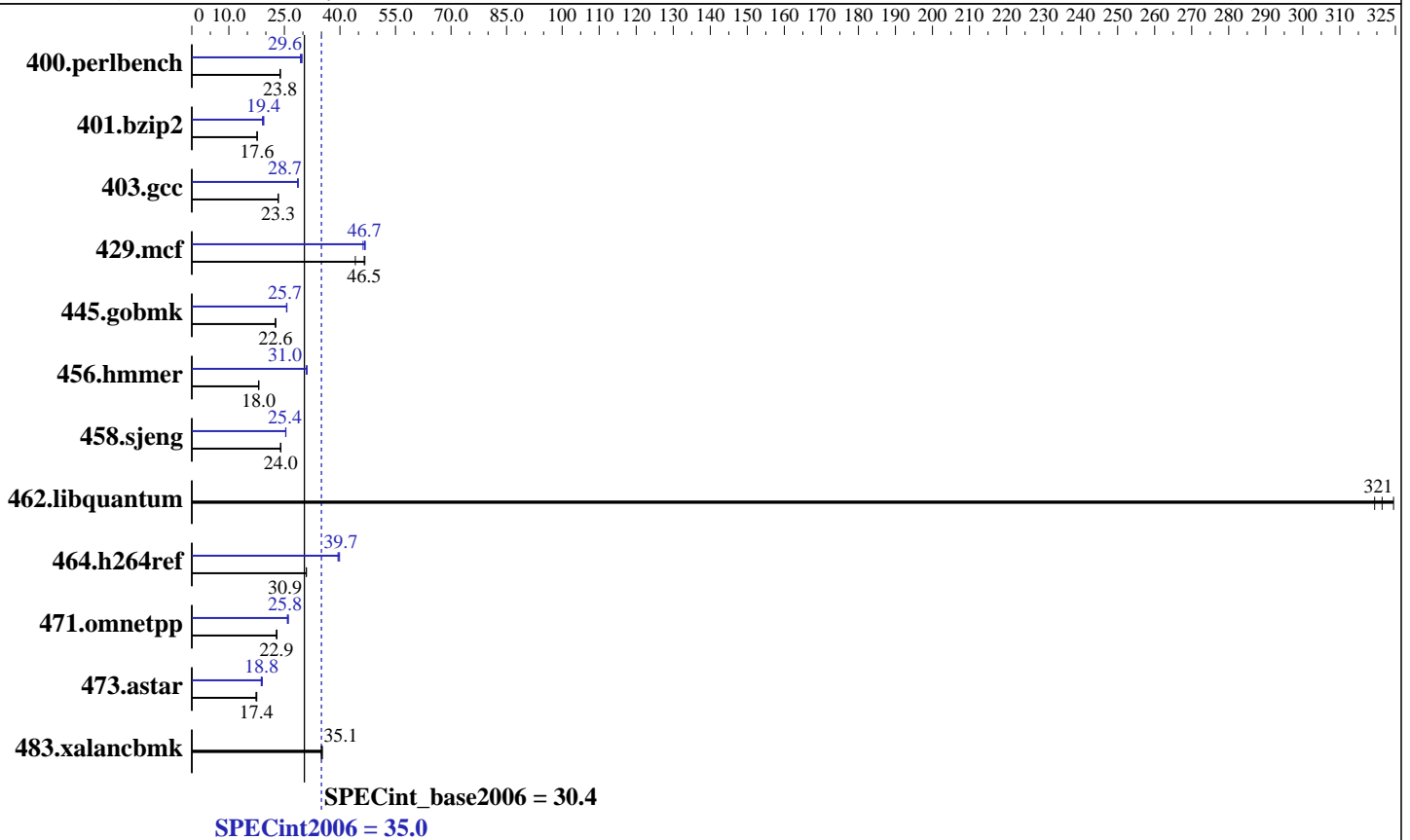
Test date: Mar-2009

Test sponsor: Sun Microsystems

Hardware Availability: Apr-2009

Tested by: Sun Microsystems

Software Availability: Nov-2008



### Hardware

CPU Name: Intel Xeon X5570  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.33 GHz  
 CPU MHz: 2933  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core  
 CPU(s) orderable: 1 or 2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 8 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 24 GB (6 x 4 GB DDR3-1333)  
 Disk Subsystem: 134 GB using 1x ST914602SSUN146GSAS SAS 10 K RPM  
 Other Hardware: None

### Software

Operating System: SuSe Linux Enterprise Server 10 (x86\_64) SP2, kernel 2.6.16.60-0.21-smp  
 Compiler: Intel C++ and Fortran Compiler 11.0 for Linux Build 20080930 Package ID: I\_cproc\_p\_11.0.066, I\_cprof\_p\_11.0.066  
 Auto Parallel: Yes  
 File System: ReiserFS  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	<b><u>410</u></b>	<b><u>23.8</u></b>	408	23.9	410	23.8	<b><u>330</u></b>	<b><u>29.6</u></b>	333	29.3	328	29.8
401.bzip2	<b><u>547</u></b>	<b><u>17.6</u></b>	547	17.6	547	17.6	<b><u>498</u></b>	<b><u>19.4</u></b>	497	19.4	506	19.1
403.gcc	<b><u>345</u></b>	<b><u>23.3</u></b>	345	23.3	345	23.3	280	28.7	282	28.6	<b><u>281</u></b>	<b><u>28.7</u></b>
429.mcf	207	44.2	<b><u>196</u></b>	<b><u>46.5</u></b>	195	46.7	197	46.2	<b><u>195</u></b>	<b><u>46.7</u></b>	195	46.8
445.gobmk	465	22.6	<b><u>464</u></b>	<b><u>22.6</u></b>	464	22.6	410	25.6	409	25.7	<b><u>409</u></b>	<b><u>25.7</u></b>
456.hammer	518	18.0	<b><u>517</u></b>	<b><u>18.0</u></b>	515	18.1	301	31.0	<b><u>301</u></b>	<b><u>31.0</u></b>	300	31.1
458.sjeng	505	23.9	503	24.1	<b><u>504</u></b>	<b><u>24.0</u></b>	476	25.4	478	25.3	<b><u>477</u></b>	<b><u>25.4</u></b>
462.libquantum	<b><u>64.5</u></b>	<b><u>321</u></b>	64.9	319	63.9	325	<b><u>64.5</u></b>	<b><u>321</u></b>	64.9	319	63.9	325
464.h264ref	716	30.9	715	31.0	<b><u>716</u></b>	<b><u>30.9</u></b>	555	39.9	<b><u>558</u></b>	<b><u>39.7</u></b>	561	39.5
471.omnetpp	274	22.8	272	23.0	<b><u>273</u></b>	<b><u>22.9</u></b>	240	26.1	242	25.8	<b><u>242</u></b>	<b><u>25.8</u></b>
473.astar	400	17.5	<b><u>403</u></b>	<b><u>17.4</u></b>	405	17.4	369	19.0	<b><u>373</u></b>	<b><u>18.8</u></b>	375	18.7
483.xalancbmk	198	34.9	<b><u>196</u></b>	<b><u>35.1</u></b>	196	35.3	198	34.9	<b><u>196</u></b>	<b><u>35.1</u></b>	196	35.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

## Platform Notes

Default BIOS settings used.

## General Notes

OMP\_NUM\_THREADS set to number of cores  
KMP\_AFFINITY set to "physical,0"

## Base Compiler Invocation

C benchmarks:  
icc

C++ benchmarks:  
icpc

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32

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## Base Portability Flags (Continued)

462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel  
-par-runtime-control -opt-prefetch

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/spec/cpu2006.1.1/lib -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/Compiler/11.0/042/bin/intel64/icc

456.hmmer: /opt/intel/Compiler/11.0/042/bin/intel64/icc

C++ benchmarks:

icpc

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX



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## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -ansi-alias -opt-prefetch

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -auto-ilp32 -opt-prefetch -ansi-alias

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-alloc  
-opt-malloc-options=3

429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2  
-ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2  
-ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -unroll4

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/spec/cpu2006.1.1/lib -lsmartheap

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs  
-L/spec/cpu2006.1.1/lib -lsmartheap

483.xalancbmk: basepeak = yes



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## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.07.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.07.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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