SPEC® CINT2006 Result
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Dell Inc.

PowerEdge M610 (Intel Xeon X5550, 2.67 GHz)

$SPECint^®2006 =$ 32.5

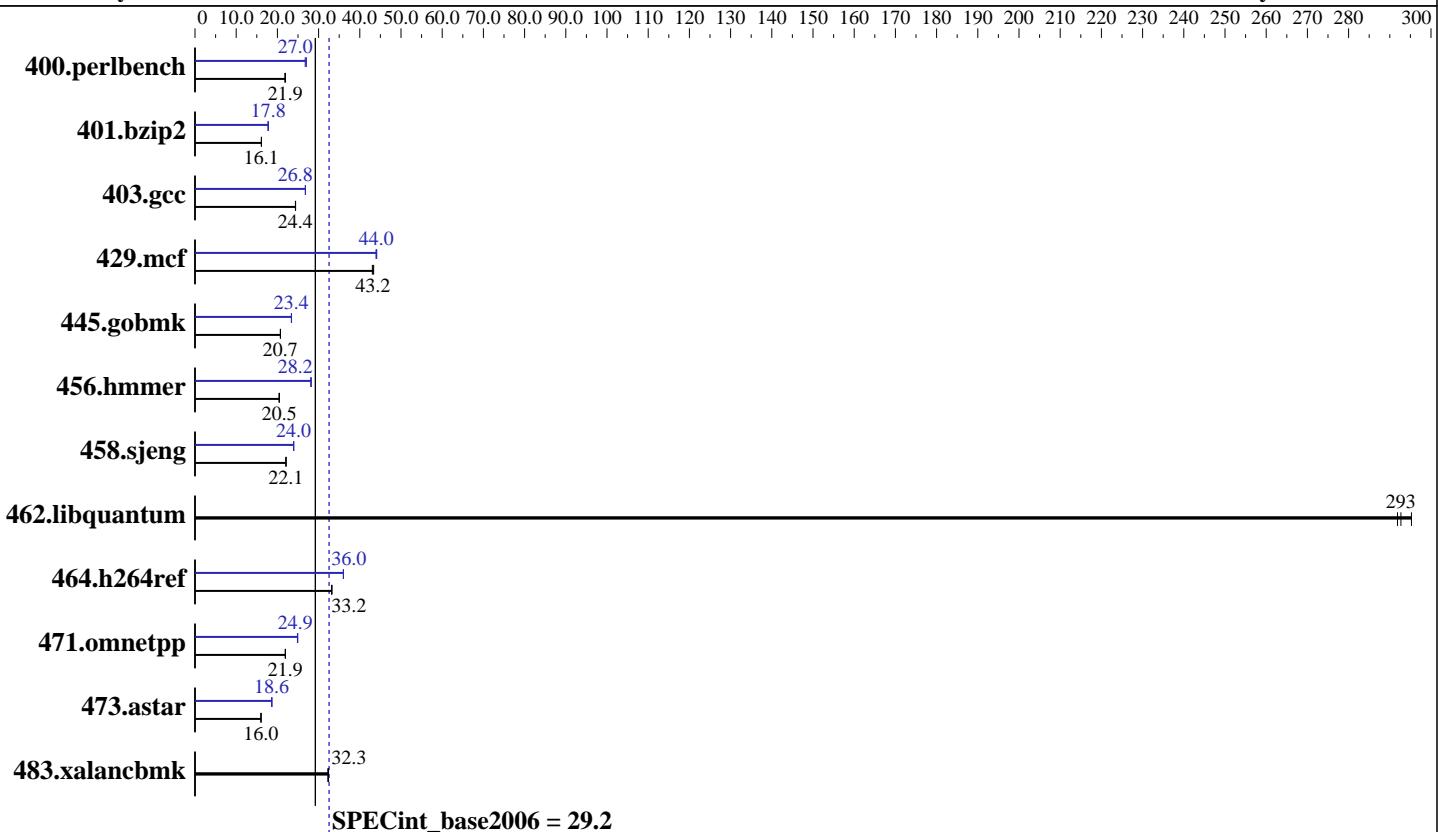$SPECint\_base2006 =$ 29.2

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Mar-2009
Hardware Availability: Mar-2009
Software Availability: Feb-2009

Benchmark	Score
400.perlbench	27.0 / 21.9
401.bzip2	17.8 / 16.1
403.gcc	26.8 / 24.4
429.mcf	44.0 / 43.2
445.gobmk	23.4 / 20.7
456.hmmer	28.2 / 20.5
458.sjeng	24.0 / 22.1
462.libquantum	293
464.h264ref	36.0 / 33.2
471.omnetpp	24.9 / 21.9
473.astar	18.6 / 16.0
483.xalancbmk	32.3

SPECint_base2006 = 29.2
SPECint2006 = 32.5

Hardware

CPU Name:	Intel Xeon X5550
CPU Characteristics:	Intel Turbo Boost Technology up to 3.06 GHz
CPU MHz:	2667
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip, 2 threads/core
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	8 MB I+D on chip per chip
Other Cache:	None
Memory:	24 GB (6 x 4 GB DDR3-1333 DR RDIMM)
Disk Subsystem:	1 x 73 GB 10000 RPM SAS
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16.60-0.21-smp
Compiler:	Intel C++ Compiler Professional 11.0 for Linux Build 20090131 Package ID: l_cproc_p_11.0.080
Auto Parallel:	Yes
File System:	ReiserFS
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502

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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	447	21.8	446	21.9	**446**	**21.9**	362	27.0	**362**	**27.0**	365	26.7
401.bzip2	**599**	**16.1**	599	16.1	598	16.1	**543**	**17.8**	543	17.8	543	17.8
403.gcc	330	24.4	330	24.4	**330**	**24.4**	**300**	**26.8**	300	26.8	300	26.8
429.mcf	212	43.0	210	43.4	**211**	**43.2**	207	44.0	**207**	**44.0**	207	44.1
445.gobmk	506	20.7	506	20.7	**506**	**20.7**	448	23.4	448	23.4	**448**	**23.4**
456.hmmer	456	20.5	456	20.5	**456**	**20.5**	331	28.2	**331**	**28.2**	331	28.2
458.sjeng	**548**	**22.1**	548	22.1	548	22.1	**505**	**24.0**	505	24.0	505	23.9
462.libquantum	71.0	292	**70.8**	**293**	70.2	295	71.0	292	**70.8**	**293**	70.2	295
464.h264ref	**667**	**33.2**	666	33.2	668	33.1	614	36.0	**614**	**36.0**	615	36.0
471.omnetpp	285	21.9	**285**	**21.9**	285	21.9	251	24.9	**251**	**24.9**	251	24.9
473.astar	438	16.0	438	16.0	**438**	**16.0**	377	18.6	376	18.7	**377**	**18.6**
483.xalancbmk	213	32.4	**214**	**32.3**	214	32.3	213	32.4	**214**	**32.3**	214	32.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to granularity=fine,scatter
```

Base Compiler Invocation

C benchmarks:
```
icc
```

C++ benchmarks:
```
icpc
```

Base Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel
-par-runtime-control -opt-prefetch
```

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Base Optimization Flags (Continued)

C++ benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc
```

```
401.bzip2: /opt/intel/Compiler/11.0/080/bin/intel64/icc
```

```
456.hmmer: /opt/intel/Compiler/11.0/080/bin/intel64/icc
```

```
458.sjeng: /opt/intel/Compiler/11.0/080/bin/intel64/icc
```

C++ benchmarks (except as noted below):
```
icpc
```

```
473.astar: /opt/intel/Compiler/11.0/080/bin/intel64/icpc
```

Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
   401.bzip2: -DSPEC_CPU_LP64
   456.hmmer: -DSPEC_CPU_LP64
   458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
   473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
               -prof-use(pass 2) -ansi-alias -opt-prefetch
```

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Peak Optimization Flags (Continued)

```
      401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                 -prof-use(pass 2) -auto-ilp32 -opt-prefetch -ansi-alias

        403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc
                 -opt-malloc-options=3

        429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

      445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
                 -ipo -no-prec-div -ansi-alias

      456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
                 -ansi-alias -auto-ilp32

      458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                 -prof-use(pass 2) -unroll4 -auto-ilp32

  462.libquantum: basepeak = yes

     464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                 -prof-use(pass 2) -unroll2 -ansi-alias
```

C++ benchmarks:

```
     471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
                 -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
                 -L/spec/cpu2006.1.1/lib -lsmartheap

       473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
                 -ansi-alias -opt-ra-region-strategy=routine -auto-ilp32
                 -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap64

   483.xalancbmk: basepeak = yes
```

Peak Other Flags

C benchmarks:

```
        403.gcc: -Dalloca=_alloca
```

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The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.02.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.02.xml

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