SPEC® CINT2006 Result
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Intel Corporation

Supermicro SuperServer 6026T-NTR+ (Intel Xeon E5506, 2.13 GHz)

SPECint®_rate2006 = 142

SPECint_rate_base2006 = 133

Copies 0 20.0 40.0 60.0 80.0 100 120 140 160 180 200 220 240 260 280 300 320 340 360 380 400 420 440 460

Benchmark	Copies	Peak	Base
400.perlbench	8 / 8	147	126
401.bzip2	8 / 8	78.5	74.1
403.gcc	8		107
429.mcf	8		174
445.gobmk	8 / 8	131	122
456.hmmer	8 / 8	161	119
458.sjeng	8 / 8	137	126
462.libquantum	8 / 8	444	444
464.h264ref	8 / 8	202	191
471.omnetpp	8		92.0
473.astar	8 / 8	87.0	78.7
483.xalancbmk	8		152

SPECint_rate_base2006 = 133
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Hardware

CPU Name:	Intel Xeon E5506
CPU Characteristics:	
CPU MHz:	2133
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1, 2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	4 MB I+D on chip per chip
Other Cache:	None
Memory:	24 GB (12 x 2GB DDR3-1066, CL7)
Disk Subsystem:	1 x 150 GB SATA, 10000RPM
Other Hardware:	None

Software

Operating System:	SuSe Linux SLES10 SP2, Kernel 2.6.16.60-0.34-smp for x86_64
Compiler:	Intel C++ Compiler 11.0 for Linux Build 20090131 Package ID: l_cproc_p_11.0.080
Auto Parallel:	No
File System:	ReiserFS
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502

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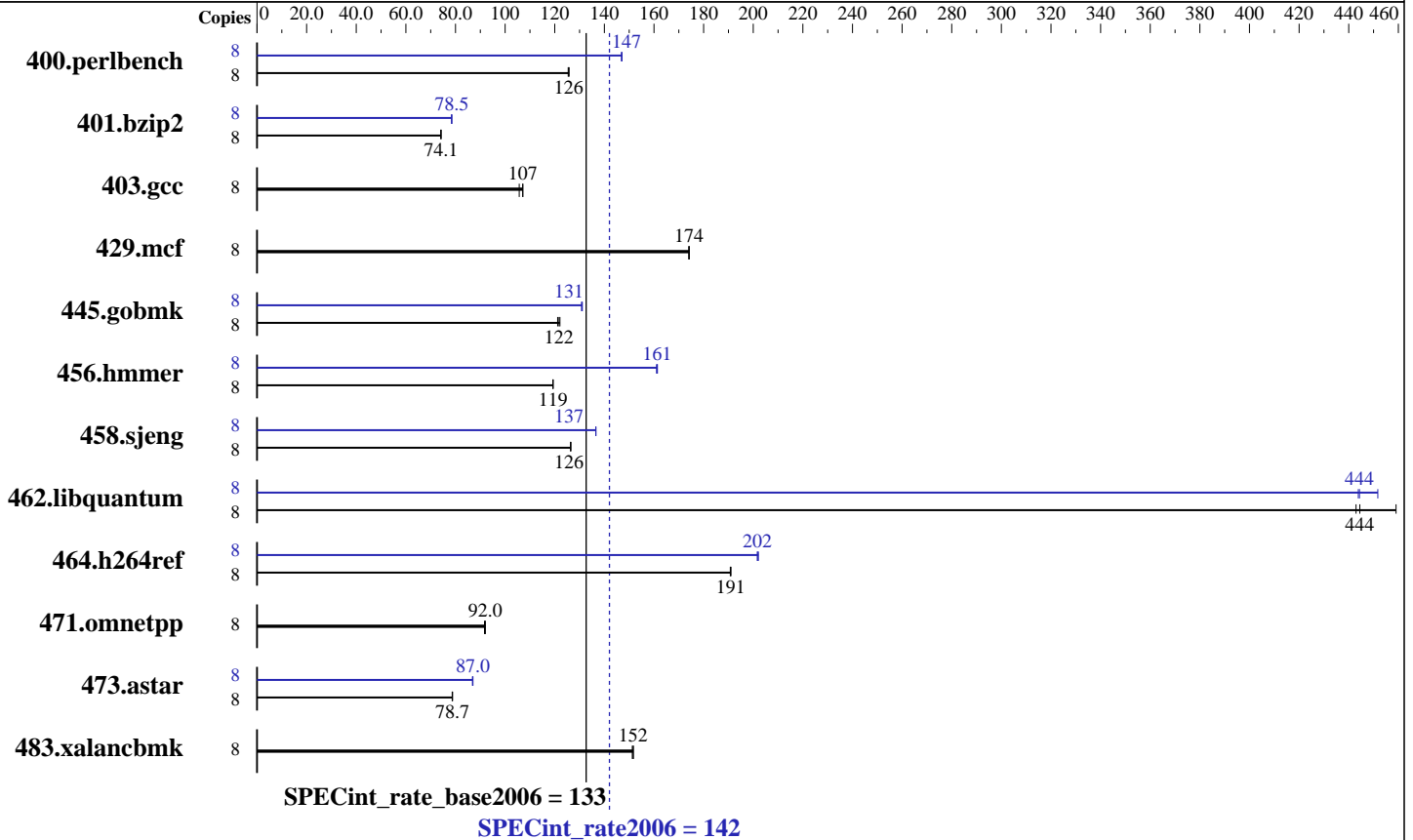SPECint_rate_base2006 =   133

CPU2006 license: 13
Test sponsor: Intel Corporation
Tested by: Intel Corporation

Test date: Feb-2009
Hardware Availability: Mar-2009
Software Availability: Feb-2009

Results Table

Benchmark	Base Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Peak Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	621	126	623	125	**622**	**126**	8	532	147	**532**	**147**	531	147
401.bzip2	8	1041	74.1	**1041**	**74.1**	1041	74.1	8	984	78.4	984	78.5	**984**	**78.5**
403.gcc	8	601	107	609	106	**601**	**107**	8	601	107	609	106	**601**	**107**
429.mcf	8	**419**	**174**	419	174	419	174	8	**419**	**174**	419	174	419	174
445.gobmk	8	693	121	**689**	**122**	687	122	8	**641**	**131**	640	131	642	131
456.hmmer	8	626	119	**626**	**119**	626	119	8	464	161	**463**	**161**	462	161
458.sjeng	8	**766**	**126**	766	126	766	126	8	**709**	**137**	709	137	709	137
462.libquantum	8	374	443	361	459	**373**	**444**	8	**373**	**444**	373	444	367	452
464.h264ref	8	**927**	**191**	927	191	928	191	8	876	202	**878**	**202**	878	202
471.omnetpp	8	545	91.7	**544**	**92.0**	544	92.0	8	545	91.7	**544**	**92.0**	544	92.0
473.astar	8	713	78.8	**713**	**78.7**	714	78.7	8	645	87.0	**646**	**87.0**	647	86.8
483.xalancbmk	8	**364**	**152**	365	151	364	152	8	**364**	**152**	365	151	364	152

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
 numactl was used to bind copies to the cores
```

Base Compiler Invocation

C benchmarks:
```
icc
```

C++ benchmarks:
```
icpc
```

Base Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3 -opt-prefetch
```

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Base Optimization Flags (Continued)

C++ benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc
```

```
401.bzip2: /opt/intel/Compiler/11.0/080/bin/intel64/icc
```

```
456.hmmer: /opt/intel/Compiler/11.0/080/bin/intel64/icc
```

```
458.sjeng: /opt/intel/Compiler/11.0/080/bin/intel64/icc
```

C++ benchmarks (except as noted below):
```
icpc
```

```
473.astar: /opt/intel/Compiler/11.0/080/bin/intel64/icpc
```

Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
  401.bzip2: -DSPEC_CPU_LP64
  456.hmmer: -DSPEC_CPU_LP64
  458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
  473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
               -prof-use(pass 2) -ansi-alias -opt-prefetch
```

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Test sponsor:	Intel Corporation	**Hardware Availability:**	Mar-2009
Tested by:	Intel Corporation	**Software Availability:**	Feb-2009

Peak Optimization Flags (Continued)

```
   401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
              -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
              -prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

     403.gcc: basepeak = yes

     429.mcf: basepeak = yes

   445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
              -ipo -no-prec-div -ansi-alias

   456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
              -ansi-alias -auto-ilp32

   458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
              -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
              -prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static
              -opt-malloc-options=3 -opt-prefetch

  464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
              -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
              -prof-use(pass 2) -unroll2 -ansi-alias
```

C++ benchmarks:

```
  471.omnetpp: basepeak = yes

    473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
              -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
              -ansi-alias -opt-ra-region-strategy=routine -auto-ilp32
              -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap64

483.xalancbmk: basepeak = yes
```

Peak Other Flags

C benchmarks:

```
     403.gcc: -Dalloca=_alloca
```

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.02.html

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You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-icl1.0-int-linux64-revA.20090710.02.xml

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For other inquiries, please contact webmaster@spec.org.

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