



# SPEC® CINT2006 Result

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## Fujitsu

### SPECint®\_rate2006 = 71.3

PRIMERGY RX200 S5, Intel Xeon E5502, 1.86 GHz

### SPECint\_rate\_base2006 = 66.2

CPU2006 license: 19

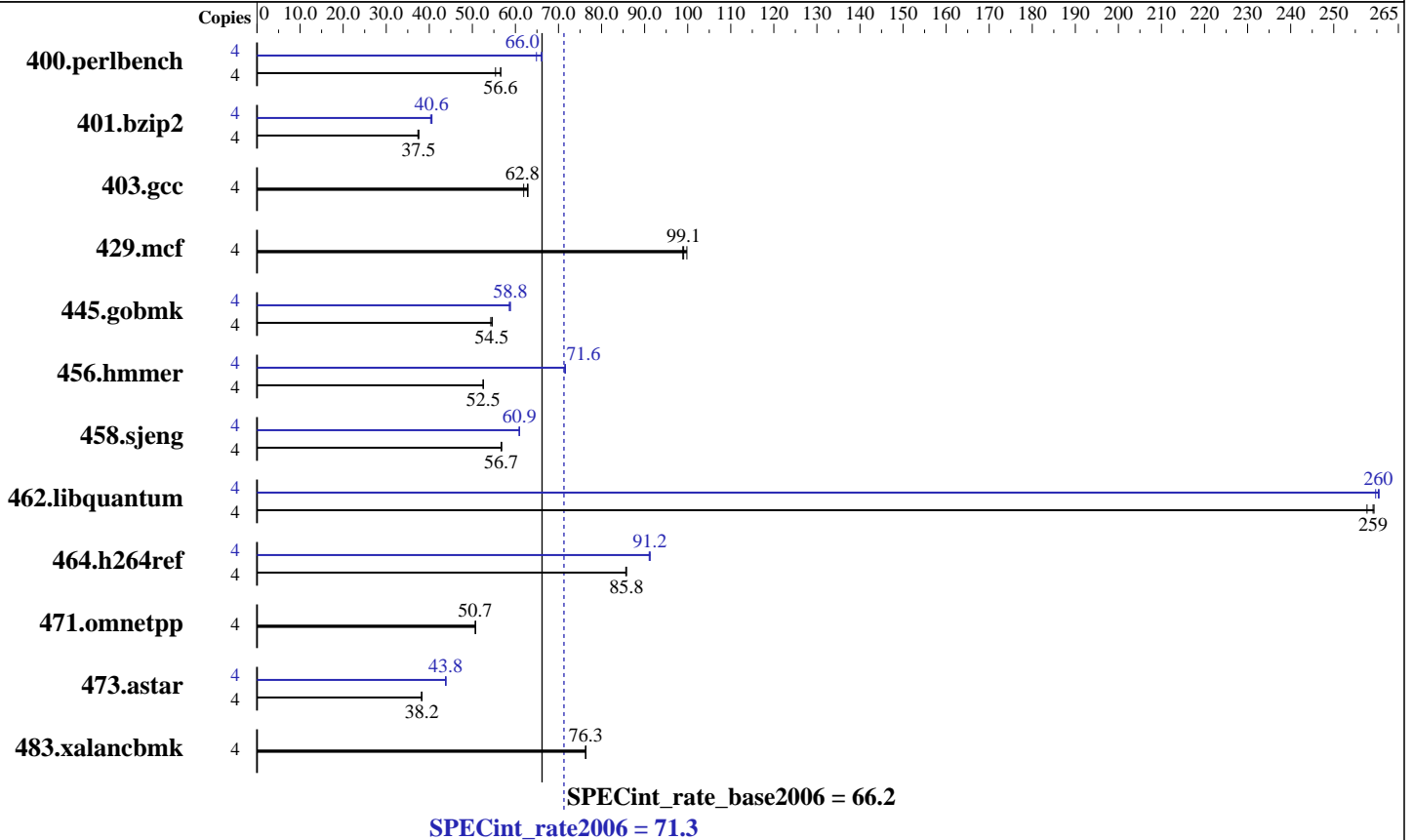
Test date: Mar-2009

Test sponsor: Fujitsu

Hardware Availability: Apr-2009

Tested by: Fujitsu Siemens Computers

Software Availability: Feb-2009



### Hardware

CPU Name: Intel Xeon E5502  
 CPU Characteristics:  
 CPU MHz: 1867  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 4 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 48 GB (12x4 GB PC3-10600R, 2 rank, CL9-9-9, ECC)  
 Disk Subsystem: 1 x SAS, 73 GB, 10000 RPM  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 10 (x86\_64) SP2, Kernel 2.6.16.60-0.21-smp  
 Compiler: Intel C++ Compiler 11.0 for Linux Build 20090131 Package ID: l\_cproc\_p\_11.0.080  
 Auto Parallel: No  
 File System: ext3  
 System State: Multi-User Run Level 3  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	706	55.4	<b><u>691</u></b>	<b><u>56.6</u></b>	691	56.6	4	602	64.9	591	66.1	<b><u>592</u></b>	<b><u>66.0</u></b>
401.bzip2	4	1033	37.4	<b><u>1028</u></b>	<b><u>37.5</u></b>	1028	37.5	4	957	40.3	950	40.6	<b><u>952</u></b>	<b><u>40.6</u></b>
403.gcc	4	<b><u>513</u></b>	<b><u>62.8</u></b>	512	62.9	520	61.9	4	<b><u>513</u></b>	<b><u>62.8</u></b>	512	62.9	520	61.9
429.mcf	4	369	98.9	<b><u>368</u></b>	<b><u>99.1</u></b>	365	99.8	4	369	98.9	<b><u>368</u></b>	<b><u>99.1</u></b>	365	99.8
445.gobmk	4	<b><u>770</u></b>	<b><u>54.5</u></b>	773	54.3	768	54.6	4	<b><u>714</u></b>	<b><u>58.8</u></b>	713	58.8	716	58.6
456.hammer	4	710	52.6	711	52.5	<b><u>711</u></b>	<b><u>52.5</u></b>	4	521	71.6	<b><u>522</u></b>	<b><u>71.6</u></b>	522	71.5
458.sjeng	4	<b><u>853</u></b>	<b><u>56.7</u></b>	852	56.8	854	56.7	4	795	60.9	795	60.9	<b><u>795</u></b>	<b><u>60.9</u></b>
462.libquantum	4	<b><u>320</u></b>	<b><u>259</u></b>	320	259	322	258	4	318	261	319	260	<b><u>318</u></b>	<b><u>260</u></b>
464.h264ref	4	<b><u>1032</u></b>	<b><u>85.8</u></b>	1034	85.6	1032	85.8	4	<b><u>971</u></b>	<b><u>91.2</u></b>	972	91.1	970	91.3
471.omnetpp	4	493	50.8	493	50.7	<b><u>493</u></b>	<b><u>50.7</u></b>	4	493	50.8	493	50.7	<b><u>493</u></b>	<b><u>50.7</u></b>
473.astar	4	735	38.2	<b><u>734</u></b>	<b><u>38.2</u></b>	734	38.3	4	<b><u>641</u></b>	<b><u>43.8</u></b>	641	43.8	640	43.9
483.xalanbmk	4	362	76.3	361	76.4	<b><u>362</u></b>	<b><u>76.3</u></b>	4	362	76.3	361	76.4	<b><u>362</u></b>	<b><u>76.3</u></b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

## General Notes

For information about Fujitsu please visit: <http://www.fujitsu.com>

## Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalanbmk: -DSPEC\_CPU\_LINUX



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## Base Optimization Flags

C benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc  
-opt-malloc-options=3 -opt-prefetch
```

C++ benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/spec/cpu2006.1.1/lib -lsmartheap
```

## Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc

```
401.bzip2: /opt/intel/Compiler/11.0/080/bin/intel64/icc
```

```
456.hmmer: /opt/intel/Compiler/11.0/080/bin/intel64/icc
```

```
458.sjeng: /opt/intel/Compiler/11.0/080/bin/intel64/icc
```

C++ benchmarks (except as noted below):

icpc

```
473.astar: /opt/intel/Compiler/11.0/080/bin/intel64/icpc
```

## Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
```

```
401.bzip2: -DSPEC_CPU_LP64
```

```
456.hmmer: -DSPEC_CPU_LP64
```

```
458.sjeng: -DSPEC_CPU_LP64
```

```
462.libquantum: -DSPEC_CPU_LINUX
```

```
473.astar: -DSPEC_CPU_LP64
```

```
483.xalancbmk: -DSPEC_CPU_LINUX
```



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## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
 -prof-use(pass 2) -ansi-alias -opt-prefetch

401.bzips2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
 -prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

403.gcc: basepeak = yes

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2  
 -ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2  
 -ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
 -prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static  
 -opt-malloc-options=3 -opt-prefetch

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
 -prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: basepeak = yes

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -ansi-alias -opt-ra-region-strategy=routine -auto-ilp32  
 -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap64

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca



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The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.02.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.02.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.1.  
Report generated on Tue Jul 22 23:18:11 2014 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 31 March 2009.