SPEC® CINT2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

PRIMERGY RX100 S5, Intel Xeon X3380, 3.16 GHz

$SPECint^®\_rate2006 =$ 87.4

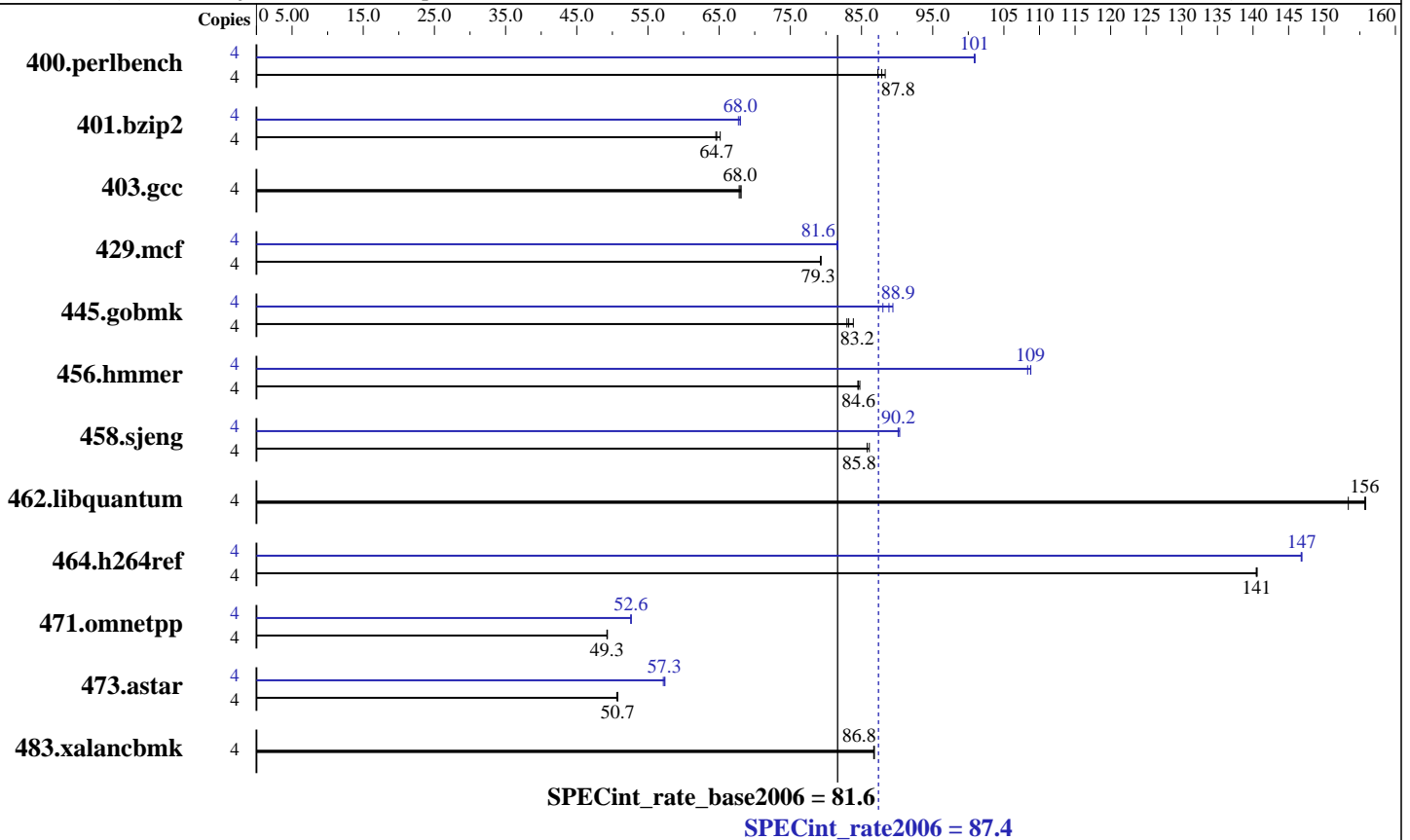$SPECint\_rate\_base2006 =$ 81.6

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu Siemens Computers

Test date: Feb-2009
Hardware Availability: Apr-2009
Software Availability: Nov-2008

Benchmark	Copies	Result
400.perlbench	4	101
	4	87.8
401.bzip2	4	68.0
	4	64.7
403.gcc	4	68.0
429.mcf	4	81.6
	4	79.3
445.gobmk	4	88.9
	4	83.2
456.hmmer	4	109
	4	84.6
458.sjeng	4	90.2
	4	85.8
462.libquantum	4	156
464.h264ref	4	147
	4	141
471.omnetpp	4	52.6
	4	49.3
473.astar	4	57.3
	4	50.7
483.xalancbmk	4	86.8

SPECint_rate_base2006 = 81.6

SPECint_rate2006 = 87.4

Hardware

CPU Name: Intel Xeon X3380
CPU Characteristics: 1333 MHz system bus
CPU MHz: 3167
FPU: Integrated
CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
CPU(s) orderable: 1 chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
L3 Cache: None
Other Cache: None
Memory: 8 GB (4x2 GB PC2-6400E, 2 rank, CL6-6-6, ECC)
Disk Subsystem: 1x SATA, 250 GB, 7200 rpm
Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16.60-0.21-smp
Compiler: Intel C++ Compiler 11.0 for Linux Build 20080730 Package ID: l_cproc_b_11.0.066
Auto Parallel: No
File System: ext3
System State: Multi-User Run Level 3
Base Pointers: 32-bit
Peak Pointers: 32/64-bit
Other Software: Microquill SmartHeap Library, Version 8.1 Binutils 2.18.50.0.7.20080502

SPEC CINT2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

PRIMERGY RX100 S5, Intel Xeon X3380, 3.16 GHz

SPECint_rate2006 =	87.4
SPECint_rate_base2006 =	81.6

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu Siemens Computers

Test date: Feb-2009
Hardware Availability: Apr-2009
Software Availability: Nov-2008

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	**445**	**87.8**	448	87.3	442	88.3	4	**387**	**101**	387	101	388	101
401.bzip2	4	592	65.2	**597**	**64.7**	598	64.5	4	**568**	**68.0**	568	68.0	570	67.7
403.gcc	4	473	68.1	475	67.8	**474**	**68.0**	4	473	68.1	475	67.8	**474**	**68.0**
429.mcf	4	460	79.3	**460**	**79.3**	460	79.2	4	447	81.7	**447**	**81.6**	447	81.6
445.gobmk	4	**504**	**83.2**	500	83.9	506	82.9	4	477	88.0	**472**	**88.9**	469	89.4
456.hmmer	4	440	84.8	442	84.5	**441**	**84.6**	4	**343**	**109**	344	108	343	109
458.sjeng	4	562	86.1	**564**	**85.8**	564	85.8	4	537	90.1	**537**	**90.2**	536	90.4
462.libquantum	4	532	156	540	153	**532**	**156**	4	532	156	540	153	**532**	**156**
464.h264ref	4	630	141	630	140	**630**	**141**	4	603	147	**603**	**147**	603	147
471.omnetpp	4	508	49.2	507	49.3	**508**	**49.3**	4	**475**	**52.6**	475	52.6	475	52.6
473.astar	4	555	50.6	553	50.8	**553**	**50.7**	4	491	57.2	489	57.4	**490**	**57.3**
483.xalancbmk	4	**318**	**86.8**	318	86.7	318	86.8	4	**318**	**86.8**	318	86.7	318	86.8

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

    The config file option 'submit' was used.
     taskset has been used to bind processes to cores

Operating System Notes

     'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Platform Notes

    BIOS configuration:
    Hardware Prefetch = Disable, Adjacent Sector Prefetch = Disable

General Notes

    The Fujitsu PRIMERGY RX100 S5 and the Fujitsu Siemens Computers PRIMERGY RX100 S5
    are electronically equivalent.

    For information about Fujitsu please visit: http://www.fujitsu.com

Base Compiler Invocation

C benchmarks:
     icc

Fujitsu

PRIMERGY RX100 S5, Intel Xeon X3380, 3.16 GHz

SPECint_rate2006 =   87.4

SPECint_rate_base2006 = 81.6

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu Siemens Computers

Test date: Feb-2009
Hardware Availability: Apr-2009
Software Availability: Nov-2008

Base Compiler Invocation (Continued)

C++ benchmarks:
    icpc

Base Portability Flags

    400.perlbench: -DSPEC_CPU_LINUX_IA32
    462.libquantum: -DSPEC_CPU_LINUX
    483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
    -xSSE4.1 -ipo -O3 -no-prec-div -static -inline-calloc
    -opt-malloc-options=3 -opt-prefetch

C++ benchmarks:
    -xSSE4.1 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
    -L/spec/cpu2006.1.1/lib -lsmartheap

Base Other Flags

C benchmarks:

        403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
    icc

    401.bzip2: /opt/intel/Compiler/11.0/066/bin/intel64/icc

    456.hmmer: /opt/intel/Compiler/11.0/066/bin/intel64/icc

C++ benchmarks:
    icpc

SPEC CINT2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

PRIMERGY RX100 S5, Intel Xeon X3380, 3.16 GHz

SPECint_rate2006 =   87.4

SPECint_rate_base2006 =   81.6

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu Siemens Computers

Test date: Feb-2009
Hardware Availability: Apr-2009
Software Availability: Nov-2008

Peak Portability Flags

    400.perlbench: -DSPEC_CPU_LINUX_IA32
        401.bzip2: -DSPEC_CPU_LP64
        456.hmmer: -DSPEC_CPU_LP64
    462.libquantum: -DSPEC_CPU_LINUX
    483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

    400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
                -no-prec-div -static -ansi-alias -opt-prefetch

        401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
                -no-prec-div -static -opt-prefetch -ansi-alias

         403.gcc: basepeak = yes

         429.mcf: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
                -no-prec-div -static -opt-prefetch

       445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -O2 -ipo
                -no-prec-div -ansi-alias

       456.hmmer: -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2
                -ansi-alias

       458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
                -no-prec-div -static -unroll4

    462.libquantum: basepeak = yes

      464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
                -no-prec-div -static -unroll2 -ansi-alias

C++ benchmarks:

      471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
                -no-prec-div -ansi-alias -opt-ra-region-strategy=block
                -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap

        473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
                -no-prec-div -ansi-alias -opt-ra-region-strategy=routine
                -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap

    483.xalancbmk: basepeak = yes

SPEC CINT2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

PRIMERGY RX100 S5, Intel Xeon X3380, 3.16 GHz

SPECint_rate2006 = 87.4

SPECint_rate_base2006 = 81.6

CPU2006 license: 19	**Test date:** Feb-2009
Test sponsor: Fujitsu	**Hardware Availability:** Apr-2009
Tested by: Fujitsu Siemens Computers	**Software Availability:** Nov-2008

Pey Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.04.html
http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090710.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.04.xml
http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090710.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Tue Jul 22 22:29:58 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 17 March 2009.