



# SPEC® CINT2006 Result

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## Fujitsu Siemens Computers

### SPECint®\_rate2006 = 81.3

PRIMERGY RX100 S5, Intel Xeon L3360, 2.83 GHz

### SPECint\_rate\_base2006 = 75.9

CPU2006 license: 22

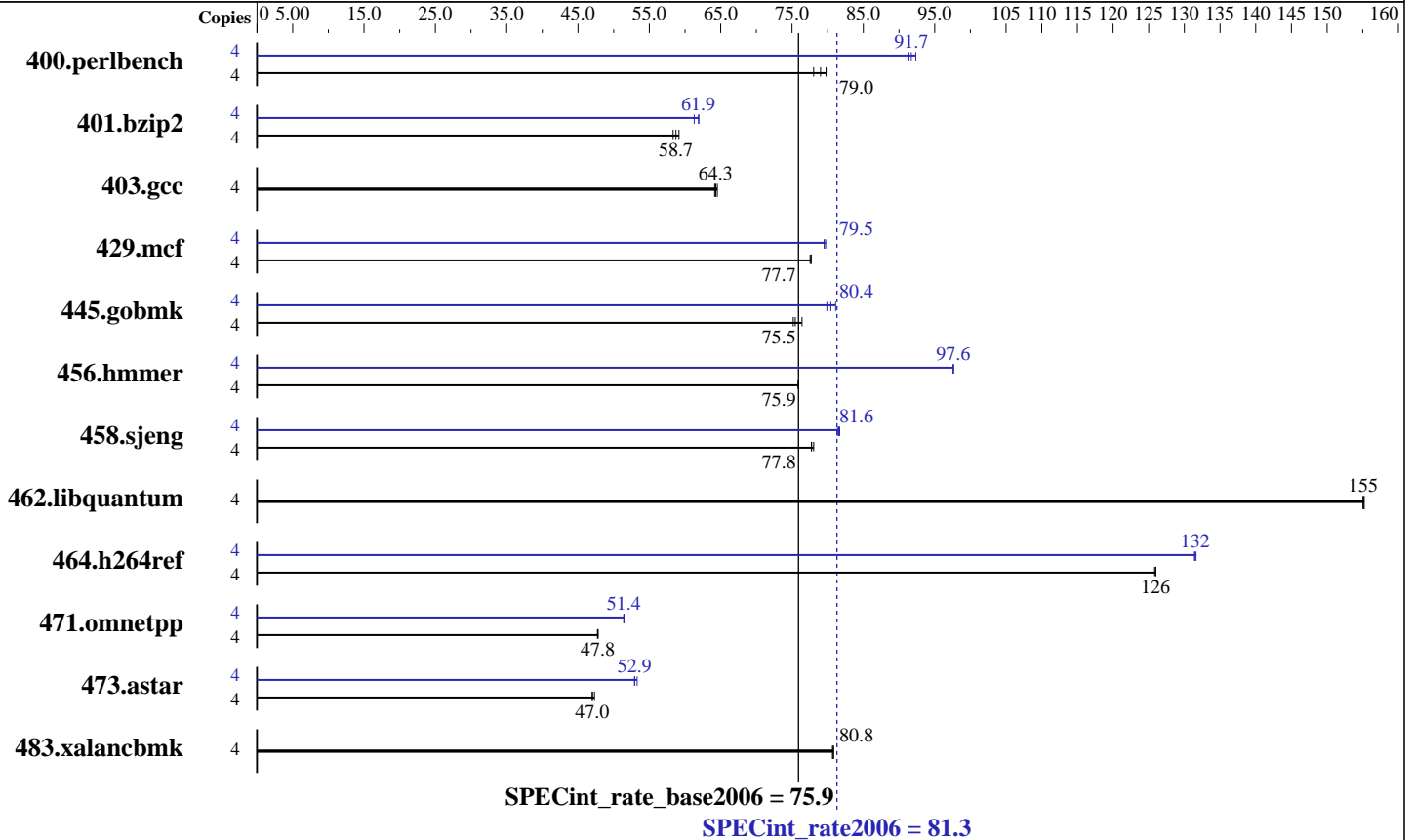
Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

Test date: Jan-2009

Hardware Availability: Apr-2009

Software Availability: Nov-2008



### Hardware

CPU Name: Intel Xeon L3360  
 CPU Characteristics: 1333 MHz system bus  
 CPU MHz: 2833  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores  
 L3 Cache: None  
 Other Cache: None  
 Memory: 8 GB (4x2 GB PC2-6400E, 2 rank, CL6-6-6, ECC)  
 Disk Subsystem: 1x SATA, 160 GB, 7200 rpm  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 10 (x86\_64) SP2, Kernel 2.6.16.60-0.21-smp  
 Compiler: Intel C++ Compiler 11.0 for Linux Build 20080730 Package ID: l\_cproc\_b\_11.0.066  
 Auto Parallel: No  
 File System: ext3  
 System State: Multi-User Run Level 3  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap Library, Version 8.1 Binutils 2.18.50.0.7.20080502



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	501	78.0	<b>495</b>	<b>79.0</b>	490	79.8	4	428	91.4	<b>426</b>	<b>91.7</b>	423	92.4
401.bzip2	4	652	59.2	662	58.3	<b>657</b>	<b>58.7</b>	4	623	61.9	<b>624</b>	<b>61.9</b>	630	61.3
403.gcc	4	<b>501</b>	<b>64.3</b>	499	64.5	502	64.2	4	<b>501</b>	<b>64.3</b>	499	64.5	502	64.2
429.mcf	4	<b>470</b>	<b>77.7</b>	470	77.5	470	77.7	4	458	79.7	<b>459</b>	<b>79.5</b>	459	79.5
445.gobmk	4	558	75.2	<b>556</b>	<b>75.5</b>	549	76.4	4	517	81.1	525	79.9	<b>522</b>	<b>80.4</b>
456.hammer	4	492	75.9	<b>492</b>	<b>75.9</b>	492	75.9	4	382	97.6	<b>382</b>	<b>97.6</b>	382	97.6
458.sjeng	4	620	78.0	<b>622</b>	<b>77.8</b>	623	77.7	4	<b>593</b>	<b>81.6</b>	595	81.4	593	81.7
462.libquantum	4	534	155	535	155	<b>534</b>	<b>155</b>	4	534	155	535	155	<b>534</b>	<b>155</b>
464.h264ref	4	<b>703</b>	<b>126</b>	703	126	703	126	4	<b>673</b>	<b>132</b>	672	132	674	131
471.omnetpp	4	524	47.7	523	47.8	<b>523</b>	<b>47.8</b>	4	486	51.4	486	51.4	<b>486</b>	<b>51.4</b>
473.astar	4	594	47.3	598	47.0	<b>597</b>	<b>47.0</b>	4	527	53.3	531	52.9	<b>530</b>	<b>52.9</b>
483.xalancbmk	4	341	80.8	342	80.7	<b>342</b>	<b>80.8</b>	4	341	80.8	342	80.7	<b>342</b>	<b>80.8</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
taskset has been used to bind processes to cores

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

## Platform Notes

BIOS configuration:  
Hardware Prefetch = Disable, Adjacent Sector Prefetch = Disable

## General Notes

For information about Fujitsu Siemens Computers please see:  
<http://www.fujitsu-siemens.com>

## Base Compiler Invocation

C benchmarks:  
icc

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## Base Compiler Invocation (Continued)

C++ benchmarks:  
icpc

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xSSE4.1 -ipo -O3 -no-prec-div -static -inline-alloc  
-opt-malloc-options=3 -opt-prefetch

C++ benchmarks:  
-xSSE4.1 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/spec/cpu2006.1.1/lib -lsmartheap

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc

401.bzip2: /opt/intel/Compiler/11.0/066/bin/intel64/icc  
456.hmmer: /opt/intel/Compiler/11.0/066/bin/intel64/icc

C++ benchmarks:  
icpc



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## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
 401.bzip2: -DSPEC\_CPU\_LP64  
 456.hmmer: -DSPEC\_CPU\_LP64  
 462.libquantum: -DSPEC\_CPU\_LINUX  
 483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
 -no-prec-div -static -ansi-alias -opt-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
 -no-prec-div -static -opt-prefetch -ansi-alias

403.gcc: basepeak = yes

429.mcf: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
 -no-prec-div -static -opt-prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -O2 -ipo  
 -no-prec-div -ansi-alias

456.hmmer: -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2  
 -ansi-alias

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
 -no-prec-div -static -unroll4

462.libquantum: basepeak = yes

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
 -no-prec-div -static -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
 -no-prec-div -ansi-alias -opt-ra-region-strategy=block  
 -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
 -no-prec-div -ansi-alias -opt-ra-region-strategy=routine  
 -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap

483.xalancbmk: basepeak = yes



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## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.04.html>

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090710.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.04.xml>

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090710.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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