



# SPEC® CINT2006 Result

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## Acer Incorporated

## SPECint®\_rate2006 = 89.5

## Acer Altos R720 (Intel Xeon X5270)

## SPECint\_rate\_base2006 = 83.8

CPU2006 license: 97

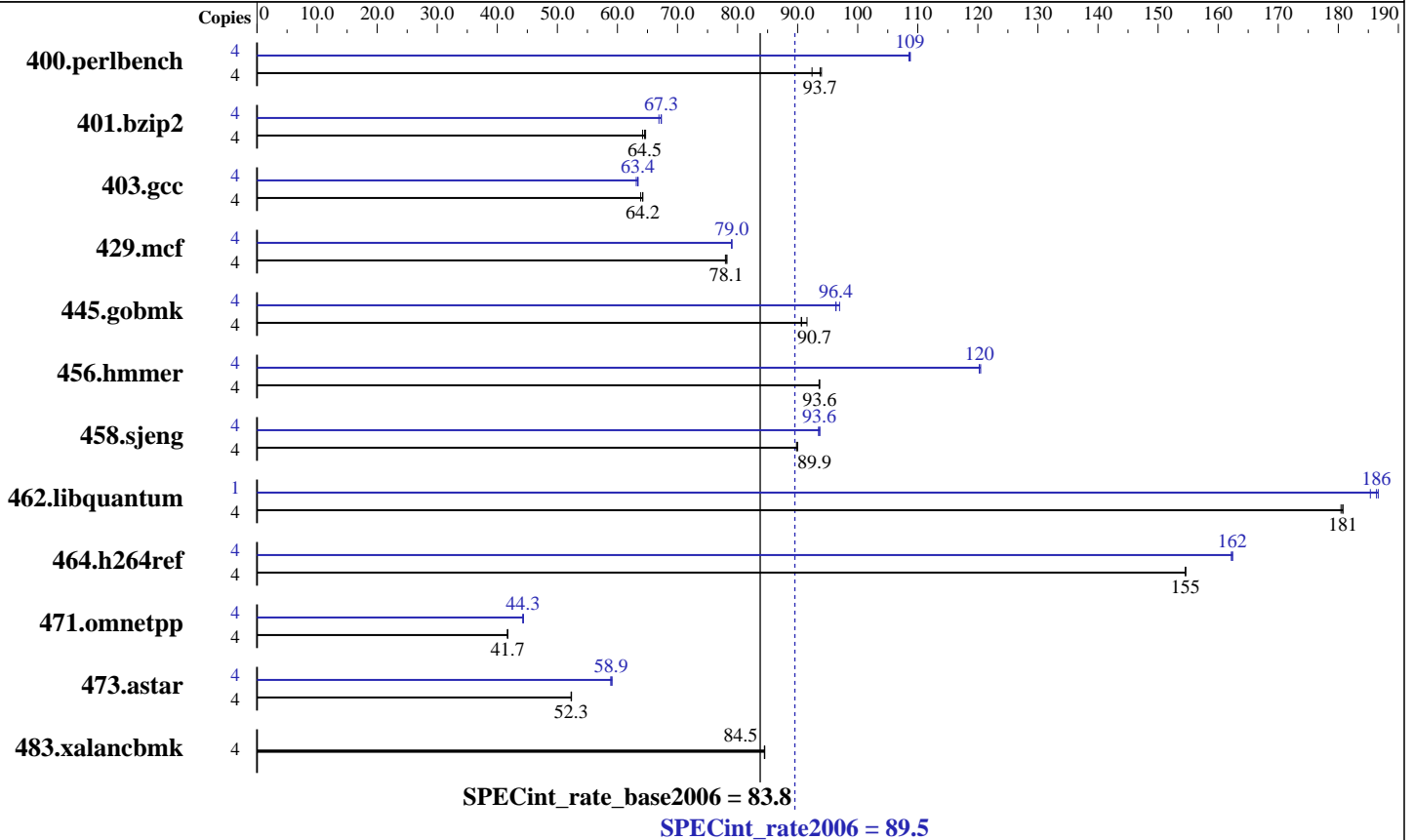
Test sponsor: Acer Incorporated

Tested by: Acer Incorporated

Test date: Dec-2008

Hardware Availability: Sep-2008

Software Availability: Nov-2008



### Hardware

CPU Name: Intel Xeon X5270  
 CPU Characteristics: 1333MHz system bus  
 CPU MHz: 3500  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip  
 CPU(s) orderable: 1, 2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 6 MB I+D on chip per chip  
 L3 Cache: None  
 Other Cache: None  
 Memory: 16 GB (8 x 2048MB ECC FB-DIMM DDR2-667 CL5-5-5)  
 Disk Subsystem: 1 x 73GB 10K RPM SAS HDD  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 10 (x86\_64) SP1, Kernel 2.6.16.46-0.12-smp  
 Compiler: Intel C++ Compiler 11.0 for Linux Build 20080730 Package ID: l\_cproc\_b\_11.0.042  
 Auto Parallel: Yes  
 File System: ReiserFS  
 System State: Multi-user, level 3  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502



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## Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	423	92.4	416	94.0	<b>417</b>	<b>93.7</b>	4	360	109	359	109	<b>360</b>	<b>109</b>
401.bzip2	4	597	64.7	<b>598</b>	<b>64.5</b>	601	64.2	4	573	67.3	577	66.9	<b>574</b>	<b>67.3</b>
403.gcc	4	504	63.9	<b>502</b>	<b>64.2</b>	501	64.2	4	<b>508</b>	<b>63.4</b>	508	63.4	510	63.1
429.mcf	4	466	78.2	<b>467</b>	<b>78.1</b>	468	78.0	4	462	79.0	<b>462</b>	<b>79.0</b>	462	79.0
445.gobmk	4	<b>463</b>	<b>90.7</b>	458	91.6	463	90.6	4	<b>435</b>	<b>96.4</b>	435	96.4	433	97.0
456.hammer	4	<b>399</b>	<b>93.6</b>	399	93.6	398	93.7	4	310	120	310	120	<b>310</b>	<b>120</b>
458.sjeng	4	<b>538</b>	<b>89.9</b>	539	89.8	538	90.0	4	517	93.7	518	93.5	<b>517</b>	<b>93.6</b>
462.libquantum	4	458	181	459	180	<b>459</b>	<b>181</b>	1	112	185	<b>111</b>	<b>186</b>	111	187
464.h264ref	4	573	155	573	155	<b>573</b>	<b>155</b>	4	546	162	<b>545</b>	<b>162</b>	545	162
471.omnetpp	4	599	41.7	<b>599</b>	<b>41.7</b>	599	41.7	4	564	44.4	<b>565</b>	<b>44.3</b>	565	44.3
473.astar	4	536	52.4	<b>537</b>	<b>52.3</b>	537	52.3	4	477	58.9	475	59.1	<b>476</b>	<b>58.9</b>
483.xalancbmk	4	<b>327</b>	<b>84.5</b>	327	84.5	327	84.5	4	<b>327</b>	<b>84.5</b>	327	84.5	327	84.5

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
taskset was used to bind processes to cores except  
for 462.libquantum peak

## Platform Notes

BIOS Settings  
High Bandwidth Optimization set to ON

## General Notes

OMP\_NUM\_THREADS set to number of cores  
KMP\_AFFINITY set to "physical,0"  
KMP\_STACKSIZE set to 64M  
'ulimit -s unlimited' was set for stacksize unlimited  
This result was measured on the Acer Altos R520.  
The Altos R720 and Altos R520 are electronically equivalent.

## Base Compiler Invocation

C benchmarks:  
icc

C++ benchmarks:  
icpc



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## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -static -inline-calloc  
-opt-malloc-options=3 -opt-prefetch

C++ benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/spec/cpu2006.1.1/lib -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/Compiler/11.0/042/bin/intel64/icc  
-L/opt/intel/Compiler/11.0/042/ipp/em64t/lib  
-I/opt/intel/Compiler/11.0/042/ipp/em64t/include

456.hmmer: /opt/intel/Compiler/11.0/042/bin/intel64/icc  
-L/opt/intel/Compiler/11.0/042/ipp/em64t/lib  
-I/opt/intel/Compiler/11.0/042/ipp/em64t/include

C++ benchmarks:

icpc

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64

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## Peak Portability Flags (Continued)

462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -static -ansi-alias -opt-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -static -opt-prefetch -ansi-alias

403.gcc: -xSSE4.1 -ipo -O3 -no-prec-div -static -inline-calloc  
-opt-malloc-options=3

429.mcf: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -static -opt-prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -O2 -ipo  
-no-prec-div -ansi-alias

456.hmmer: -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2  
-ansi-alias

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -static -unroll4

462.libquantum: -xSSE4.1 -ipo -O3 -no-prec-div -static  
-opt-malloc-options=3 -parallel -par-runtime-control  
-opt-prefetch

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -static -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -ansi-alias -opt-ra-region-strategy=block  
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine  
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap

483.xalancbmk: basepeak = yes



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## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.04.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.04.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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