SPEC® CINT2006 Result

Unisys Corporation

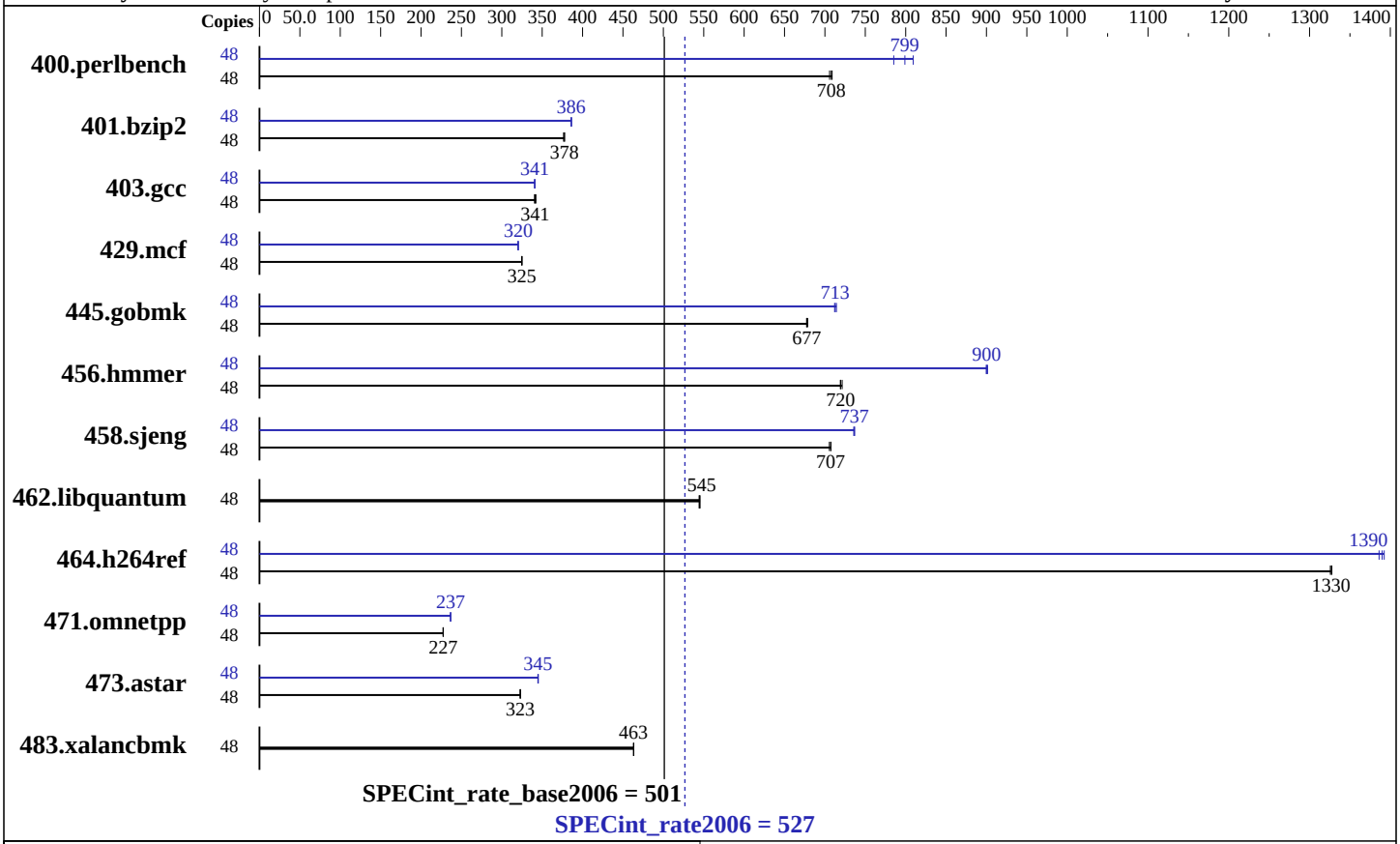
Unisys ES7000 Model 7600R, Intel Xeon X7460, 2.66GHz

SPECint®_rate2006 = 527

SPECint_rate_base2006 = 501

CPU2006 license: 15
Test sponsor: Unisys Corporation
Tested by: Unisys Corporation

Test date: Nov-2008
Hardware Availability: Nov-2008
Software Availability: Jan-2009



Hardware

CPU Name:	Intel Xeon X7460
CPU Characteristics:	
CPU MHz:	2667
FPU:	Integrated
CPU(s) enabled:	48 cores, 8 chips, 6 cores/chip
CPU(s) orderable:	4,8,12,16 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	9 MB I+D on chip per chip, 3 MB shared / 2 cores
L3 Cache:	16 MB I+D on chip per chip
Other Cache:	None
Memory:	64 GB (16x4GB,2 rank, PC2-5300F-555-11-E0)
Disk Subsystem:	2 x 146 GB SAS,15000 RPM
Other Hardware:	None

Software

Operating System:	SuSe Linux Enterprise Server 10 (x86_64) SP2(Kernel 2.6.16-60.0.21-smp)
Compiler:	Intel C++ Compiler 11.0 for Linux (Build 20080730) Package ID: l_cproc_b_11.0.042
Auto Parallel:	No
File System:	ReiserFS
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	48	**662**	**708**	664	706	661	709	48	597	785	**587**	**799**	579	809
401.bzip2	48	**1227**	**378**	1230	376	1226	378	48	1201	386	1199	386	**1200**	**386**
403.gcc	48	1129	342	1135	340	**1132**	**341**	48	1135	341	1134	341	**1134**	**341**
429.mcf	48	**1347**	**325**	1347	325	1349	325	48	**1366**	**320**	1366	320	1368	320
445.gobmk	48	743	677	742	679	**743**	**677**	48	**706**	**713**	707	712	705	714
456.hmmer	48	**622**	**720**	621	721	623	719	48	498	900	**497**	**900**	497	901
458.sjeng	48	**821**	**707**	821	707	823	706	48	788	737	**788**	**737**	789	736
462.libquantum	48	1827	544	1823	545	**1825**	**545**	48	1827	544	1823	545	**1825**	**545**
464.h264ref	48	**801**	**1330**	801	1330	800	1330	48	763	1390	766	1390	**764**	**1390**
471.omnetpp	48	1319	228	**1319**	**227**	1320	227	48	1268	237	**1268**	**237**	1268	237
473.astar	48	1043	323	**1044**	**323**	1044	323	48	978	345	**977**	**345**	976	345
483.xalancbmk	48	715	463	**715**	**463**	715	463	48	715	463	**715**	**463**	715	463

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
 taskset was used to bind processes to cores.
```

Operating System Notes

```
'ulimit -s unlimited' allows stack to grow to system limit.

/proc/sys/vm/zone_reclaim_mode=1 to enable zone_reclaim.
```

Base Compiler Invocation

C benchmarks:
`icc`

C++ benchmarks:
`icpc`

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
462.libquantum: `-DSPEC_CPU_LINUX`

Continued on next page

Base Portability Flags (Continued)

483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xSSE4.1 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3 -opt-prefetch

C++ benchmarks:
-xSSE4.1 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/Compiler/11.0/066/bin/intel64/icc

456.hmmer: /opt/intel/Compiler/11.0/066/bin/intel64/icc

C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xSSE4.1 -ipo -O3 -no-prec-div -static -ansi-alias -opt-prefetch`

401.bzip2: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias`

403.gcc: `-xSSE4.1 -ipo -O3 -no-prec-div -static -inline-calloc -opt-malloc-options=3`

429.mcf: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch`

445.gobmk: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xSSE4.1 -O2 -ipo -no-prec-div -ansi-alias`

456.hmmer: `-xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2 -ansi-alias`

458.sjeng: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xSSE4.1 -ipo -O3 -no-prec-div -static -unroll4`

462.libquantum: `basepeak = yes`

464.h264ref: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2 -ansi-alias`

C++ benchmarks:

471.omnetpp: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xSSE4.1 -ipo -O3 -no-prec-div -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap`

473.astar: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xSSE4.1 -ipo -O3 -no-prec-div -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap`

483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at

http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.01.html
http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.12.html

You can also download the XML flags sources by saving the following links:

http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.01.xml
http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.12.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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