



SPEC® CINT2006 Result

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Hewlett-Packard Company

SPECint®_rate2006 = 138

ProLiant BL460c G5
(3.0 GHz, Intel Xeon E5450)

SPECint_rate_base2006 = 129

CPU2006 license: 3

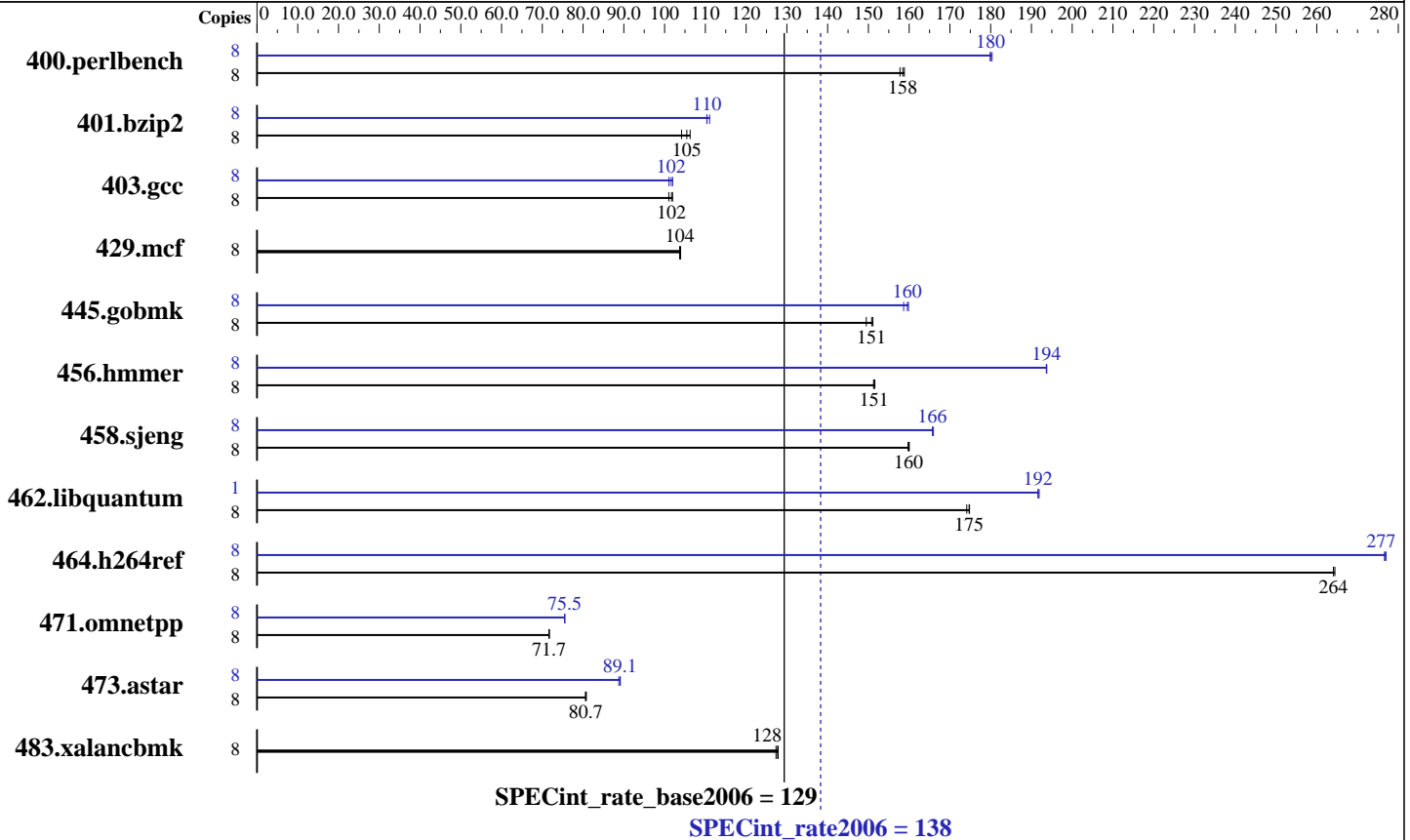
Test date: Dec-2008

Test sponsor: Hewlett-Packard Company

Hardware Availability: Nov-2008

Tested by: Hewlett-Packard Company

Software Availability: Nov-2008



Hardware

CPU Name: Intel Xeon E5450
 CPU Characteristics: 3.0 GHz, 2x6 MB L2 shared, 1333 MHz system bus
 CPU MHz: 3000
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (4x4 GB PC2-5300F CL5)
 Disk Subsystem: 1x72 GB 15 K SAS
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
 Compiler: Intel C++ Compiler 11.0 for Linux Build 20080930 Package ID: l_cproc_b_11.0.069
 Auto Parallel: Yes
 File System: ext3
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: MicroQuill SmartHeap Library 8.1 binutils-2.17.50



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	495	158	<u>493</u>	<u>158</u>	492	159	8	<u>434</u>	<u>180</u>	435	180	433	180
401.bzip2	8	741	104	726	106	<u>732</u>	<u>105</u>	8	699	110	<u>699</u>	<u>110</u>	695	111
403.gcc	8	632	102	<u>633</u>	<u>102</u>	637	101	8	637	101	<u>634</u>	<u>102</u>	631	102
429.mcf	8	704	104	<u>703</u>	<u>104</u>	702	104	8	704	104	<u>703</u>	<u>104</u>	702	104
445.gobmk	8	555	151	<u>557</u>	<u>151</u>	561	149	8	529	159	525	160	<u>526</u>	<u>160</u>
456.hammer	8	<u>493</u>	<u>151</u>	493	151	492	152	8	385	194	<u>385</u>	<u>194</u>	385	194
458.sjeng	8	606	160	<u>605</u>	<u>160</u>	605	160	8	583	166	<u>584</u>	<u>166</u>	584	166
462.libquantum	8	952	174	<u>949</u>	<u>175</u>	948	175	1	108	192	<u>108</u>	<u>192</u>	108	192
464.h264ref	8	<u>670</u>	<u>264</u>	669	264	670	264	8	640	277	<u>640</u>	<u>277</u>	639	277
471.omnetpp	8	<u>698</u>	<u>71.7</u>	698	71.7	697	71.7	8	<u>662</u>	<u>75.5</u>	662	75.5	662	75.5
473.astar	8	697	80.6	<u>696</u>	<u>80.7</u>	695	80.8	8	<u>631</u>	<u>89.1</u>	630	89.2	633	88.8
483.xalancbmk	8	<u>432</u>	<u>128</u>	432	128	434	127	8	<u>432</u>	<u>128</u>	432	128	434	127

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
taskset was used to bind processes to cores except
for 462.libquantum peak

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to physical,0
KMP_STACKSIZE set to 64M

Platform Notes

BIOS configuration:
Power Regulator set to Static High Performance Mode
Adjacent Sector Prefetch Disabled

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc



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Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3 -opt-prefetch

C++ benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/cpu2006/SmartHeap_8.1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/Compiler/11.0/069/bin/intel64/icc
-L/opt/intel/Compiler/11.0/069/ipp/em64t/lib
-I/opt/intel/Compiler/11.0/069/ipp/em64t/include

456.hmmer: /opt/intel/Compiler/11.0/069/bin/intel64/icc
-L/opt/intel/Compiler/11.0/069/ipp/em64t/lib
-I/opt/intel/Compiler/11.0/069/ipp/em64t/include

C++ benchmarks:

icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64

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Peak Portability Flags (Continued)

462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -ansi-alias -opt-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -opt-prefetch -ansi-alias

403.gcc: -xSSE4.1 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3

429.mcf: basepeak = yes

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -O2 -ipo
-no-prec-div -ansi-alias

456.hmmer: -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2
-ansi-alias

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll4

462.libquantum: -xSSE4.1 -ipo -O3 -no-prec-div -static
-opt-malloc-options=3 -parallel -par-runtime-control
-opt-prefetch

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs -L/cpu2006/SmartHeap_8.1/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs -L/cpu2006/SmartHeap_8.1/lib -lsmartheap

483.xalancbmk: basepeak = yes



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Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revD.html>

<http://www.spec.org/cpu2006/flags/HP-Intel-Linux-Settings-flags.20090713.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revD.xml>

<http://www.spec.org/cpu2006/flags/HP-Intel-Linux-Settings-flags.20090713.xml>

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For other inquiries, please contact webmaster@spec.org.

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