



SPEC® CINT2006 Result

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Acer Incorporated

SPECint®_rate2006 = Not Run

Acer Altos R520 (Intel Xeon X5470)

SPECint_rate_base2006 = 74.7

CPU2006 license: 97

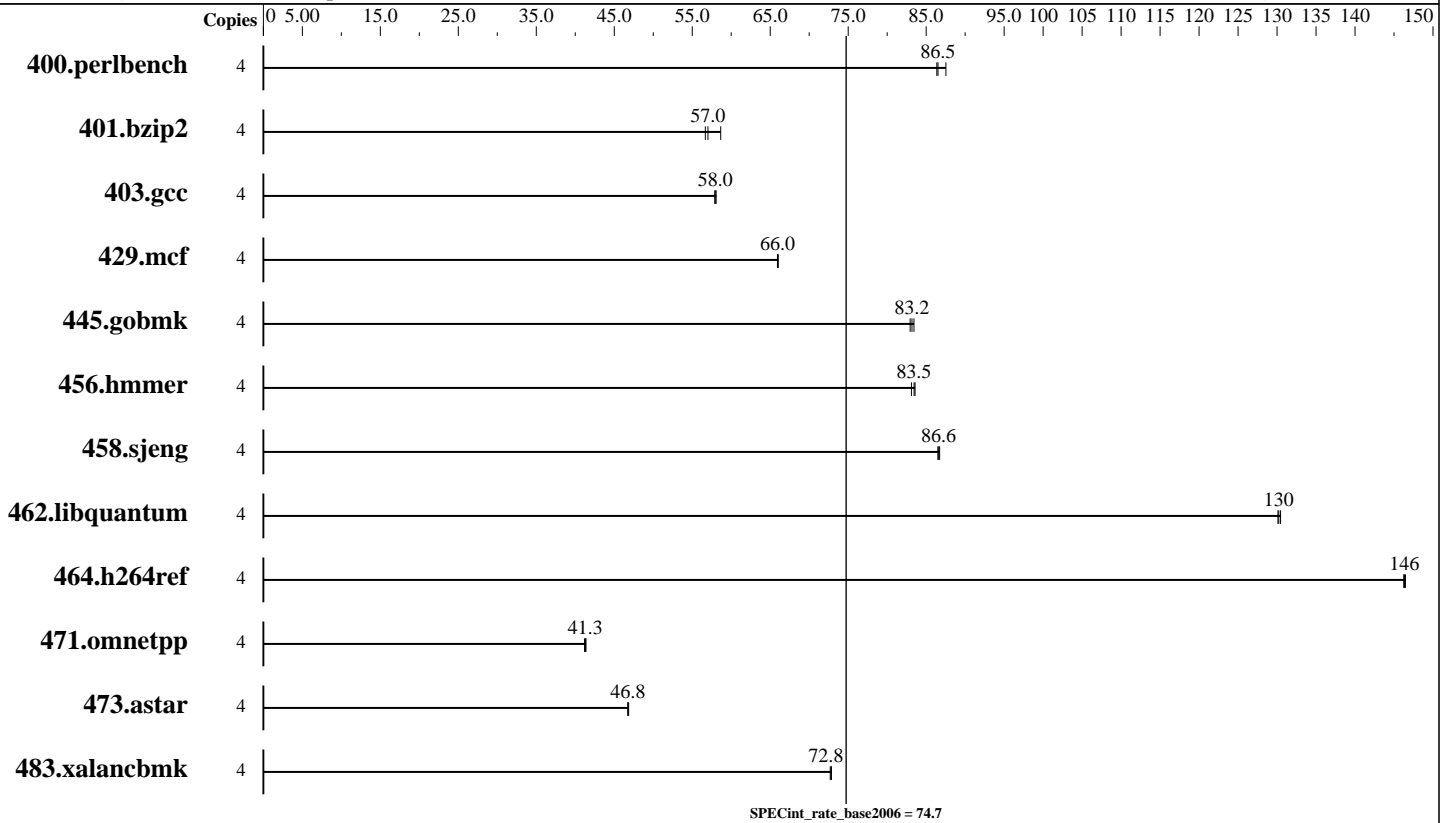
Test sponsor: Acer Incorporated

Tested by: Acer Incorporated

Test date: Dec-2008

Hardware Availability: Sep-2008

Software Availability: Nov-2008



Hardware

CPU Name: Intel Xeon X5470
 CPU Characteristics: 1333MHz system bus
 CPU MHz: 3333
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
 CPU(s) orderable: 1, 2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (8 x 2048MB ECC FB-DIMM DDR2-667 CL5-5-5)
 Disk Subsystem: 1 x 73GB 10K RPM SAS HDD
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
 Compiler: Intel C++ Compiler 11.0 for Linux Build 20080930 Package ID: l_cproc_p_11.0.066
 Auto Parallel: No
 File System: ReiserFS
 System State: Multi-user, level 3
 Base Pointers: 32-bit
 Peak Pointers: Not Applicable
 Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502



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Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio		
400.perlbench	4	446	87.5	452	86.4	452	86.5									
401.bzip2	4	658	58.7	677	57.0	681	56.7									
403.gcc	4	555	58.1	556	57.9	556	58.0									
429.mcf	4	552	66.0	553	66.0	553	65.9									
445.gobmk	4	506	82.9	503	83.4	504	83.2									
456.hammer	4	449	83.1	447	83.5	447	83.6									
458.sjeng	4	559	86.6	558	86.7	560	86.5									
462.libquantum	4	635	130	636	130	637	130									
464.h264ref	4	605	146	605	146	604	146									
471.omnetpp	4	607	41.2	604	41.4	605	41.3									
473.astar	4	601	46.7	599	46.8	600	46.8									
483.xalancbmk	4	379	72.8	380	72.7	379	72.8									

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

Platform Notes

BIOS Settings
Adjacent Cache Line Prefetch disabled
Hardware Prefetch disabled

General Notes

taskset was used to bind processes to cores except for 462.libquantum peak
OMP_NUM_THREADS set to number of processors
KMP_AFFINITY set to "physical,0"
KMP_STACKSIZE set to 64M
'ulimit -s unlimited' was set for stacksize unlimited
This result was measured on the Acer Altos R520.
The Altos R720 and Altos R520 are electronically equivalent.

Base Compiler Invocation

C benchmarks:
icc

Continued on next page



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Base Compiler Invocation (Continued)

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xSSE4.1 -ipo -O3 -no-prec-div -static -inline-alloc
-opt-malloc-options=3 -opt-prefetch

C++ benchmarks:
-xSSE4.1 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.04.html>
<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090710.00.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.04.xml>
<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090710.00.xml>



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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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