



SPEC® CFP2006 Result

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NEC Corporation

Express5800/320Fd-MR
(Intel Xeon E5450)

SPECfp®_rate2006 = 43.7

SPECfp_rate_base2006 = 41.9

CPU2006 license: 9006

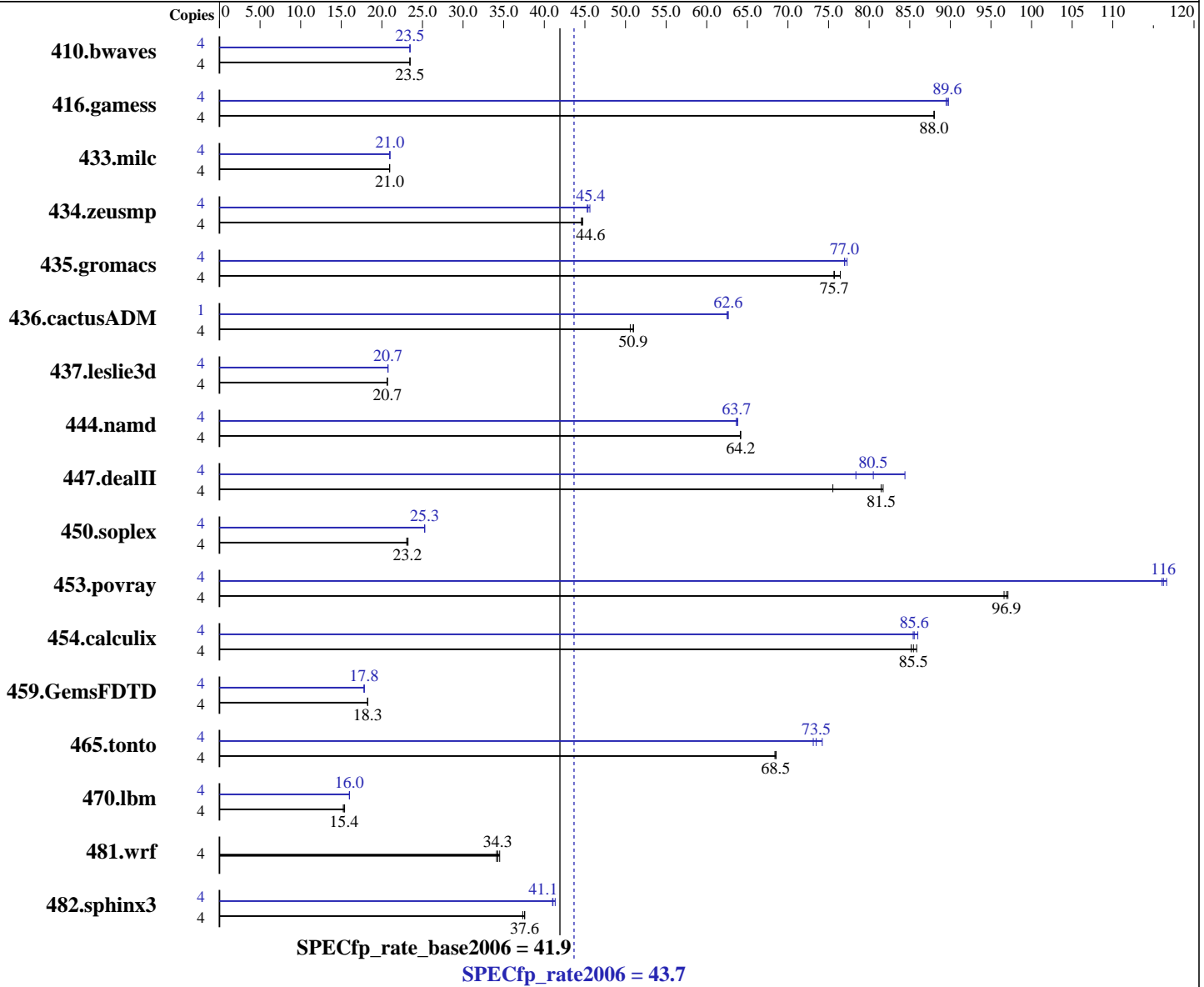
Test sponsor: NEC Corporation

Tested by: NEC Corporation

Test date: Dec-2008

Hardware Availability: Oct-2008

Software Availability: Nov-2008



Hardware

CPU Name: Intel Xeon E5450
 CPU Characteristics: 1333 MHz system bus
 CPU MHz: 3000
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
 CPU(s) orderable: 1,2 chips (fault tolerant, see Platform Notes)
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores

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Software

Operating System: Red Hat Enterprise Linux Server release 5.2
 Advanced Platform, Kernel 2.6.18-92.1.13.el5 on an x86_64
 Compiler: Intel C++ and Fortran Compiler 11.0 for Linux
 Build 20081105 Package ID: l_cproc_p_11.0.074, l_cprof_p_11.0.074
 Auto Parallel: Yes
 File System: ext3
 System State: Run level 3 (multi-user)

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L3 Cache: None
Other Cache: None
Memory: 24 GB (6x4 GB PC2-5300F, 2 rank, CL5-5-5, ECC)
Disk Subsystem: 2x146.5 GB SAS, 15000 RPM, Software RAID Level1
Other Hardware: None

Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: ft Server Control Software 6.0.2-198

Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio		
410.bwaves	4	2318	23.5	2317	23.5	<u>2317</u>	<u>23.5</u>	4	2318	23.4	<u>2317</u>	<u>23.5</u>	2316	23.5		
416.gamess	4	890	88.0	<u>890</u>	<u>88.0</u>	890	88.0	4	872	89.8	<u>874</u>	<u>89.6</u>	875	89.5		
433.milc	4	1753	20.9	<u>1752</u>	<u>21.0</u>	1752	21.0	4	1750	21.0	1750	21.0	<u>1750</u>	<u>21.0</u>		
434.zeusmp	4	816	44.6	814	44.7	<u>816</u>	<u>44.6</u>	4	<u>802</u>	<u>45.4</u>	798	45.6	804	45.3		
435.gromacs	4	374	76.5	377	75.7	<u>377</u>	<u>75.7</u>	4	371	77.0	<u>371</u>	<u>77.0</u>	370	77.3		
436.cactusADM	4	945	50.6	<u>938</u>	<u>50.9</u>	937	51.0	1	191	62.6	191	62.5	<u>191</u>	<u>62.6</u>		
437.leslie3d	4	1818	20.7	<u>1818</u>	<u>20.7</u>	1821	20.6	4	<u>1812</u>	<u>20.7</u>	1812	20.8	1812	20.7		
444.namd	4	<u>500</u>	<u>64.2</u>	500	64.2	500	64.1	4	<u>504</u>	<u>63.7</u>	504	63.7	503	63.8		
447.dealII	4	<u>562</u>	<u>81.5</u>	560	81.7	606	75.5	4	542	84.4	<u>568</u>	<u>80.5</u>	584	78.4		
450.soplex	4	1447	23.1	<u>1441</u>	<u>23.2</u>	1437	23.2	4	1320	25.3	1321	25.3	<u>1320</u>	<u>25.3</u>		
453.povray	4	<u>220</u>	<u>96.9</u>	219	97.1	220	96.6	4	183	116	182	117	<u>183</u>	<u>116</u>		
454.calculix	4	<u>386</u>	<u>85.5</u>	387	85.2	384	85.8	4	386	85.4	384	86.0	<u>386</u>	<u>85.6</u>		
459.GemsFDTD	4	2327	18.2	<u>2324</u>	<u>18.3</u>	2323	18.3	4	2383	17.8	2381	17.8	<u>2381</u>	<u>17.8</u>		
465.tonto	4	574	68.5	575	68.4	<u>575</u>	<u>68.5</u>	4	538	73.1	530	74.2	<u>536</u>	<u>73.5</u>		
470.lbm	4	3603	15.3	<u>3578</u>	<u>15.4</u>	3566	15.4	4	3436	16.0	<u>3433</u>	<u>16.0</u>	3430	16.0		
481.wrf	4	<u>1304</u>	<u>34.3</u>	1295	34.5	1309	34.1	4	<u>1304</u>	<u>34.3</u>	1295	34.5	1309	34.1		
482.sphinx3	4	2087	37.3	2074	37.6	<u>2074</u>	<u>37.6</u>	4	1901	41.0	1886	41.3	<u>1898</u>	<u>41.1</u>		

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
taskset was used to bind processes to cores except
for 436.cactusADM peak

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to "physical,0"
KMP_STACKSIZE set to 64M



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Hardware Availability: Oct-2008

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Platform Notes

This Express5800/320Fd-MR is a fault-tolerant server. Two modules are installed in this server. Each module physically has "1CPU chips,24GB memory", The total physical configuration is "2CPU chips,48GB memory". Using fault-tolerant lockstep technology, these two modules communicate with each other and execute the same instructions at the same time, The operating system only sees "1CPU chips,24GB memory" as the other components add only redundancy and do not contribute to any performance benefit.

General Notes

The NEC Express5800/320Fd-MR(Intel Xeon E5450) and the Bull NovaScale R630 E1 MR(Intel Xeon E5450, 3.00 GHz) models are electronically equivalent. The results have been measured on a NEC Express5800/320Fd-MR(Intel Xeon E5450) model.

Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.deallI: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64

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Base Portability Flags (Continued)

481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

C++ benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

Fortran benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

Benchmarks using both Fortran and C:

-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

482.sphinx3: /opt/intel/Compiler/11.0/074/bin/ia32/icc
-L/opt/intel/Compiler/11.0/074/ipp/ia32/lib
-I/opt/intel/Compiler/11.0/074/ipp/ia32/include

C++ benchmarks (except as noted below):

icpc

450.soplex: /opt/intel/Compiler/11.0/074/bin/ia32/icpc
-L/opt/intel/Compiler/11.0/074/ipp/ia32/lib
-I/opt/intel/Compiler/11.0/074/ipp/ia32/include

Fortran benchmarks (except as noted below):

ifort

437.leslie3d: /opt/intel/Compiler/11.0/074/bin/ia32/ifort
-L/opt/intel/Compiler/11.0/074/ipp/ia32/lib
-I/opt/intel/Compiler/11.0/074/ipp/ia32/include

Benchmarks using both Fortran and C:

icc ifort



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Peak Portability Flags

```

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

```

Peak Optimization Flags

C benchmarks:

```

433.milc: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
         -no-prec-div -static -fno-alias

```

```

470.lbm: -xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch
         -auto-ilp32

```

```

482.sphinx3: -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2

```

C++ benchmarks:

```

444.namd: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
         -no-prec-div -static -fno-alias -auto-ilp32

```

```

447.dealII: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
          -no-prec-div -static -unroll2 -ansi-alias -scalar-rep-

```

```

450.soplex: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
          -no-prec-div -static -opt-malloc-options=3

```

```

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
          -no-prec-div -static -unroll4 -ansi-alias

```

Fortran benchmarks:

```

410.bwaves: -xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

```

```

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
          -no-prec-div -static -unroll2 -Ob0 -ansi-alias
          -scalar-rep-

```

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Peak Optimization Flags (Continued)

434.zeusmp: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static

437.leslie3d: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -opt-malloc-options=3 -opt-prefetch

459.GemsFDTD: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll2 -Ob0 -opt-prefetch

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll4 -auto

Benchmarks using both Fortran and C:

435.gromacs: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -opt-prefetch -auto-ilp32

436.cactusADM: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll2 -opt-prefetch -parallel
-auto-ilp32

454.calculix: -xSSE4.1 -ipo -O3 -no-prec-div -static -auto-ilp32

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revD.html>

<http://www.spec.org/cpu2006/flags/NEC-Intel-Linux-Settings-flags-revB.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revD.xml>

<http://www.spec.org/cpu2006/flags/NEC-Intel-Linux-Settings-flags-revB.xml>

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For other inquiries, please contact webmaster@spec.org.

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