



# SPEC® CINT2006 Result

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## NEC Corporation

Express5800/120Rh-1  
(Intel Xeon X5470)

SPECint®\_rate2006 = 144

SPECint\_rate\_base2006 = 130

CPU2006 license: 9006

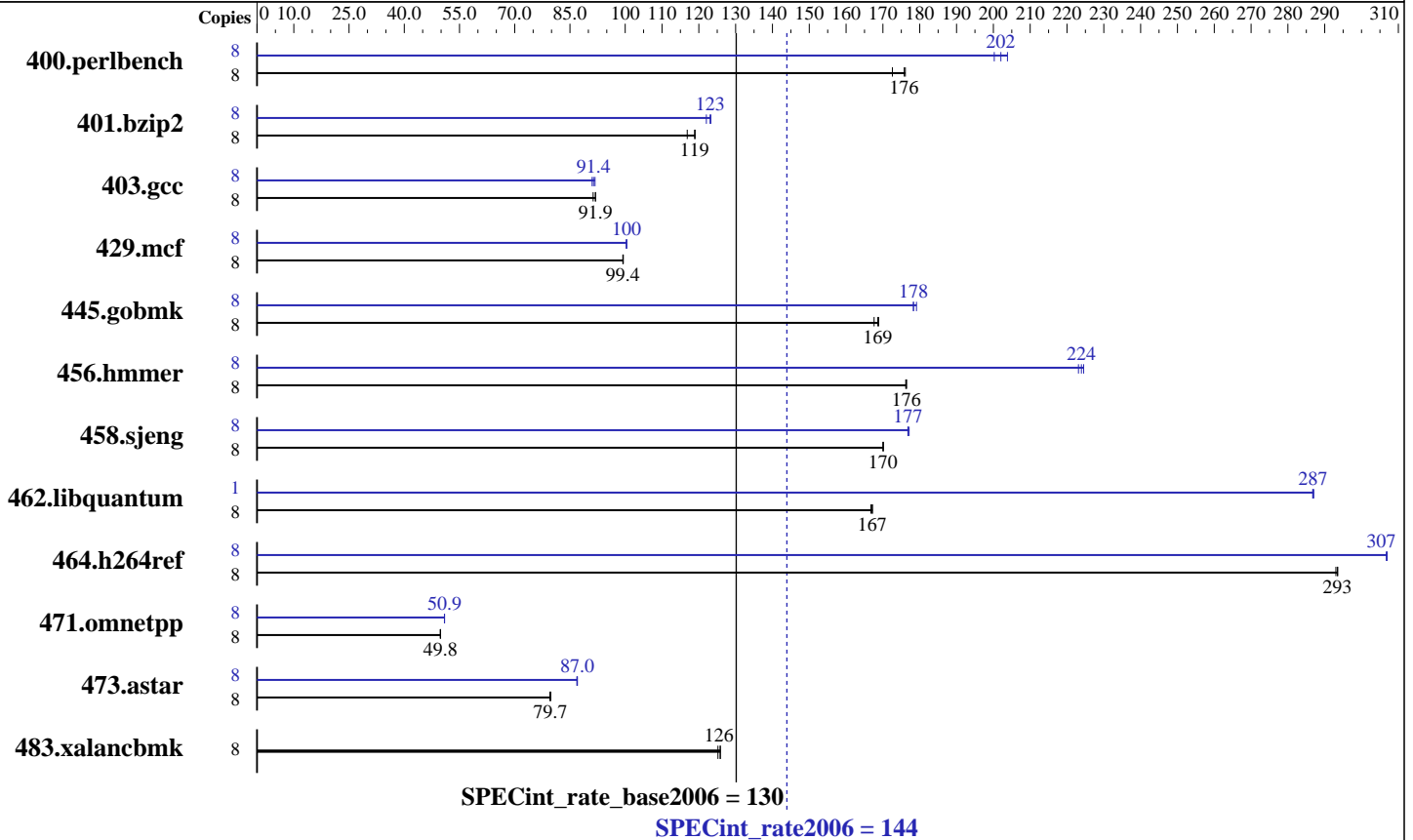
Test sponsor: NEC Corporation

Tested by: NEC Corporation

Test date: Nov-2008

Hardware Availability: Oct-2008

Software Availability: Nov-2008



### Hardware

CPU Name: Intel Xeon X5470  
 CPU Characteristics: 3.33 GHz, 2x6 MB L2 shared, 1333 MHz system bus  
 CPU MHz: 3333  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores  
 L3 Cache: None  
 Other Cache: None  
 Memory: 16 GB (8x2 GB PC2-5300F, 2 rank, CL5-5-5, ECC)  
 Disk Subsystem: 1x146.5 GB SAS, 15000RPM  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 10 (x86\_64) SP2, Kernel 2.6.16.60-0.21-smp  
 Compiler: Intel C++ Compiler 11.0 for Linux Build 20080730 Package ID: l\_cproc\_b\_11.0.044  
 Auto Parallel: Yes  
 File System: ext2  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: MicroQuill SmartHeap Library 8.1 Binutils 2.18.50.0.7.20080502



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	453	173	<b>445</b>	<b>176</b>	444	176	8	383	204	<b>387</b>	<b>202</b>	390	200
401.bzip2	8	<b>650</b>	<b>119</b>	649	119	661	117	8	<b>627</b>	<b>123</b>	626	123	633	122
403.gcc	8	705	91.3	<b>701</b>	<b>91.9</b>	701	91.9	8	<b>705</b>	<b>91.4</b>	708	91.0	701	91.8
429.mcf	8	<b>734</b>	<b>99.4</b>	733	99.5	734	99.4	8	<b>727</b>	<b>100</b>	727	100	727	100
445.gobmk	8	497	169	501	168	<b>498</b>	<b>169</b>	8	471	178	<b>471</b>	<b>178</b>	469	179
456.hammer	8	424	176	<b>423</b>	<b>176</b>	423	176	8	332	225	335	223	<b>333</b>	<b>224</b>
458.sjeng	8	569	170	<b>569</b>	<b>170</b>	569	170	8	547	177	547	177	<b>547</b>	<b>177</b>
462.libquantum	8	992	167	994	167	<b>992</b>	<b>167</b>	1	72.2	287	<b>72.2</b>	<b>287</b>	72.3	287
464.h264ref	8	604	293	603	294	<b>603</b>	<b>293</b>	8	577	307	577	307	<b>577</b>	<b>307</b>
471.omnetpp	8	1003	49.8	<b>1004</b>	<b>49.8</b>	1004	49.8	8	982	50.9	<b>982</b>	<b>50.9</b>	982	50.9
473.astar	8	706	79.5	<b>705</b>	<b>79.7</b>	704	79.8	8	<b>646</b>	<b>87.0</b>	646	86.9	645	87.0
483.xalancbmk	8	<b>439</b>	<b>126</b>	439	126	441	125	8	<b>439</b>	<b>126</b>	439	126	441	125

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
taskset was used to bind processes to cores except  
for 462.libquantum peak

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run  
OMP\_NUM\_THREADS set to number of cores  
KMP\_AFFINITY set to "physical,0"  
KMP\_STACKSIZE set to 64M

## Platform Notes

Bios settings:  
Hardware Prefetcher: Enabled  
Adjacent Cache Line Prefetch: Disabled

## General Notes

The NEC Express5800/120Rh-1(Intel Xeon X5470),  
the NEC Express5800/120Rj-2(Intel Xeon X5470),  
the Bull NovaScale R440 E1(Intel Xeon X5470, 3.33 GHz) and  
the Bull NovaScale R460 E1(Intel Xeon X5470, 3.33 GHz) models are electronically equivalent.  
The results have been measured on a NEC Express5800/120Rj-2(Intel Xeon X5470) model.



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## Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -static -inline-calloc  
-opt-malloc-options=3 -opt-prefetch

C++ benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/opt/SmartHeap\_8.1/lib -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/Compiler/11.0/044/bin/intel64/icc  
-L/opt/intel/Compiler/11.0/044/ipp/em64t/lib  
-I/opt/intel/Compiler/11.0/044/ipp/em64t/include

456.hmmer: /opt/intel/Compiler/11.0/044/bin/intel64/icc  
-L/opt/intel/Compiler/11.0/044/ipp/em64t/lib  
-I/opt/intel/Compiler/11.0/044/ipp/em64t/include

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## Peak Compiler Invocation (Continued)

C++ benchmarks:  
icpc

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -static -ansi-alias -opt-prefetch  
401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -static -opt-prefetch -ansi-alias  
403.gcc: -xSSE4.1 -ipo -O3 -no-prec-div -static -inline-alloc  
-opt-malloc-options=3  
429.mcf: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -static -opt-prefetch  
445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -O2 -ipo  
-no-prec-div -ansi-alias  
456.hmmer: -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2  
-ansi-alias  
458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -static -unroll4  
462.libquantum: -xSSE4.1 -ipo -O3 -no-prec-div -static  
-opt-malloc-options=3 -parallel -par-runtime-control  
-opt-prefetch  
464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -static -unroll2 -ansi-alias

C++ benchmarks:

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## Peak Optimization Flags (Continued)

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -ansi-alias -opt-ra-region-strategy=block  
-Wl,-z,muldefs -L/opt/SmartHeap\_8.1/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine  
-Wl,-z,muldefs -L/opt/SmartHeap\_8.1/lib -lsmartheap

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revD.20090713.html>

<http://www.spec.org/cpu2006/flags/NEC-Intel-Linux-Settings-flags-revB.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revD.20090713.xml>

<http://www.spec.org/cpu2006/flags/NEC-Intel-Linux-Settings-flags-revB.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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