



SPEC® CINT2006 Result

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Fujitsu Siemens Computers

SPECint®_rate2006 = 59.2

CELSIUS H270, Intel Core 2 Extreme QX9300 processor

SPECint_rate_base2006 = 54.2

CPU2006 license: 22

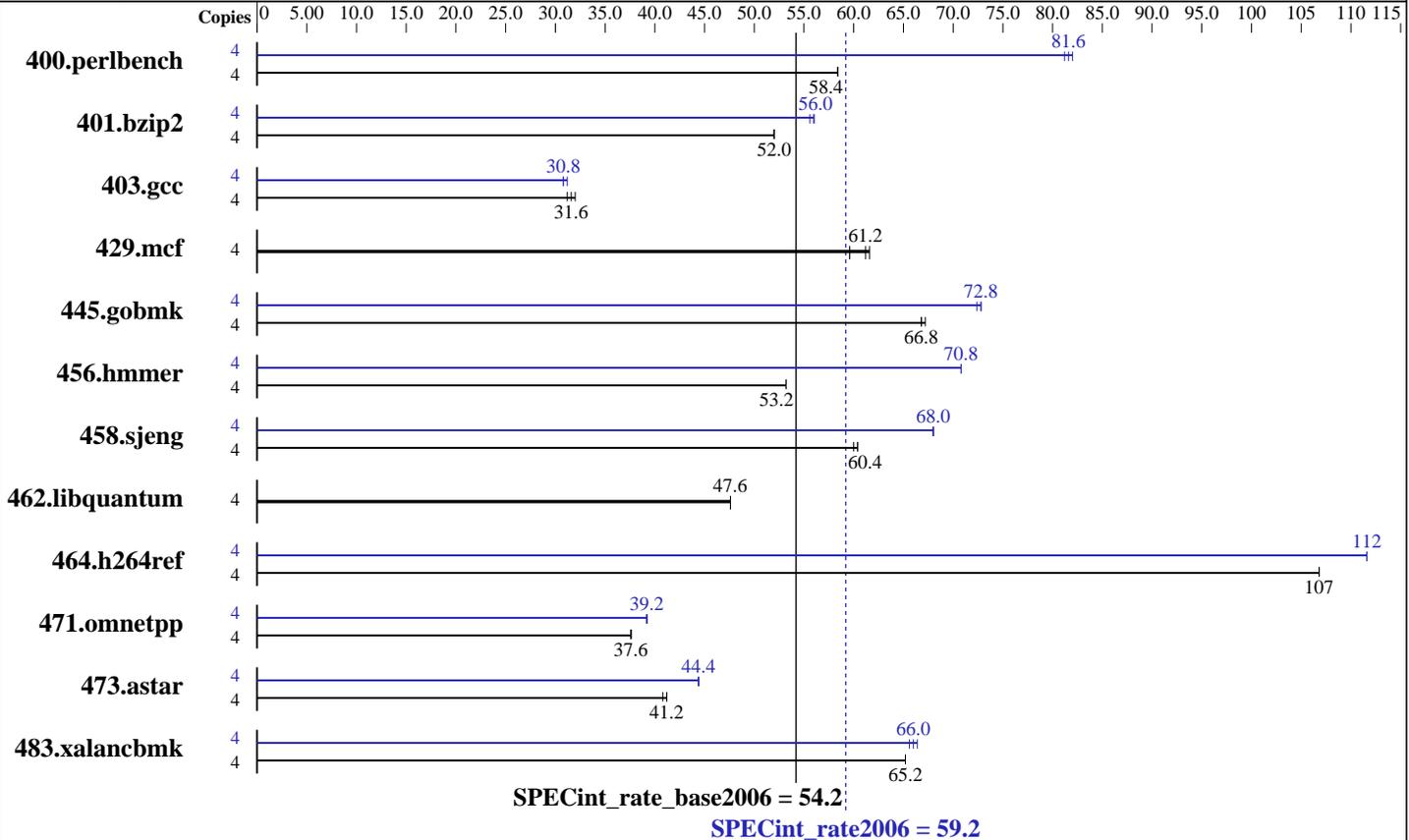
Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

Test date: Nov-2008

Hardware Availability: Dec-2008

Software Availability: Feb-2008



Hardware

CPU Name: Intel Core 2 Extreme QX9300
 CPU Characteristics:
 CPU MHz: 2530
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 8 GB (2x4 GB DDR3 1066 MHz SDRAM)
 Disk Subsystem: 1 x 80 GB SATA 7200 RPM
 Other Hardware: None

Software

Operating System: Windows Vista Ultimate, 64 bit Version
 Compiler: Intel C++ Compiler for applications running on IA-32, Version 10.1, Build 20080212
 Intel C++ Compiler for applications running on Intel 64, Version 10.1, Build 20080212
 Microsoft Visual Studio 2005 with SP1 (for libraries)
 Auto Parallel: No
 File System: NTFS
 System State: Default
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: SmartHeap Library Version 9 from <http://www.microquill.com/>



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	670	58.4	668	58.4	669	58.4	4	478	82.0	479	81.6	481	81.2
401.bzip2	4	740	52.0	741	52.0	744	52.0	4	691	56.0	692	55.6	691	56.0
403.gcc	4	1010	32.0	1028	31.2	1022	31.6	4	1033	31.2	1040	30.8	1048	30.8
429.mcf	4	595	61.2	592	61.6	613	59.6	4	595	61.2	592	61.6	613	59.6
445.gobmk	4	625	67.2	628	66.8	627	66.8	4	577	72.8	578	72.4	577	72.8
456.hammer	4	703	53.2	703	53.2	703	53.2	4	527	70.8	527	70.8	527	70.8
458.sjeng	4	804	60.0	804	60.4	804	60.4	4	711	68.0	711	68.0	711	68.0
462.libquantum	4	1748	47.6	1741	47.6	1747	47.6	4	1748	47.6	1741	47.6	1747	47.6
464.h264ref	4	830	107	829	107	829	107	4	794	112	794	112	794	112
471.omnetpp	4	666	37.6	665	37.6	665	37.6	4	635	39.2	636	39.2	635	39.2
473.astar	4	685	40.8	684	41.2	685	41.2	4	634	44.4	633	44.4	634	44.4
483.xalancbmk	4	423	65.2	424	65.2	423	65.2	4	416	66.4	420	65.6	418	66.0

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Screen Saver was disabled
Power scheme was set to "Always On"
"Turn off monitor" option within this scheme was set to "Never".

Platform Notes

BIOS default settings have been used.

General Notes

For information about Fujitsu Siemens Computers please see:
<http://www.fujitsu-siemens.com>

Base Compiler Invocation

C benchmarks:
icl -Qvc8 -Qc99

C++ benchmarks:
icl -Qvc8



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Base Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
483.xalancbmk: -Qoption, cpp, --no_wchar_t_keyword

Base Optimization Flags

C benchmarks:
-fast -Qvec-guard-write -F512000000

C++ benchmarks:
-fast -Qcxx_features -F512000000 shlW32M.lib -link -FORCE:MULTIPLE

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icl -Qvc8 -Qc99

401.bzip2: C:\DevelTools\Intel\Compiler\C++\10.1.021\EM64T\Bin\icl.exe
-IC:\DevelTools\Intel\Compiler\C++\10.1.021\EM64T\Include
-link -LIBPATH:C:\DevelTools\Intel\Compiler\C++\10.1.021\EM64T\Lib
-link -LIBPATH:C:\ProgramFiles\MicrosoftVisualStudio8\VC\lib\AMD64
-link -LIBPATH:C:\ProgramFiles\MicrosoftVisualStudio8\VC\PlatformSDK\lib\AMD64
-link -LIBPATH:C:\ProgramFiles\MicrosoftVisualStudio8\VC\lib

456.hmmr: C:\DevelTools\Intel\Compiler\C++\10.1.021\EM64T\Bin\icl.exe
-IC:\DevelTools\Intel\Compiler\C++\10.1.021\EM64T\Include
-link -LIBPATH:C:\DevelTools\Intel\Compiler\C++\10.1.021\EM64T\Lib
-link -LIBPATH:C:\ProgramFiles\MicrosoftVisualStudio8\VC\lib\AMD64
-link -LIBPATH:C:\ProgramFiles\MicrosoftVisualStudio8\VC\PlatformSDK\lib\AMD64
-link -LIBPATH:C:\ProgramFiles\MicrosoftVisualStudio8\VC\lib

C++ benchmarks:
icl -Qvc8



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Peak Portability Flags

```

401.bzip2: -DSPEC_CPU_P64
403.gcc: -DSPEC_CPU_WIN32
456.hmmer: -DSPEC_CPU_P64
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword

```

Peak Optimization Flags

C benchmarks:

```

400.perlbench: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
               -Qprefetch -F512000000 shlw32M.lib -link -FORCE:MULTIPLE

401.bzip2: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qprefetch
           -F512000000

403.gcc: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -F512000000

429.mcf: basepeak = yes

445.gobmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxT -O2 -Qipo
           -Qprec-div- -Qansi-alias -F512000000

456.hmmer: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2
           -Qansi-alias -Qopt-multi-version-aggressive -F512000000

458.sjeng: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll4
           -F512000000

462.libquantum: basepeak = yes

464.h264ref: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2
            -Qansi-alias -F512000000

```

C++ benchmarks:

```

471.omnetpp: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
             -Qopt-ra-region-strategy=block -Qcxx_features -F512000000
             shlw32M.lib -link -FORCE:MULTIPLE

473.astar: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
           -Qopt-ra-region-strategy=routine -Qcxx_features -F512000000
           shlw32M.lib -link -FORCE:MULTIPLE

483.xalancbmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
              -Qcxx_features -F512000000 shlw32M.lib
              -link -FORCE:MULTIPLE

```



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Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

http://www.spec.org/cpu2006/flags/CPU2006_flags.20090713.html

You can also download the XML flags source by saving the following link:

http://www.spec.org/cpu2006/flags/CPU2006_flags.20090713.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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