SPEC® CINT2006 Result
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Fujitsu Siemens Computers

PRIMERGY BX620 S4, Intel Xeon X5470, 3.33 GHz

$SPECint®\_rate2006 = 148$

$SPECint\_rate\_base2006 = 134$

CPU2006 license: 22	**Test date:** Oct-2008
Test sponsor: Fujitsu Siemens Computers	**Hardware Availability:** Oct-2008
Tested by: Fujitsu Siemens Computers	**Software Availability:** Nov-2008

SPECint_rate_base2006 = 134

SPECint_rate2006 = 148

Hardware

CPU Name:	Intel Xeon X5470
CPU Characteristics:	1333 MHz system bus
CPU MHz:	3333
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	12 MB I+D on chip per chip, 6 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (8x2 GB PC2-5300F, 2 rank, CL5-5-5, ECC)
Disk Subsystem:	1x SATA, 120 GB, 5400 rpm
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16.60-0.21-smp
Compiler:	Intel C++ Compiler 11.0 for Linux Build 20080730 Package ID: l_cproc_b_11.0.042
Auto Parallel:	Yes
File System:	ext3
System State:	Multi-User Run Level 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap Library, Version 8.1 Binutils 2.18.50.0.7.20080502

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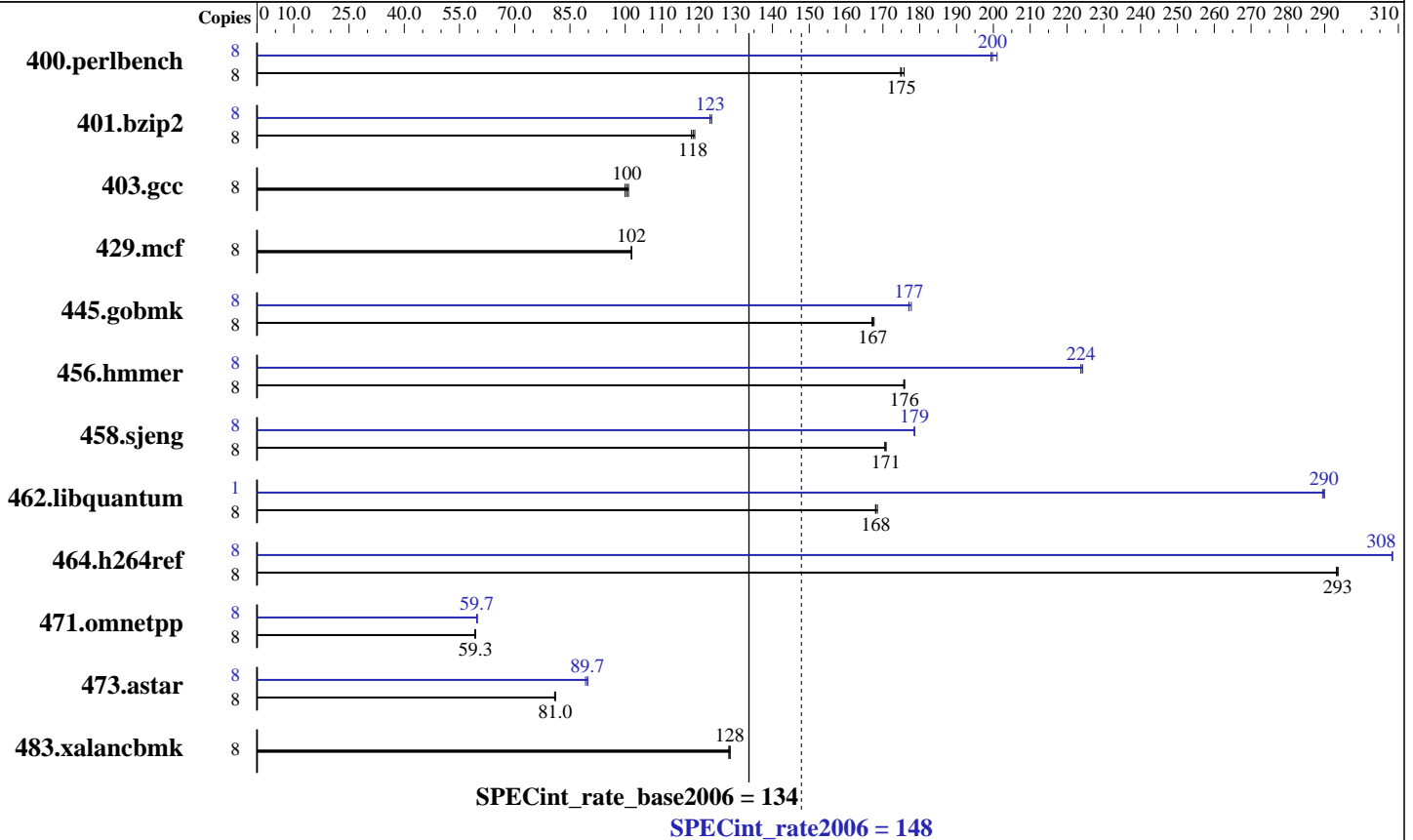
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Results Table

Benchmark	Copies	Base Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Peak Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	445	176	**447**	**175**	447	175	8	392	199	389	201	**391**	**200**
401.bzip2	8	649	119	**652**	**118**	654	118	8	628	123	625	124	**627**	**123**
403.gcc	8	**641**	**100**	638	101	644	100	8	**641**	**100**	638	101	644	100
429.mcf	8	718	102	717	102	**717**	**102**	8	718	102	717	102	**717**	**102**
445.gobmk	8	501	168	**502**	**167**	502	167	8	**474**	**177**	472	178	474	177
456.hmmer	8	425	176	**424**	**176**	424	176	8	334	224	**333**	**224**	333	224
458.sjeng	8	**567**	**171**	568	170	567	171	8	542	178	**542**	**179**	542	179
462.libquantum	8	987	168	984	169	**986**	**168**	1	71.6	289	71.5	290	**71.5**	**290**
464.h264ref	8	**603**	**293**	604	293	603	294	8	574	308	574	308	**574**	**308**
471.omnetpp	8	843	59.3	**843**	**59.3**	845	59.2	8	**837**	**59.7**	835	59.9	838	59.7
473.astar	8	**693**	**81.0**	693	81.1	695	80.8	8	**626**	**89.7**	629	89.2	625	89.8
483.xalancbmk	8	430	129	**430**	**128**	431	128	8	430	129	**430**	**128**	431	128

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
 taskset has been used to bind processes to cores except
 for 462.libquantum peak
```

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
OMP_NUM_THREADS set to number of cores (default)
KMP_AFFINITY set to "physical,0"
KMP_STACKSIZE set to 64M
```

Platform Notes

```
BIOS configuration:
Adjacent Sector Prefetch = Disable
```

General Notes

```
For information about Fujitsu Siemens Computers please see:
http://www.fujitsu-siemens.com
```

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Base Compiler Invocation

C benchmarks:
    icc

C++ benchmarks:
    icpc

Base Portability Flags

    400.perlbench: -DSPEC_CPU_LINUX_IA32
    462.libquantum: -DSPEC_CPU_LINUX
    483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
    -xSSE4.1 -ipo -O3 -no-prec-div -static -inline-calloc
    -opt-malloc-options=3 -opt-prefetch

C++ benchmarks:
    -xSSE4.1 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
    -L/spec/cpu2006.1.1/lib -lsmartheap

Base Other Flags

C benchmarks:

    403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
    icc

    401.bzip2: /opt/intel/Compiler/11.0/042/bin/intel64/icc
            -L/opt/intel/Compiler/11.0/042/ipp/em64t/lib
            -I/opt/intel/Compiler/11.0/042/ipp/em64t/include

    456.hmmer: /opt/intel/Compiler/11.0/042/bin/intel64/icc
            -L/opt/intel/Compiler/11.0/042/ipp/em64t/lib
            -I/opt/intel/Compiler/11.0/042/ipp/em64t/include

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Peak Compiler Invocation (Continued)

C++ benchmarks:
    icpc

Peak Portability Flags

    400.perlbench: -DSPEC_CPU_LINUX_IA32
       401.bzip2: -DSPEC_CPU_LP64
       456.hmmer: -DSPEC_CPU_LP64
   462.libquantum: -DSPEC_CPU_LINUX
   483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

    400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
            -no-prec-div -static -ansi-alias -opt-prefetch

       401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
            -no-prec-div -static -opt-prefetch -ansi-alias

         403.gcc: basepeak = yes

         429.mcf: basepeak = yes

       445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -O2 -ipo
            -no-prec-div -ansi-alias

       456.hmmer: -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2
            -ansi-alias

       458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
            -no-prec-div -static -unroll4

   462.libquantum: -xSSE4.1 -ipo -O3 -no-prec-div -static
            -opt-malloc-options=3 -parallel -par-runtime-control
            -opt-prefetch

      464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
            -no-prec-div -static -unroll2 -ansi-alias

C++ benchmarks:

      471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
            -no-prec-div -ansi-alias -opt-ra-region-strategy=block
            -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap

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Peak Optimization Flags (Continued)

```
473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
           -no-prec-div -ansi-alias -opt-ra-region-strategy=routine
           -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap

483.xalancbmk: basepeak = yes
```

Peak Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-icl1.0-int-linux64-revA.20090713.16.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-icl1.0-int-linux64-revA.20090713.16.xml

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