SPEC® CINT2006 Result

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Dell Inc.

PowerEdge 1950 III (Intel Xeon L5240, 3.00 GHz)

SPECint®_rate2006 =   82.0

SPECint_rate_base2006 = 77.4
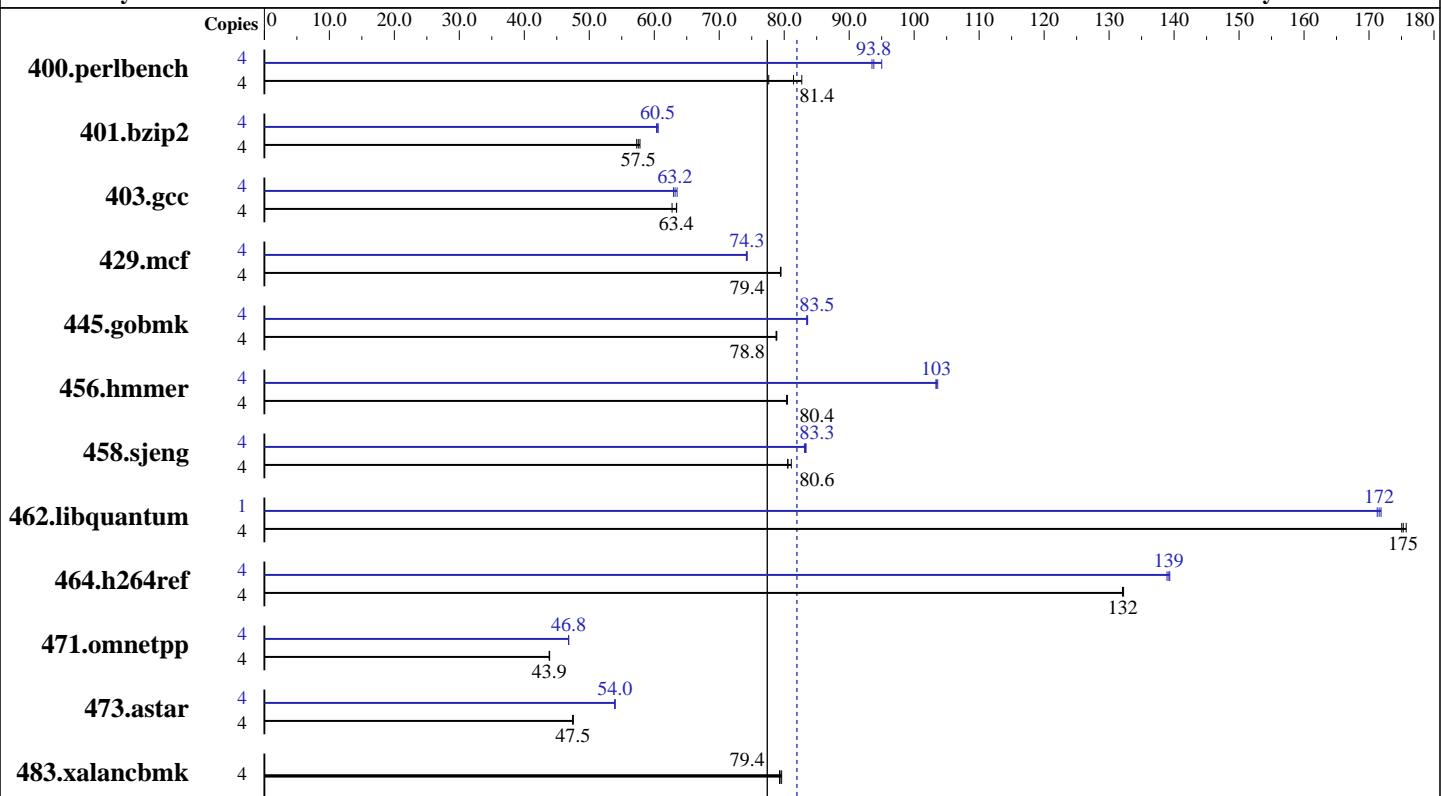
CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Oct-2008
Hardware Availability: Sep-2008
Software Availability: Nov-2008

SPECint_rate_base2006 = 77.4

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Hardware

CPU Name:	Intel Xeon L5240
CPU Characteristics:	
CPU MHz:	3000
FPU:	Integrated
CPU(s) enabled:	4 cores, 2 chips, 2 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	6 MB I+D on chip per chip
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (8 x 2 GB 667 MHz ECC CL5 FB-DIMM)
Disk Subsystem:	1 x 73 GB 15000 RPM SAS
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16-60.0.21-smp
Compiler:	Intel C++ Compiler 11.0 for Linux Build 20080730 Package ID: l_cproc_b_11.0.042
Auto Parallel:	Yes
File System:	ReiserFS
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	503	77.6	473	82.7	**480**	**81.4**	4	**417**	**93.8**	418	93.5	411	95.0
401.bzip2	4	668	57.8	674	57.3	**671**	**57.5**	4	640	60.4	637	60.6	**638**	**60.5**
403.gcc	4	**508**	**63.4**	513	62.7	507	63.5	4	**509**	**63.2**	507	63.5	511	63.0
429.mcf	4	459	79.5	459	79.4	**459**	**79.4**	4	491	74.3	492	74.2	**491**	**74.3**
445.gobmk	4	533	78.8	532	78.9	**533**	**78.8**	4	**502**	**83.5**	502	83.6	503	83.5
456.hmmer	4	464	80.4	**464**	**80.4**	464	80.5	4	**361**	**103**	360	104	361	103
458.sjeng	4	597	81.1	601	80.5	**601**	**80.6**	4	581	83.4	**581**	**83.3**	582	83.1
462.libquantum	4	472	176	**473**	**175**	474	175	1	**121**	**172**	121	171	121	172
464.h264ref	4	669	132	670	132	**670**	**132**	4	637	139	**636**	**139**	635	139
471.omnetpp	4	569	43.9	570	43.8	**570**	**43.9**	4	534	46.8	**534**	**46.8**	534	46.8
473.astar	4	591	47.5	**591**	**47.5**	592	47.4	4	521	53.9	**520**	**54.0**	520	54.0
483.xalancbmk	4	348	79.3	347	79.6	**348**	**79.4**	4	348	79.3	347	79.6	**348**	**79.4**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

    The config file option 'submit' was used.

Operating System Notes

    'ulimit -s unlimited' was used to set the stack size to unlimited prior to run

Platform Notes

    BIOS Settings:
    Adjacent Cache Line Prefetch = Disabled (Default = Enabled)

General Notes

    taskset was used to bind processes to cores except
    for 462.libquantum peak
    OMP_NUM_THREADS set to number of processors
    KMP_AFFINITY set to "physical,0"
    KMP_STACKSIZE set to 64M

Base Compiler Invocation

C benchmarks:
    icc

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Base Compiler Invocation (Continued)

C++ benchmarks:
    icpc

Base Portability Flags

      400.perlbench: -DSPEC_CPU_LINUX_IA32
      462.libquantum: -DSPEC_CPU_LINUX
      483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
    -xSSE4.1 -ipo -O3 -no-prec-div -static -inline-calloc
    -opt-malloc-options=3 -opt-prefetch

C++ benchmarks:
    -xSSE4.1 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
    -L/spec/cpu2006.1.1/lib -lsmartheap

Base Other Flags

C benchmarks:

        403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
    icc

      401.bzip2: /opt/intel/Compiler/11.0/042/bin/intel64/icc
               -L/opt/intel/Compiler/11.0/042/ipp/em64t/lib
               -I/opt/intel/Compiler/11.0/042/ipp/em64t/include

      456.hmmer: /opt/intel/Compiler/11.0/042/bin/intel64/icc
               -L/opt/intel/Compiler/11.0/042/ipp/em64t/lib
               -I/opt/intel/Compiler/11.0/042/ipp/em64t/include

C++ benchmarks:
    icpc

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Peak Portability Flags

```
    400.perlbench: -DSPEC_CPU_LINUX_IA32
        401.bzip2: -DSPEC_CPU_LP64
        456.hmmer: -DSPEC_CPU_LP64
    462.libquantum: -DSPEC_CPU_LINUX
    483.xalancbmk: -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
    400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
               -no-prec-div -static -ansi-alias -opt-prefetch

        401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
               -no-prec-div -static -opt-prefetch -ansi-alias

         403.gcc: -xSSE4.1 -ipo -O3 -no-prec-div -static -inline-calloc
               -opt-malloc-options=3

         429.mcf: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
               -no-prec-div -static -opt-prefetch

       445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -O2 -ipo
               -no-prec-div -ansi-alias

        456.hmmer: -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2
               -ansi-alias

        458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
               -no-prec-div -static -unroll4

    462.libquantum: -xSSE4.1 -ipo -O3 -no-prec-div -static
               -opt-malloc-options=3 -parallel -par-runtime-control
               -opt-prefetch

      464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
               -no-prec-div -static -unroll2 -ansi-alias
```

C++ benchmarks:

```
      471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
               -no-prec-div -ansi-alias -opt-ra-region-strategy=block
               -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap

         473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
               -no-prec-div -ansi-alias -opt-ra-region-strategy=routine
               -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap
```

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Peak Optimization Flags (Continued)

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090713.02.html
http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.11.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090713.02.xml
http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.11.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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