



SPEC® CINT2006 Result

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IBM Corporation

SPECint®2006 = 28.4

IBM System x iDataPlex dx360 (Intel Xeon E5472)

SPECint_base2006 = 25.0

CPU2006 license: 11

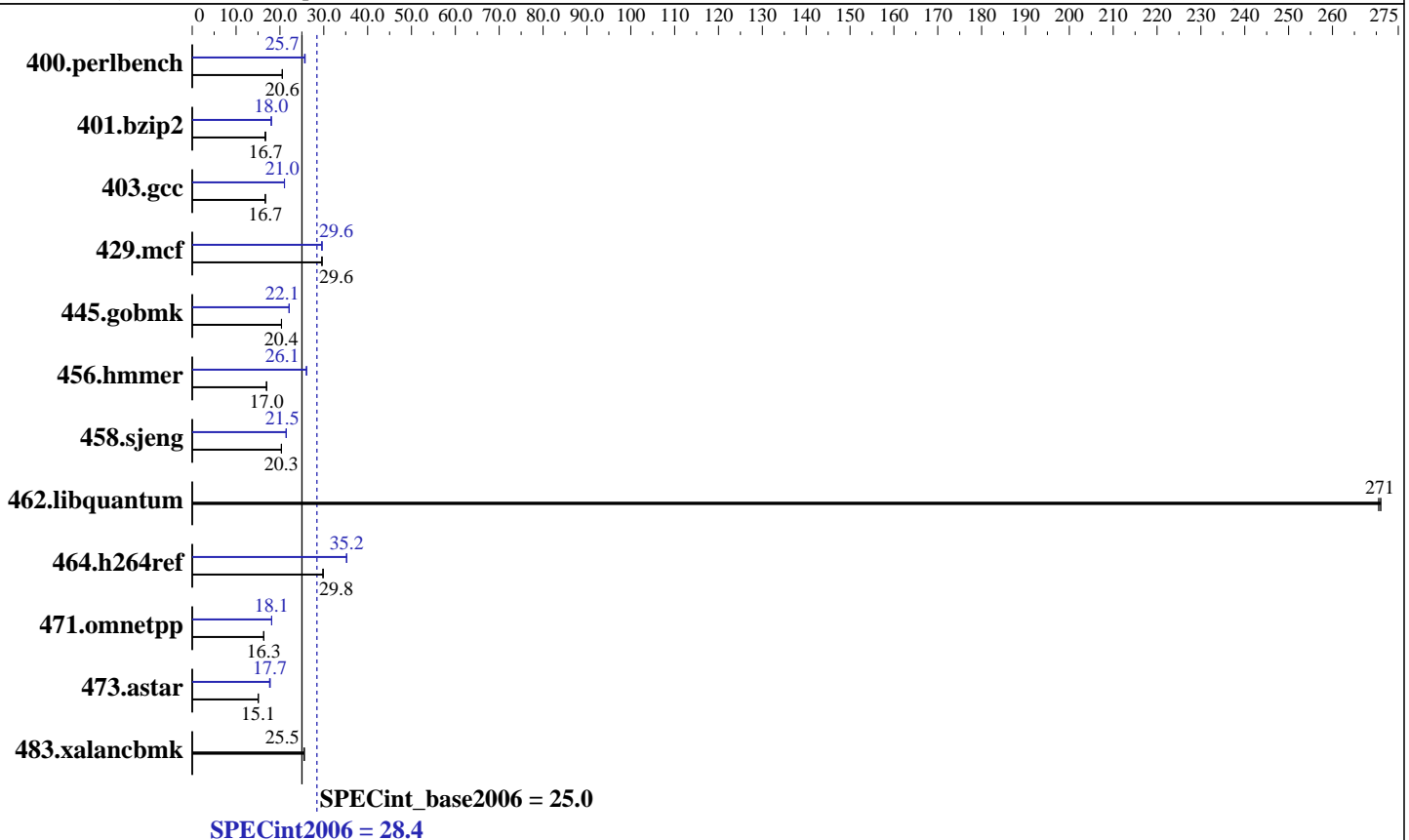
Test date: Oct-2008

Test sponsor: IBM Corporation

Hardware Availability: Nov-2008

Tested by: IBM Corporation

Software Availability: Nov-2008



Hardware

CPU Name: Intel Xeon E5472
 CPU Characteristics: 1600MHz system bus
 CPU MHz: 3000
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (4 x 4 GB DDR2-6400F ECC)
 Disk Subsystem: 1 x 250 GB SATA, 7200 RPM
 Other Hardware: None

Software

Operating System: SuSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
 Compiler: Intel C++ Compiler 11.0 for Linux Build 20080730 Package ID: l_cproc_b_11.0.042
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	475	20.6	476	20.5	<u>475</u>	<u>20.6</u>	382	25.6	379	25.8	<u>380</u>	<u>25.7</u>
401.bzip2	581	16.6	577	16.7	<u>577</u>	<u>16.7</u>	<u>535</u>	<u>18.0</u>	536	18.0	535	18.1
403.gcc	<u>483</u>	<u>16.7</u>	482	16.7	483	16.7	382	21.1	383	21.0	<u>383</u>	<u>21.0</u>
429.mcf	309	29.6	<u>308</u>	<u>29.6</u>	308	29.6	308	29.6	309	29.5	<u>308</u>	<u>29.6</u>
445.gobmk	516	20.3	<u>515</u>	<u>20.4</u>	515	20.4	475	22.1	<u>475</u>	<u>22.1</u>	475	22.1
456.hammer	550	17.0	551	16.9	<u>550</u>	<u>17.0</u>	358	26.1	<u>358</u>	<u>26.1</u>	358	26.1
458.sjeng	596	20.3	594	20.4	<u>595</u>	<u>20.3</u>	<u>564</u>	<u>21.5</u>	564	21.5	566	21.4
462.libquantum	<u>76.5</u>	<u>271</u>	76.4	271	76.6	271	<u>76.5</u>	<u>271</u>	76.4	271	76.6	271
464.h264ref	743	29.8	<u>742</u>	<u>29.8</u>	740	29.9	<u>628</u>	<u>35.2</u>	628	35.2	629	35.2
471.omnetpp	<u>383</u>	<u>16.3</u>	383	16.3	384	16.3	345	18.1	<u>345</u>	<u>18.1</u>	345	18.1
473.astar	466	15.1	<u>466</u>	<u>15.1</u>	466	15.0	<u>396</u>	<u>17.7</u>	395	17.8	398	17.6
483.xalancbmk	270	25.5	<u>270</u>	<u>25.5</u>	270	25.5	270	25.5	<u>270</u>	<u>25.5</u>	270	25.5

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

OMP_NUM_THREADS set to number of processors

KMP_AFFINITY set to "physical,0"

Hardware Prefetch Enabled and Adjacent Sector Prefetch Enabled

'ulimit -s unlimited' was used to set the stack size to unlimited prior to run

Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32

462.libquantum: -DSPEC_CPU_LINUX

483.xalancbmk: -DSPEC_CPU_LINUX



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Base Optimization Flags

C benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -static -parallel
-par-runtime-control -opt-prefetch

C++ benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/Compiler/11.0/042/bin/intel64/icc
-L/opt/intel/Compiler/11.0/042/ipp/em64t/lib
-I/opt/intel/Compiler/11.0/042/ipp/em64t/include

456.hmmer: /opt/intel/Compiler/11.0/042/bin/intel64/icc
-L/opt/intel/Compiler/11.0/042/ipp/em64t/lib
-I/opt/intel/Compiler/11.0/042/ipp/em64t/include

C++ benchmarks:

icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

Continued on next page



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Peak Optimization Flags (Continued)

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -ansi-alias -opt-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -auto-ilp32 -opt-prefetch
-ansi-alias

403.gcc: -xSSE4.1 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3

429.mcf: -xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -O2 -ipo
-no-prec-div -ansi-alias

456.hmmr: -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2
-ansi-alias -auto-ilp32

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll4

462.libquantum: basepeak = yes

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.04.html>



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You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.04.xml>

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