



SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

SPECint®_rate2006 = 129

PowerEdge 2950 III (Intel Xeon E5430, 2.66 GHz)

SPECint_rate_base2006 = 119

CPU2006 license: 55

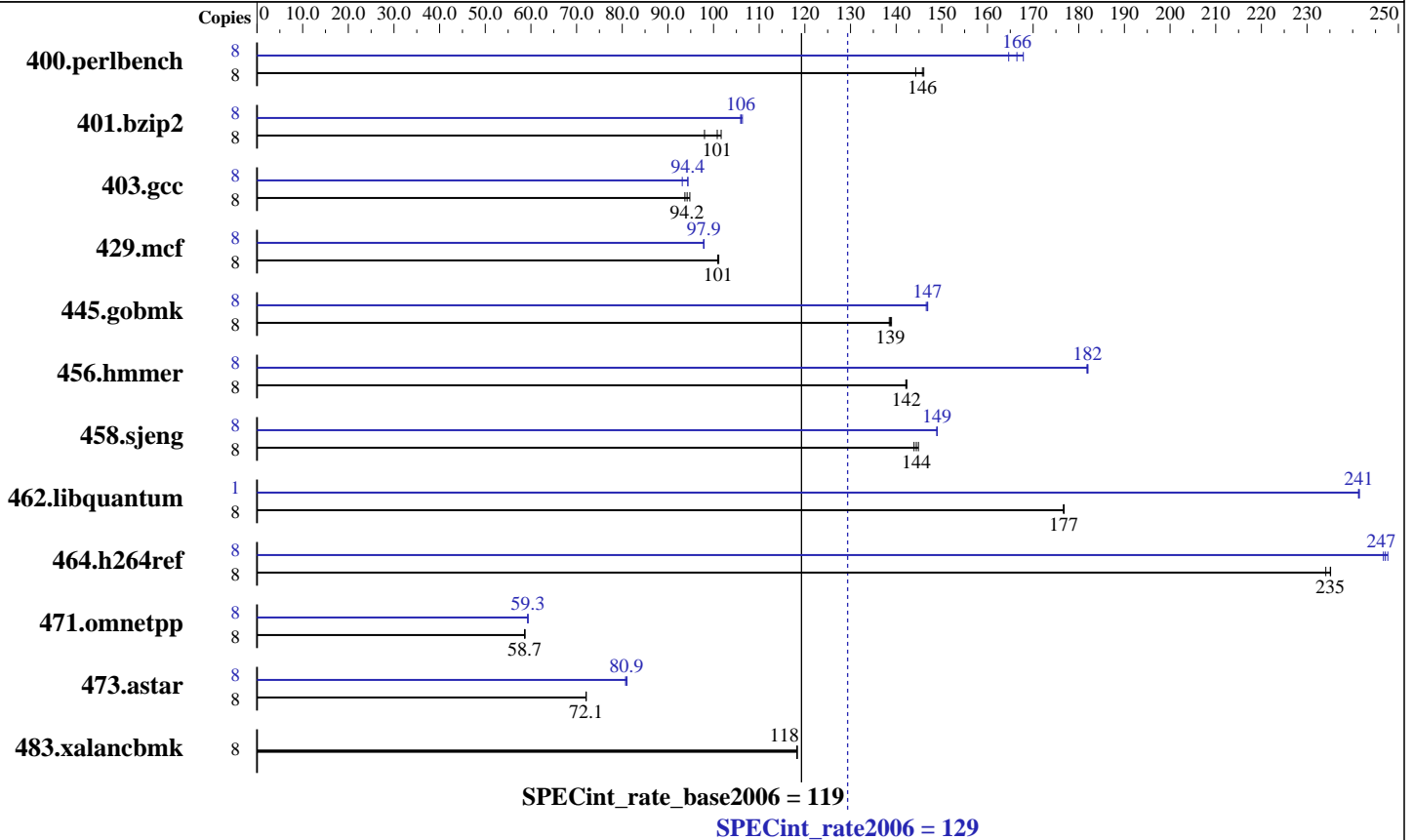
Test date: Oct-2008

Test sponsor: Dell Inc.

Hardware Availability: Sep-2008

Tested by: Dell Inc.

Software Availability: Nov-2008



Hardware

CPU Name: Intel Xeon E5430
 CPU Characteristics: 2666
 CPU MHz: 2666
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (4 x 4 GB 667 MHz ECC CL5 FB-DIMM)
 Disk Subsystem: 1 x 73 GB 15000 RPM SAS, 1 x 73 GB 10000 RPM SAS
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16-60.0.21-smp
 Compiler: Intel C++ Compiler 11.0 for Linux Build 20080730 Package ID: l_cproc_b_11.0.042
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	542	144	<u>536</u>	<u>146</u>	535	146	8	466	168	<u>470</u>	<u>166</u>	475	165
401.bzip2	8	788	98.0	<u>766</u>	<u>101</u>	759	102	8	726	106	729	106	<u>728</u>	<u>106</u>
403.gcc	8	687	93.7	<u>684</u>	<u>94.2</u>	679	94.8	8	691	93.1	682	94.4	<u>682</u>	<u>94.4</u>
429.mcf	8	721	101	723	101	<u>723</u>	<u>101</u>	8	<u>745</u>	<u>97.9</u>	746	97.8	745	97.9
445.gobmk	8	606	138	<u>605</u>	<u>139</u>	604	139	8	571	147	572	147	<u>572</u>	<u>147</u>
456.hammer	8	<u>525</u>	<u>142</u>	524	142	525	142	8	<u>410</u>	<u>182</u>	411	182	410	182
458.sjeng	8	668	145	673	144	<u>670</u>	<u>144</u>	8	650	149	650	149	<u>650</u>	<u>149</u>
462.libquantum	8	<u>938</u>	<u>177</u>	938	177	938	177	1	<u>85.9</u>	<u>241</u>	85.8	241	85.9	241
464.h264ref	8	756	234	<u>753</u>	<u>235</u>	753	235	8	718	247	<u>716</u>	<u>247</u>	715	248
471.omnetpp	8	<u>852</u>	<u>58.7</u>	852	58.7	853	58.6	8	843	59.3	843	59.3	<u>843</u>	<u>59.3</u>
473.astar	8	<u>779</u>	<u>72.1</u>	779	72.1	779	72.1	8	696	80.7	<u>694</u>	<u>80.9</u>	693	81.0
483.xalancbmk	8	<u>467</u>	<u>118</u>	466	118	467	118	8	<u>467</u>	<u>118</u>	466	118	467	118

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
taskset was used to bind processes to cores except
for 462.libquantum peak

Operating System Notes

'ulimit -s unlimited' was used to set the stack size to unlimited prior to run

Platform Notes

BIOS Settings:
Adjacent Cache Line Prefetch = Disabled (Default = Enabled)

General Notes

OMP_NUM_THREADS set to number of processors
KMP_AFFINITY set to "physical,0"
KMP_STACKSIZE set to 64M

Base Compiler Invocation

C benchmarks:
icc

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Base Compiler Invocation (Continued)

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xSSE4.1 -ipo -O3 -no-prec-div -static -inline-alloc
-opt-malloc-options=3 -opt-prefetch

C++ benchmarks:
-xSSE4.1 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/Compiler/11.0/042/bin/intel64/icc
-L/opt/intel/Compiler/11.0/042/ipp/em64t/lib
-I/opt/intel/Compiler/11.0/042/ipp/em64t/include

456.hmmer: /opt/intel/Compiler/11.0/042/bin/intel64/icc
-L/opt/intel/Compiler/11.0/042/ipp/em64t/lib
-I/opt/intel/Compiler/11.0/042/ipp/em64t/include

C++ benchmarks:
icpc



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Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
 401.bzip2: -DSPEC_CPU_LP64
 456.hmmer: -DSPEC_CPU_LP64
 462.libquantum: -DSPEC_CPU_LINUX
 483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
 -no-prec-div -static -ansi-alias -opt-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
 -no-prec-div -static -opt-prefetch -ansi-alias

403.gcc: -xSSE4.1 -ipo -O3 -no-prec-div -static -inline-alloc
 -opt-malloc-options=3

429.mcf: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
 -no-prec-div -static -opt-prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -O2 -ipo
 -no-prec-div -ansi-alias

456.hmmer: -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2
 -ansi-alias

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
 -no-prec-div -static -unroll4

462.libquantum: -xSSE4.1 -ipo -O3 -no-prec-div -static
 -opt-malloc-options=3 -parallel -par-runtime-control
 -opt-prefetch

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
 -no-prec-div -static -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
 -no-prec-div -ansi-alias -opt-ra-region-strategy=block
 -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmarheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
 -no-prec-div -ansi-alias -opt-ra-region-strategy=routine
 -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmarheap

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Peak Optimization Flags (Continued)

483.xalanbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090713.02.html>

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.02.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090713.02.xml>

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.02.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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