SPEC® CFP2006 Result

Hewlett-Packard Company

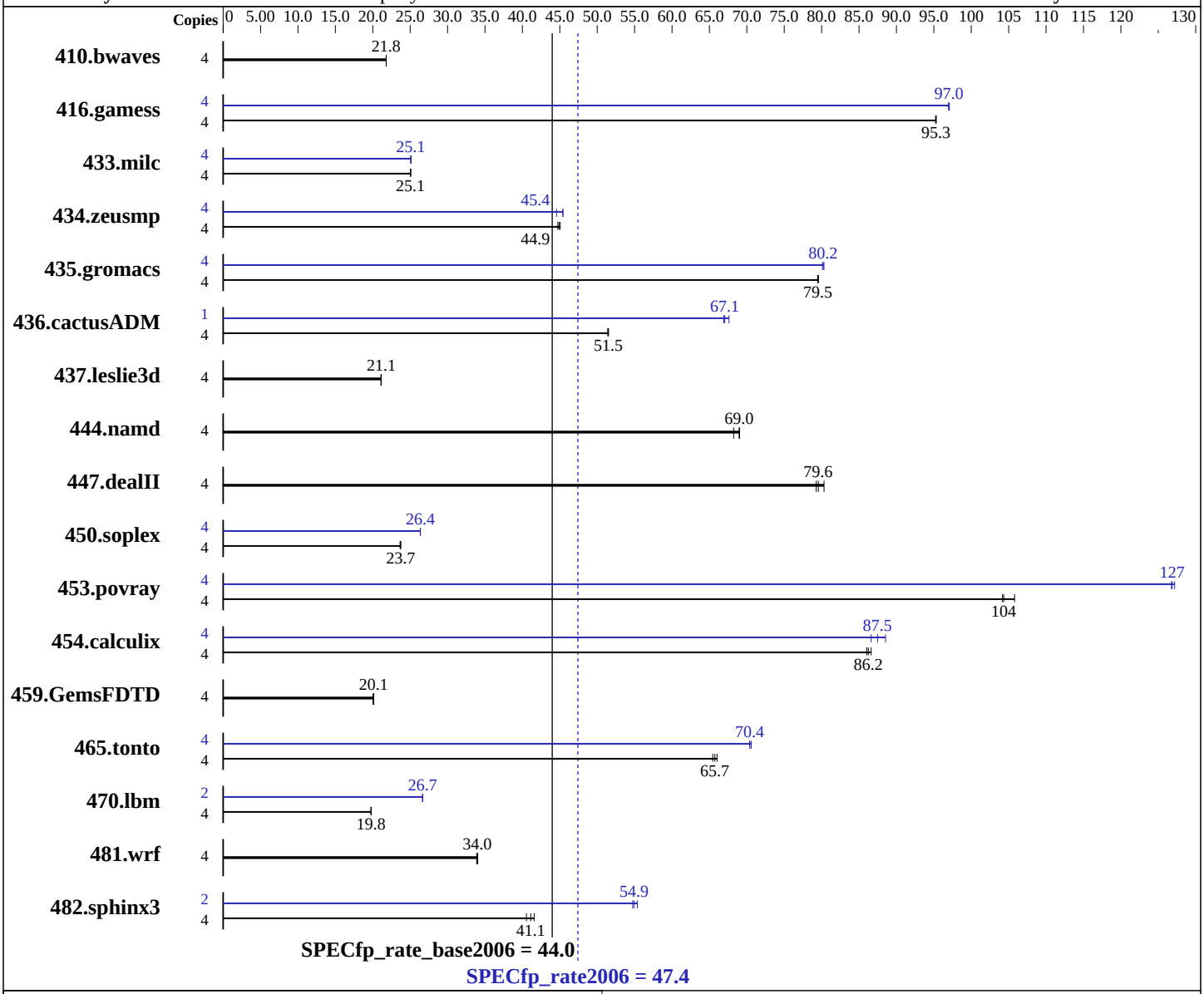
ProLiant DL160 G5p
(3.20 GHz, Intel Xeon X5482)

SPECfp®_rate2006 = 47.4

SPECfp_rate_base2006 = 44.0

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
Tested by: Hewelett-Packard Company

Test date: Oct-2008
Hardware Availability: Sep-2008
Software Availability: Nov-2008



Hardware

CPU Name:	Intel Xeon X5482
CPU Characteristics:	3.20 GHz, 2x6 MB L2 Shared, 1600 MHz system bus
CPU MHz:	3200
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	12 MB I+D on chip per chip, 6 MB shared / 2 cores

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Software

Operating System:	SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
Compiler:	Intel C++ and Fortran Compiler 11.0 for Linux Build 20080730 Package ID: l_cproc_b_11.0.042, l_fproc_b_11.0.042
Auto Parallel:	Yes
File System:	ext3
System State:	Run level 3 (multi-user)
Base Pointers:	64-bit

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L3 Cache: None
Other Cache: None
Memory: 16 GB (8x2 GB PC2-6400F CL5)
Disk Subsystem: 1x72 GB 15 K SAS
Other Hardware: None

Peak Pointers: 32/64-bit
Other Software: Binutils 2.18.50.0.7.20080502

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	4	2493	21.8	2495	21.8	**2494**	**21.8**	4	2493	21.8	2495	21.8	**2494**	**21.8**
416.gamess	4	822	95.3	822	95.2	**822**	**95.3**	4	807	97.1	**807**	**97.0**	808	97.0
433.milc	4	1466	25.0	**1465**	**25.1**	1465	25.1	4	1464	25.1	1463	25.1	**1464**	**25.1**
434.zeusmp	4	808	45.1	814	44.7	**811**	**44.9**	4	817	44.6	801	45.4	**802**	**45.4**
435.gromacs	4	359	79.5	359	79.6	**359**	**79.5**	4	356	80.1	**356**	**80.2**	356	80.3
436.cactusADM	4	930	51.4	**928**	**51.5**	928	51.5	1	179	66.9	177	67.6	**178**	**67.1**
437.leslie3d	4	1783	21.1	1778	21.1	**1779**	**21.1**	4	1783	21.1	1778	21.1	**1779**	**21.1**
444.namd	4	465	69.0	**465**	**69.0**	470	68.2	4	465	69.0	**465**	**69.0**	470	68.2
447.dealII	4	577	79.3	570	80.3	**575**	**79.6**	4	577	79.3	570	80.3	**575**	**79.6**
450.soplex	4	1411	23.6	**1406**	**23.7**	1406	23.7	4	**1265**	**26.4**	1266	26.4	1265	26.4
453.povray	4	201	106	204	104	**204**	**104**	4	167	127	168	127	**168**	**127**
454.calculix	4	384	86.0	381	86.6	**383**	**86.2**	4	373	88.5	381	86.6	**377**	**87.5**
459.GemsFDTD	4	2118	20.0	2109	20.1	**2111**	**20.1**	4	2118	20.0	2109	20.1	**2111**	**20.1**
465.tonto	4	596	66.0	**599**	**65.7**	601	65.4	4	558	70.6	**559**	**70.4**	559	70.4
470.lbm	4	2780	19.8	2780	19.8	**2780**	**19.8**	2	1030	26.7	**1031**	**26.7**	1031	26.7
481.wrf	4	**1315**	**34.0**	1313	34.0	1318	33.9	4	**1315**	**34.0**	1313	34.0	1318	33.9
482.sphinx3	4	1875	41.6	**1896**	**41.1**	1923	40.5	2	712	54.7	704	55.4	**710**	**54.9**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
 taskset was used to bind processes to cores except
 for 436.cactusADM peak
```

Operating System Notes

```
 'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
 OMP_NUM_THREADS set to number of processors
 KMP_AFFINITY set to "physical,0"
 KMP_STACKSIZE set to 64M
```

Platform Notes

```
BIOS configuration:
Adjacent Cache Line Prefetch Disabled
Hardware Prefetch Disabled
```

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

C++ benchmarks:
-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

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Base Optimization Flags (Continued)

Fortran benchmarks:
`-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch`

Benchmarks using both Fortran and C:
`-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch`

Peak Compiler Invocation

C benchmarks (except as noted below):
`icc`

482.sphinx3: `/opt/intel/Compiler/11.0/042/bin/ia32/icc -L/opt/intel/Compiler/11.0/042/ipp/ia32/lib -I/opt/intel/Compiler/11.0/042/ipp/ia32/include`

C++ benchmarks (except as noted below):
`icpc`

450.soplex: `/opt/intel/Compiler/11.0/042/bin/ia32/icpc -L/opt/intel/Compiler/11.0/042/ipp/ia32/lib -I/opt/intel/Compiler/11.0/042/ipp/ia32/include`

Fortran benchmarks:
`ifort`

Benchmarks using both Fortran and C:
`icc ifort`

Peak Portability Flags

410.bwaves: `-DSPEC_CPU_LP64`
416.gamess: `-DSPEC_CPU_LP64`
433.milc: `-DSPEC_CPU_LP64`
434.zeusmp: `-DSPEC_CPU_LP64`
435.gromacs: `-DSPEC_CPU_LP64 -nofor_main`
436.cactusADM: `-DSPEC_CPU_LP64 -nofor_main`
437.leslie3d: `-DSPEC_CPU_LP64`
444.namd: `-DSPEC_CPU_LP64`
447.dealII: `-DSPEC_CPU_LP64`
453.povray: `-DSPEC_CPU_LP64`
454.calculix: `-DSPEC_CPU_LP64 -nofor_main`
459.GemsFDTD: `-DSPEC_CPU_LP64`
465.tonto: `-DSPEC_CPU_LP64`
470.lbm: `-DSPEC_CPU_LP64`
481.wrf: `-DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

433.milc: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xSSE4.1 -ipo -O3 -no-prec-div -static -fno-alias`

470.lbm: `-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch -auto-ilp32`

482.sphinx3: `-xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2`

C++ benchmarks:

444.namd: `basepeak = yes`

447.dealII: `basepeak = yes`

450.soplex: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-malloc-options=3`

453.povray: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xSSE4.1 -ipo -O3 -no-prec-div -static -unroll4 -ansi-alias`

Fortran benchmarks:

410.bwaves: `basepeak = yes`

416.gamess: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2 -Ob0 -ansi-alias -scalar-rep-`

434.zeusmp: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xSSE4.1 -ipo -O3 -no-prec-div -static`

437.leslie3d: `basepeak = yes`

459.GemsFDTD: `basepeak = yes`

465.tonto: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xSSE4.1 -ipo -O3 -no-prec-div -static -unroll4 -auto`

Benchmarks using both Fortran and C:

435.gromacs: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch -auto-ilp32`

436.cactusADM: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2 -opt-prefetch -parallel -auto-ilp32`

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Peak Optimization Flags (Continued)

454.calculix: `-xSSE4.1 -ipo -O3 -no-prec-div -static -auto-ilp32`

481.wrf: `basepeak = yes`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revD.20090713.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revD.20090713.xml`

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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