



# SPEC® CINT2006 Result

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## IBM Corporation

**SPECint®2006 = 29.1**

## IBM System x3650 (Intel Xeon X5260)

**SPECint\_base2006 = 25.6**

CPU2006 license: 11

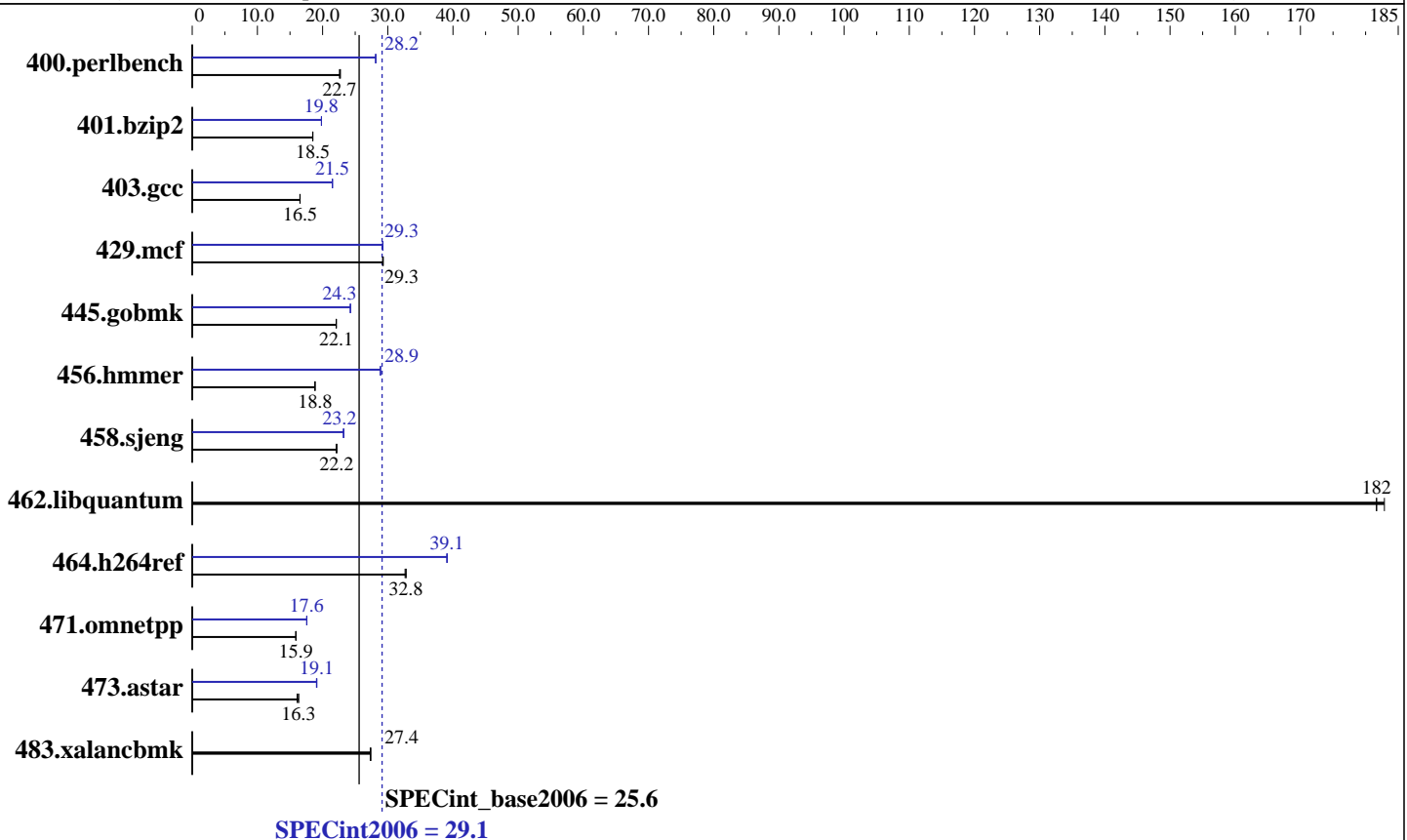
Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: Oct-2008

Hardware Availability: Nov-2008

Software Availability: Nov-2008



### Hardware

CPU Name: Intel Xeon X5260  
 CPU Characteristics: 1333MHz system bus  
 CPU MHz: 3333  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 6 MB I+D on chip per chip  
 L3 Cache: None  
 Other Cache: None  
 Memory: 16 GB (8 x 2 GB DDR2-5300F ECC)  
 Disk Subsystem: 1 x 36 GB SAS, 15000 RPM  
 Other Hardware: None

### Software

Operating System: SuSE Linux Enterprise Server 10 (x86\_64) SP1, Kernel 2.6.16.46-0.12-smp  
 Compiler: Intel C++ Compiler 11.0 for Linux Build 20080730 Package ID: l\_cproc\_b\_11.0.042  
 Auto Parallel: Yes  
 File System: ReiserFS  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	<b>430</b>	<b>22.7</b>	433	22.6	430	22.7	<b>347</b>	<b>28.2</b>	347	28.1	347	28.2
401.bzip2	522	18.5	<b>522</b>	<b>18.5</b>	521	18.5	487	19.8	487	19.8	<b>487</b>	<b>19.8</b>
403.gcc	487	16.5	<b>487</b>	<b>16.5</b>	486	16.6	374	21.5	<b>374</b>	<b>21.5</b>	374	21.5
429.mcf	312	29.2	311	29.3	<b>311</b>	<b>29.3</b>	312	29.3	312	29.2	<b>312</b>	<b>29.3</b>
445.gobmk	474	22.1	475	22.1	<b>475</b>	<b>22.1</b>	432	24.3	<b>432</b>	<b>24.3</b>	432	24.3
456.hmmr	495	18.8	495	18.8	<b>495</b>	<b>18.8</b>	323	28.9	<b>323</b>	<b>28.9</b>	323	28.9
458.sjeng	545	22.2	<b>546</b>	<b>22.2</b>	548	22.1	520	23.3	522	23.2	<b>522</b>	<b>23.2</b>
462.libquantum	113	183	114	182	<b>114</b>	<b>182</b>	113	183	114	182	<b>114</b>	<b>182</b>
464.h264ref	678	32.7	675	32.8	<b>675</b>	<b>32.8</b>	567	39.1	<b>566</b>	<b>39.1</b>	566	39.1
471.omnetpp	<b>393</b>	<b>15.9</b>	393	15.9	392	15.9	356	17.6	355	17.6	<b>356</b>	<b>17.6</b>
473.astar	430	16.3	<b>430</b>	<b>16.3</b>	436	16.1	367	19.1	<b>368</b>	<b>19.1</b>	368	19.1
483.xalancbmk	<b>252</b>	<b>27.4</b>	253	27.3	252	27.4	<b>252</b>	<b>27.4</b>	253	27.3	252	27.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## General Notes

OMP\_NUM\_THREADS set to number of processors

KMP\_AFFINITY set to "physical,0"

Hardware Prefetch Enabled, Adjacent Sector Prefetch Enabled

'ulimit -s unlimited' was used to set the stack size to unlimited prior to run

## Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32

462.libquantum: -DSPEC\_CPU\_LINUX

483.xalancbmk: -DSPEC\_CPU\_LINUX



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## Base Optimization Flags

C benchmarks:

```
-xSSE4.1 -ipo -O3 -no-prec-div -static -parallel  
-par-runtime-control -opt-prefetch
```

C++ benchmarks:

```
-xSSE4.1 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/spec/cpu2006.1.1/lib -lsmartheap
```

## Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc

```
401.bzip2: /opt/intel/Compiler/11.0/042/bin/intel64/icc  
-L/opt/intel/Compiler/11.0/042/ipp/em64t/lib  
-I/opt/intel/Compiler/11.0/042/ipp/em64t/include
```

```
456.hmmer: /opt/intel/Compiler/11.0/042/bin/intel64/icc  
-L/opt/intel/Compiler/11.0/042/ipp/em64t/lib  
-I/opt/intel/Compiler/11.0/042/ipp/em64t/include
```

C++ benchmarks:

icpc

## Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32  
401.bzip2: -DSPEC_CPU_LP64  
456.hmmer: -DSPEC_CPU_LP64  
462.libquantum: -DSPEC_CPU_LINUX  
483.xalancbmk: -DSPEC_CPU_LINUX
```

## Peak Optimization Flags

C benchmarks:

Continued on next page



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## Peak Optimization Flags (Continued)

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -static -ansi-alias -opt-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -static -auto-ilp32 -opt-prefetch  
-ansi-alias

403.gcc: -xSSE4.1 -ipo -O3 -no-prec-div -static -inline-calloc  
-opt-malloc-options=3

429.mcf: -xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -O2 -ipo  
-no-prec-div -ansi-alias

456.hmmcr: -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2  
-ansi-alias -auto-ilp32

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -static -unroll4

462.libquantum: basepeak = yes

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -static -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -ansi-alias -opt-ra-region-strategy=block  
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine  
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090713.11.html>

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.03.html>



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You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090713.11.xml>

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.03.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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