



# SPEC® CINT2006 Result

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## Fujitsu Siemens Computers

**SPECint®2006 = 23.6**

CELSIUS M460, Intel Core 2 Quad Q9650 processor

**SPECint\_base2006 = 21.2**

CPU2006 license: 22

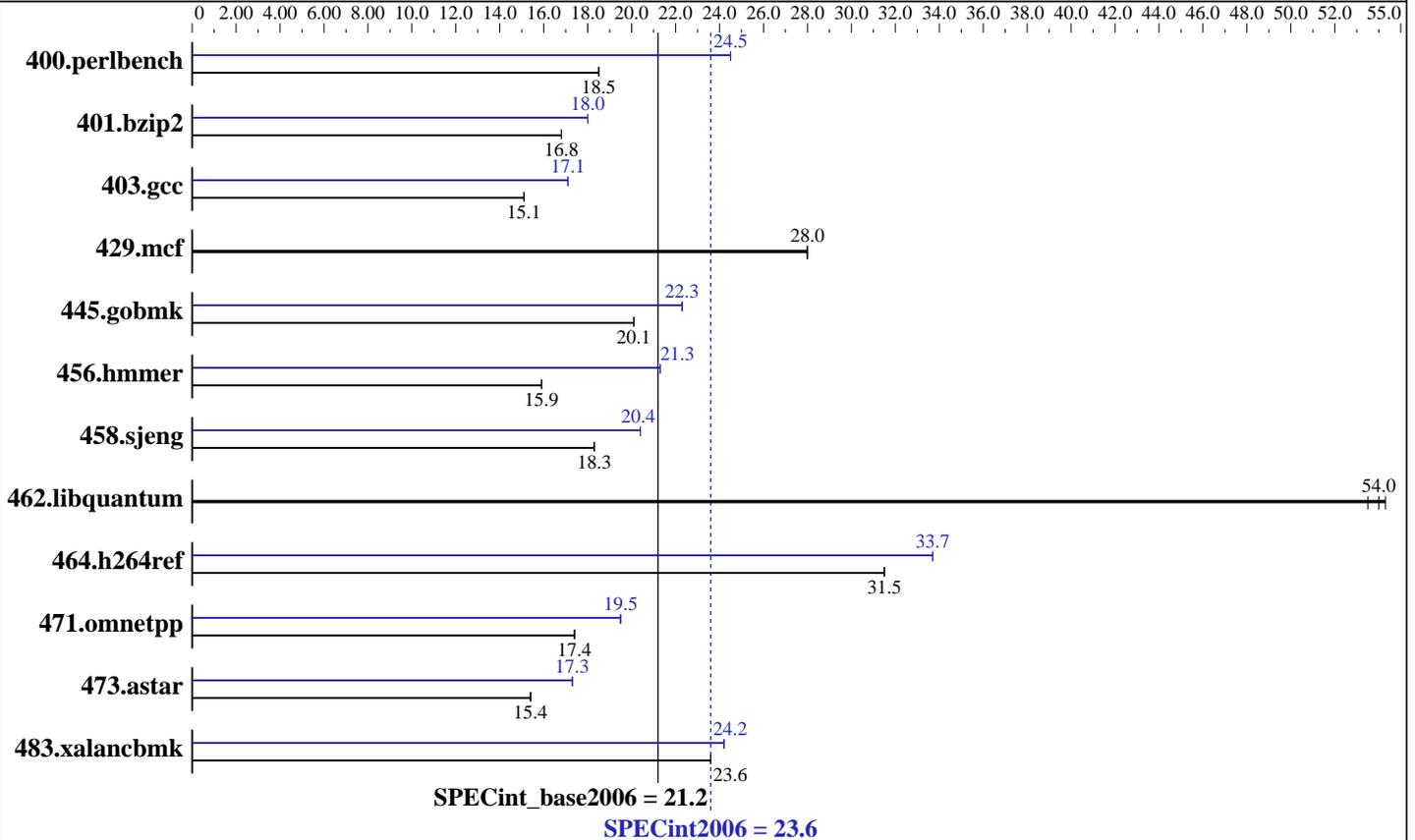
Test date: Oct-2008

Test sponsor: Fujitsu Siemens Computers

Hardware Availability: Sep-2008

Tested by: Fujitsu Siemens Computers

Software Availability: Feb-2008



### Hardware

CPU Name: Intel Core 2 Quad Q9650  
 CPU Characteristics:  
 CPU MHz: 3000  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores  
 L3 Cache: None  
 Other Cache: None  
 Memory: 4 GB (4x1 GB PC2-6400 CL6 SDRAM)  
 Disk Subsystem: 1 x 400 GB SATA 7200 RPM  
 Other Hardware: None

### Software

Operating System: Windows Vista Ultimate (x64)  
 Compiler: Intel C++ Compiler for applications running on IA-32, Version 10.1, Build 20080312  
 Intel C++ Compiler for applications running on Intel 64, Version 10.1, Build 20080312  
 Microsoft Visual Studio 2005 with SP1 (for libraries)  
 Auto Parallel: Yes  
 File System: NTFS  
 System State: Default  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: MicroQuill SmartHeap Library 9



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio										
400.perlbench	529	18.5	<b><u>528</u></b>	<b><u>18.5</u></b>	528	18.5	399	24.5	<b><u>399</u></b>	<b><u>24.5</u></b>	399	24.5
401.bzip2	574	16.8	<b><u>574</u></b>	<b><u>16.8</u></b>	574	16.8	535	18.0	<b><u>535</u></b>	<b><u>18.0</u></b>	535	18.0
403.gcc	<b><u>533</u></b>	<b><u>15.1</u></b>	534	15.1	532	15.1	470	17.1	470	17.1	<b><u>470</u></b>	<b><u>17.1</u></b>
429.mcf	326	28.0	325	28.0	<b><u>326</u></b>	<b><u>28.0</u></b>	326	28.0	325	28.0	<b><u>326</u></b>	<b><u>28.0</u></b>
445.gobmk	<b><u>523</u></b>	<b><u>20.1</u></b>	522	20.1	523	20.1	470	22.3	470	22.3	<b><u>470</u></b>	<b><u>22.3</u></b>
456.hammer	<b><u>588</u></b>	<b><u>15.9</u></b>	588	15.9	588	15.9	439	21.3	<b><u>439</u></b>	<b><u>21.3</u></b>	439	21.3
458.sjeng	660	18.3	<b><u>660</u></b>	<b><u>18.3</u></b>	660	18.3	594	20.4	594	20.4	<b><u>594</u></b>	<b><u>20.4</u></b>
462.libquantum	381	54.3	387	53.5	<b><u>384</u></b>	<b><u>54.0</u></b>	381	54.3	387	53.5	<b><u>384</u></b>	<b><u>54.0</u></b>
464.h264ref	703	31.5	702	31.5	<b><u>702</u></b>	<b><u>31.5</u></b>	657	33.7	656	33.7	<b><u>656</u></b>	<b><u>33.7</u></b>
471.omnetpp	<b><u>360</u></b>	<b><u>17.4</u></b>	360	17.4	360	17.4	<b><u>321</u></b>	<b><u>19.5</u></b>	321	19.5	321	19.5
473.astar	<b><u>456</u></b>	<b><u>15.4</u></b>	456	15.4	455	15.4	<b><u>407</u></b>	<b><u>17.3</u></b>	407	17.3	407	17.3
483.xalancbmk	292	23.6	293	23.6	<b><u>292</u></b>	<b><u>23.6</u></b>	285	24.2	<b><u>285</u></b>	<b><u>24.2</u></b>	285	24.2

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Screen Saver was disabled  
Power scheme was set to Always On, modified to Turnoff monitor: Never.

## Platform Notes

BIOS default settings have been used.

## General Notes

For information about Fujitsu Siemens Computers please see:  
<http://www.fujitsu-siemens.com>

## Base Compiler Invocation

C benchmarks:  
icl -Qvc8 -Qc99

C++ benchmarks:  
icl -Qvc8



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## Base Portability Flags

```
403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword
```

## Base Optimization Flags

C benchmarks:

```
-fast -Qparallel -Qvec-guard-write -Qpar-runtime-control -F512000000
libguide40.lib
```

C++ benchmarks:

```
-fast -Qcxx-features -F512000000 libguide40.lib shlw32M.lib
-link -FORCE:MULTIPLE
```

## Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

## Peak Compiler Invocation

C benchmarks (except as noted below):

```
icl -Qvc8 -Qc99
```

```
401.bzip2: C:\DevelTools\Intel\Compiler\C++\10.1.021\EM64T\Bin\icl.exe
```

```
-IC:\DevelTools\Intel\Compiler\C++\10.1.021\EM64T\Include
```

```
-link -LIBPATH:C:\DevelTools\Intel\Compiler\C++\10.1.021\EM64T\Lib
```

```
-link -LIBPATH:C:\ProgramFiles\MicrosoftVisualStudio8\VC\lib\AMD64
```

```
-link -LIBPATH:C:\ProgramFiles\MicrosoftVisualStudio8\VC\PlatformSDK\lib\AMD64
```

```
-link -LIBPATH:C:\ProgramFiles\MicrosoftVisualStudio8\VC\lib
```

```
456.hmmer: C:\DevelTools\Intel\Compiler\C++\10.1.021\EM64T\Bin\icl.exe
```

```
-IC:\DevelTools\Intel\Compiler\C++\10.1.021\EM64T\Include
```

```
-link -LIBPATH:C:\DevelTools\Intel\Compiler\C++\10.1.021\EM64T\Lib
```

```
-link -LIBPATH:C:\ProgramFiles\MicrosoftVisualStudio8\VC\lib\AMD64
```

```
-link -LIBPATH:C:\ProgramFiles\MicrosoftVisualStudio8\VC\PlatformSDK\lib\AMD64
```

```
-link -LIBPATH:C:\ProgramFiles\MicrosoftVisualStudio8\VC\lib
```

C++ benchmarks:

```
icl -Qvc8
```



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## Peak Portability Flags

401.bzip2: -DSPEC\_CPU\_P64  
 403.gcc: -DSPEC\_CPU\_WIN32  
 456.hmmr: -DSPEC\_CPU\_P64  
 464.h264ref: -DSPEC\_CPU\_NO\_INTTYPES -DWIN32  
 483.xalancbmk: -Qoption, cpp, --no\_wchar\_t\_keyword

## Peak Optimization Flags

C benchmarks:

400.perlbench: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast -Qparallel  
 -Qpar-runtime-control -Qansi-alias -Qprefetch -F512000000  
 libguide40.lib shlW32M.lib -link -FORCE:MULTIPLE

401.bzip2: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast -Qprefetch  
 -F512000000 libguide40.lib

403.gcc: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast -F512000000  
 libguide40.lib

429.mcf: basepeak = yes

445.gobmk: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -O2 -Qipo -QxT  
 -Qprec-div- -Qansi-alias -F512000000

456.hmmr: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast -Qunroll2  
 -Qansi-alias -Qopt-multi-version-aggressive -F512000000  
 libguide40.lib

458.sjeng: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast -Qunroll4  
 -F512000000 libguide40.lib

462.libquantum: basepeak = yes

464.h264ref: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast -Qunroll2  
 -Qansi-alias -F512000000 libguide40.lib

C++ benchmarks:

471.omnetpp: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast -Qcxx-features  
 -Qansi-alias -Qopt-ra-region-strategy=block -F512000000  
 libguide40.lib shlW32M.lib -link -FORCE:MULTIPLE

473.astar: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast -Qcxx-features  
 -Qansi-alias -Qopt-ra-region-strategy=routine -F512000000  
 libguide40.lib shlW32M.lib -link -FORCE:MULTIPLE

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## Peak Optimization Flags (Continued)

```
483.xalanbmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qcxx-features
              -Qansi-alias -F512000000 libguide40.lib shlw32M.lib
              -link -FORCE:MULTIPLE
```

## Peak Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

The flags file that was used to format this result can be browsed at

[http://www.spec.org/cpu2006/flags/CPU2006\\_flags.20090713.html](http://www.spec.org/cpu2006/flags/CPU2006_flags.20090713.html)

You can also download the XML flags source by saving the following link:

[http://www.spec.org/cpu2006/flags/CPU2006\\_flags.20090713.xml](http://www.spec.org/cpu2006/flags/CPU2006_flags.20090713.xml)

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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