SPEC® CINT2006 Result

Fujitsu Siemens Computers

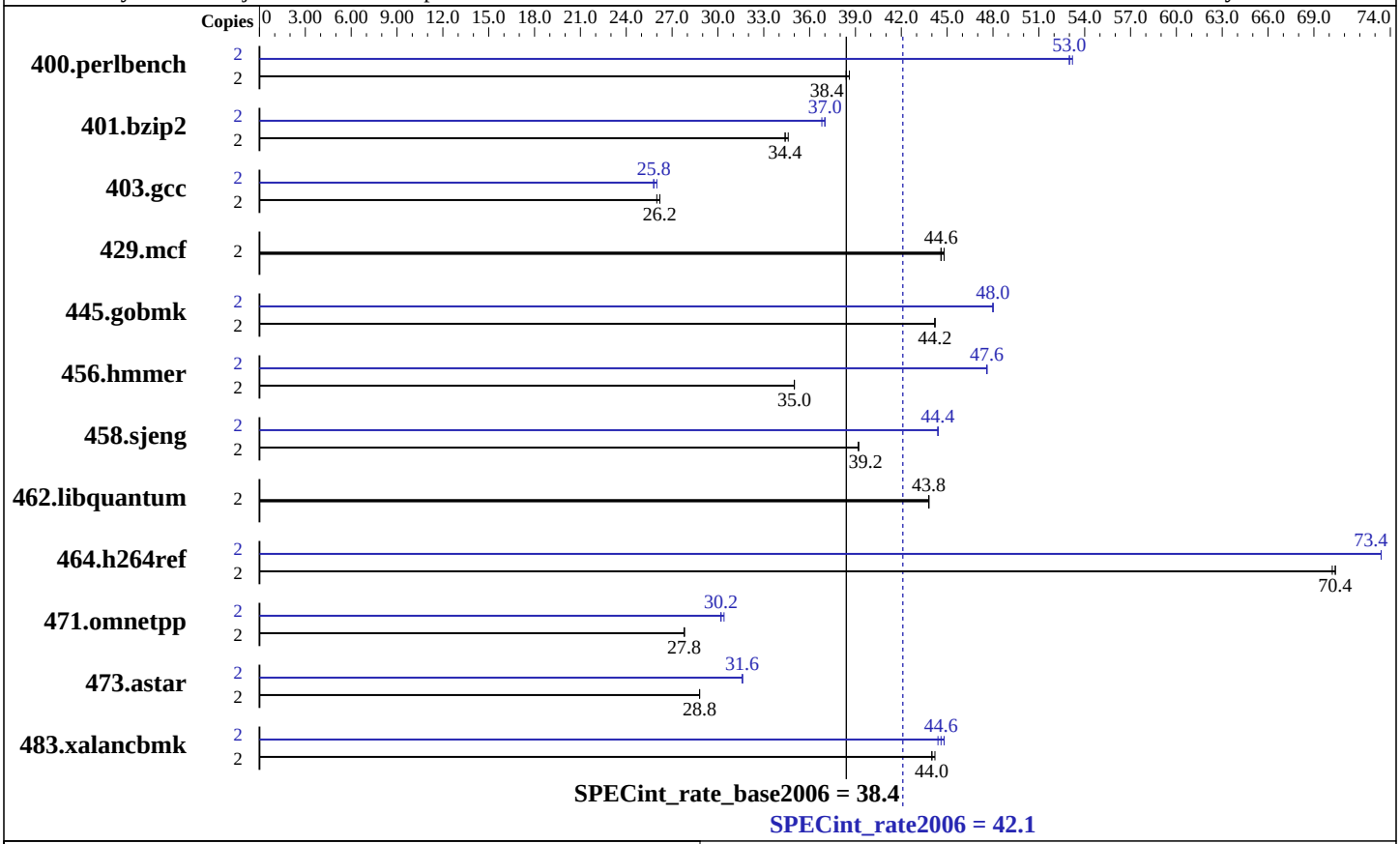
CELSIUS M460, Intel Core 2 Duo E8600 processor

SPECint®_rate2006 = 42.1

SPECint_rate_base2006 = 38.4

CPU2006 license: 22
Test sponsor: Fujitsu Siemens Computers
Tested by: Fujitsu Siemens Computers

Test date: Oct-2008
Hardware Availability: Sep-2008
Software Availability: Feb-2008



Hardware

CPU Name:	Intel Core 2 Duo E8600
CPU Characteristics:	
CPU MHz:	3333
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	6 MB I+D on chip per chip
L3 Cache:	None
Other Cache:	None
Memory:	4 GB (4x1 GB PC2-6400 CL6 SDRAM)
Disk Subsystem:	1 x 400 GB SATA 7200 RPM
Other Hardware:	None

Software

Operating System:	Windows Vista Ultimate (x64)
Compiler:	Intel C++ Compiler for applications running on IA-32, Version 10.1, Build 20080212 Intel C++ Compiler for applications running on Intel 64, Version 10.1, Build 20080212 Microsoft Visual Studio 2005 with SP1 (for libraries)
Auto Parallel:	No
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	SmartHeap Library Version 9 from http://www.microquill.com/

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	**509**	**38.4**	510	38.4	505	38.6	2	367	53.2	369	53.0	**369**	**53.0**
401.bzip2	2	561	34.4	**561**	**34.4**	559	34.6	2	521	37.0	**523**	**37.0**	524	36.8
403.gcc	2	619	26.0	615	26.2	**617**	**26.2**	2	**622**	**25.8**	620	26.0	623	25.8
429.mcf	2	**410**	**44.6**	407	44.8	410	44.6	2	**410**	**44.6**	407	44.8	410	44.6
445.gobmk	2	**475**	**44.2**	475	44.2	475	44.2	2	**437**	**48.0**	437	48.0	436	48.0
456.hmmer	2	533	35.0	**533**	**35.0**	533	35.0	2	392	47.6	**392**	**47.6**	392	47.6
458.sjeng	2	616	39.2	**616**	**39.2**	616	39.2	2	545	44.4	**546**	**44.4**	546	44.4
462.libquantum	2	946	43.8	947	43.8	**947**	**43.8**	2	946	43.8	947	43.8	**947**	**43.8**
464.h264ref	2	630	70.2	629	70.4	**629**	**70.4**	2	603	73.4	**603**	**73.4**	603	73.4
471.omnetpp	2	449	27.8	450	27.8	**449**	**27.8**	2	**413**	**30.2**	413	30.2	413	30.4
473.astar	2	487	28.8	487	28.8	**487**	**28.8**	2	445	31.6	445	31.6	**445**	**31.6**
483.xalancbmk	2	312	44.2	**314**	**44.0**	314	44.0	2	309	44.8	311	44.4	**310**	**44.6**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
Screen Saver was disabled
Power scheme was set to Always On, modified to Turnoff monitor: Never.
```

Platform Notes

```
BIOS default settings have been used.
```

General Notes

```
For information about Fujitsu Siemens Computers please see:
http://www.fujitsu-siemens.com
```

Base Compiler Invocation

C benchmarks:

```
icl -Qvc8 -Qc99
```

C++ benchmarks:

```
icl -Qvc8
```

Base Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`
483.xalancbmk: `-Qoption,cpp,--no_wchar_t_keyword`

Base Optimization Flags

C benchmarks:
`-fast -Qvec-guard-write -F512000000`

C++ benchmarks:
`-fast -Qcxx_features -F512000000 shlW32M.lib -link -FORCE:MULTIPLE`

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
`icl -Qvc8 -Qc99`

401.bzip2: `C:\DevelTools\Intel\Compiler\C++\10.1.021\EM64T\Bin\icl.exe`
`-IC:\DevelTools\Intel\Compiler\C++\10.1.021\EM64T\Include`
`-link -LIBPATH:C:\DevelTools\Intel\Compiler\C++\10.1.021\EM64T\Lib`
`-link -LIBPATH:C:\ProgramFiles\MicrosoftVisualStudio8\VC\lib\AMD64`
`-link -LIBPATH:C:\ProgramFiles\MicrosoftVisualStudio8\VC\PlatformSDK\lib\AMD64`
`-link -LIBPATH:C:\ProgramFiles\MicrosoftVisualStudio8\VC\lib`

456.hmmer: `C:\DevelTools\Intel\Compiler\C++\10.1.021\EM64T\Bin\icl.exe`
`-IC:\DevelTools\Intel\Compiler\C++\10.1.021\EM64T\Include`
`-link -LIBPATH:C:\DevelTools\Intel\Compiler\C++\10.1.021\EM64T\Lib`
`-link -LIBPATH:C:\ProgramFiles\MicrosoftVisualStudio8\VC\lib\AMD64`
`-link -LIBPATH:C:\ProgramFiles\MicrosoftVisualStudio8\VC\PlatformSDK\lib\AMD64`
`-link -LIBPATH:C:\ProgramFiles\MicrosoftVisualStudio8\VC\lib`

C++ benchmarks:
`icl -Qvc8`

Peak Portability Flags

401.bzip2: -DSPEC_CPU_P64
403.gcc: -DSPEC_CPU_WIN32
456.hmmer: -DSPEC_CPU_P64
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword

Peak Optimization Flags

C benchmarks:

400.perlbench: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
-Qprefetch -F512000000 shlW32M.lib -link -FORCE:MULTIPLE

401.bzip2: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qprefetch
-F512000000

403.gcc: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -F512000000

429.mcf: basepeak = yes

445.gobmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxT -O2 -Qipo
-Qprec-div- -Qansi-alias -F512000000

456.hmmer: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2
-Qansi-alias -Qopt-multi-version-aggressive -F512000000

458.sjeng: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll4
-F512000000

462.libquantum: basepeak = yes

464.h264ref: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2
-Qansi-alias -F512000000

C++ benchmarks:

471.omnetpp: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
-Qopt-ra-region-strategy=block -Qcxx_features -F512000000
shlW32M.lib -link -FORCE:MULTIPLE

473.astar: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
-Qopt-ra-region-strategy=routine -Qcxx_features -F512000000
shlW32M.lib -link -FORCE:MULTIPLE

483.xalancbmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
-Qcxx_features -F512000000 shlW32M.lib
-link -FORCE:MULTIPLE

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/CPU2006_flags.20090713.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/CPU2006_flags.20090713.xml`

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
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